BenQ

Applicable Country & Regions:
Taiwan/Europe

Product Service Manual – Level 3

Service Manual for BenQ:
Monitor-TV/ML2441
<9H.V0S75.JCE>

Version: 00a
Date:2010/03/18

Notice:
For RO to input specific "Legal Requirement" in specific NS regarding to responsibility and liability statements.
Please check BenQ's eSupport web site, http://esupport.benq.com, to ensure that you have the most recent version of this manual.

Content Index

Level 3 - Component Repair to Circuit Boards

Theory of Circuit Operation

Video:

The MSD237HFG is a highly integrated controller IC for LCD/PDP DTV applications with resolutions up to full-HD(1920x1080).It is configured with an integrated triple-ADC/PLL, a multi-standard TV video and audio decoder ,a motion adaptive video de-interlacer, a scaling engine, the MstarACE-3 color engine ,an advanced 2D graphics engine,a transport processor, a high-definition(HD)H.264 video decoder ,a RearlMedia decoder,a JEPG video decoder,an MPEG-4 decoder ,and a 24-bit DSP for MPEG audio decoding,a DVI/HDCP/HDMI receiver , and a peripheral control unit providing a vriety of HDTV control funtions.

For digital TV application,the MSD237HFG comprises an MPEG-2 transport processor with advanced section filtering capability, an MPEG-2 (MP@ML profile) video decoder, an MPEG-4 decoder, an H.264 video decoder and an audio DSP decoder for MPEG audio streams,MPEG layer I and II digital audio decoder with analog audio outputs that are designed to support exiting and future DVB-T programs while handling conditional access.

For analog TV, the MSD237HFG includes NTSC/PAL/SECAM multi-standard video decoder comprising a 3-D motion adaptive comb filter and time-based correction,and a NICAM/A2 audio decoder to support worldwide television standards.

Audio:

The PAM8603E is STEREO CLASS-D AUDIO POWER AMPLIFIER IC with output of 2 x 2.5w to 4hm load with 5V supply voltage. PAM8603E features Power Limit Function that were eveloped by PAM original digital amplifier technology. PAM8603E has overcurrent protection function for speaker output terminals, high temperature protection function, and lowsupply voltage malfunction prevention function.

TUNER FEATURES:

INTERMEDIATE FREQUENCIES

SYSTEM	FREQUENCY (MHz) [1]				
	B/G	I	D/K	L	L'
Picture carrier	38.90	38.90	38.90	38.90	33.9
Color	34.47	34.47	34.47	34.47	38.33
Sound 1	33.40	32.90	32.40	32.40	40.40
Digital IF Center Frequency (Bandwidth: 7MHz / 8MHz)	36.1667				

CHANNEL COVERAGE

BAND	FREQUENCY(MHz)
Low band	45.25 to 147.00 MHz
Mid band	147.25 to 431.00MHz
High band	431.25 to 863.25 MHz

PINNING

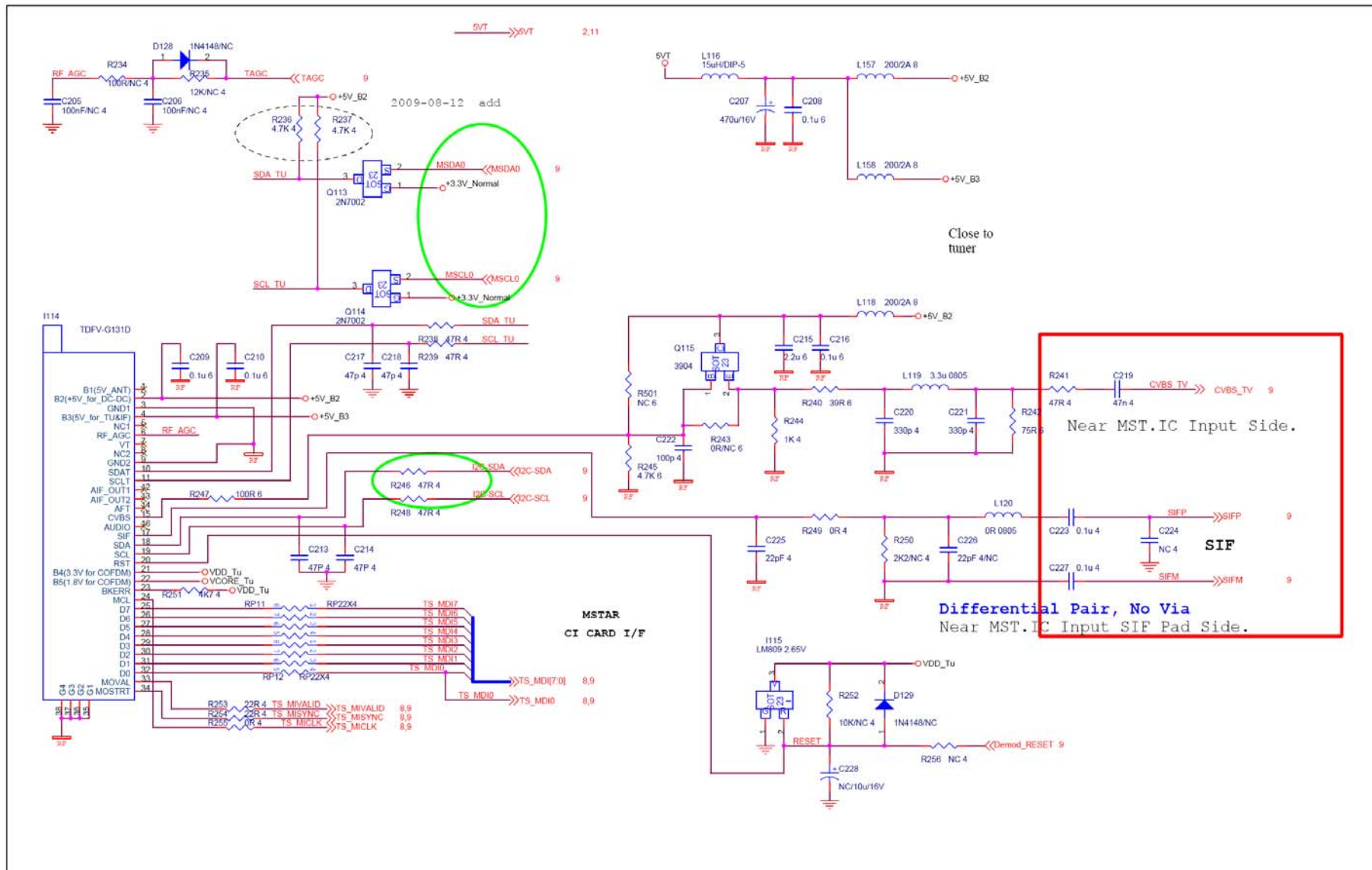| SYMBOL	PIN	DESCRIPTION
ANT(5V)	1	Supply voltage +5V , for ANT
BB(CTR)	2	Supply voltage +5V , for Booster
GND	3	Ground
+B(5V)	4	Supply voltage +5 V , for Tuner & IF Section
N.C	5	Not connected
RF AGC	6	RF AGC output
VT	7	Tuning Voltage, DO NOT CONNECT IT to ANYWHERE
N.C	8	Not connected
GND	9	Ground
DATA	10	I2C Bus for TUNER PLL, Analog IC. Use it for I2C Bus
CLOCK	11	I2C Bus for TUNER PLL, Analog IC. Use it for I2C Bus
AIF	12	TU IF Output. DO NOT CONNECT IT to ANYWHERE
AIF	13	TU IF Output. DO NOT CONNECT IT to ANYWHERE
N.C	14	AFT (Auto Fine Tuning) Output, DO NOT CONNECT IT to
Video	15	Analog Video Output
Audio	16	Analog Audio Output
SIF	17	Second IF Sound Output
SDA	18	I2C Bus for COFDM IC, Use it for I2C Bus interface
SCL	19	I2C Bus for COFDM IC, Use it for I2C Bus interface
RST	20	COFDM IC Reset . Active at low level
3.3V	21	3.3V Supply for COFDM IC
1.8V	22	1.8V Supply for COFDM IC
ERR	23	Output Error Signal. In case of uncorrectable
MCL	24	Output Byte Clock signal(=MCLK)
D7	25	Output D7 signal (=MDO7)
D6	26	Output D6 signal
D5	27	Output D5 signal
D4	28	Output D4 signal
D3	29	Output D3 signal
D2	30	Output D2 signal
D1	31	Output D1 signal
D0	32	Output D0 signal
VAL	33	Output Data Valid signal (=MOVAL)
SYNC	34	Output Packet sync signal (=MOSTRT)

Circuit Schematics
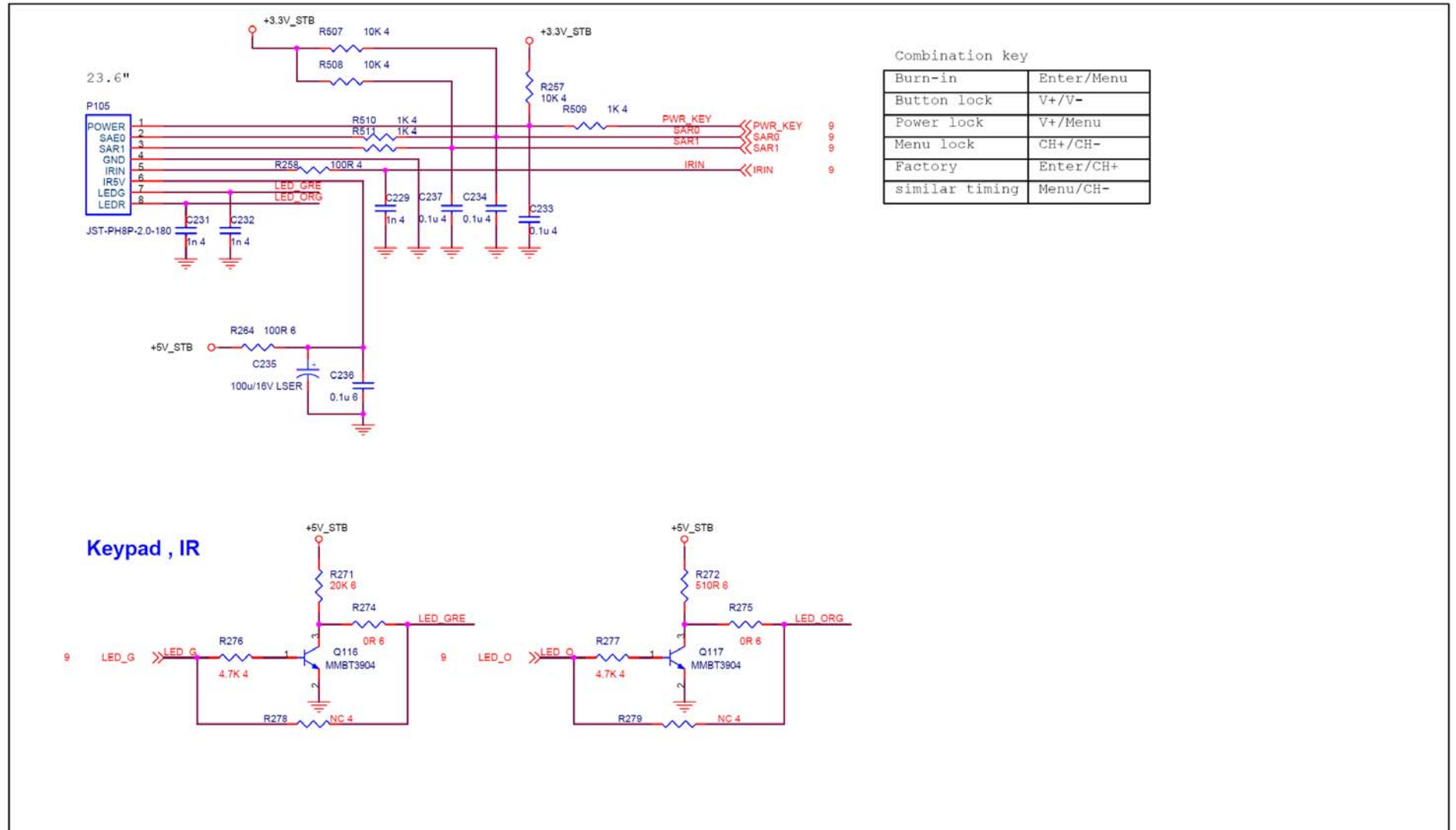## DC/DC POWER

SCART Interface

SCART Audio OUT
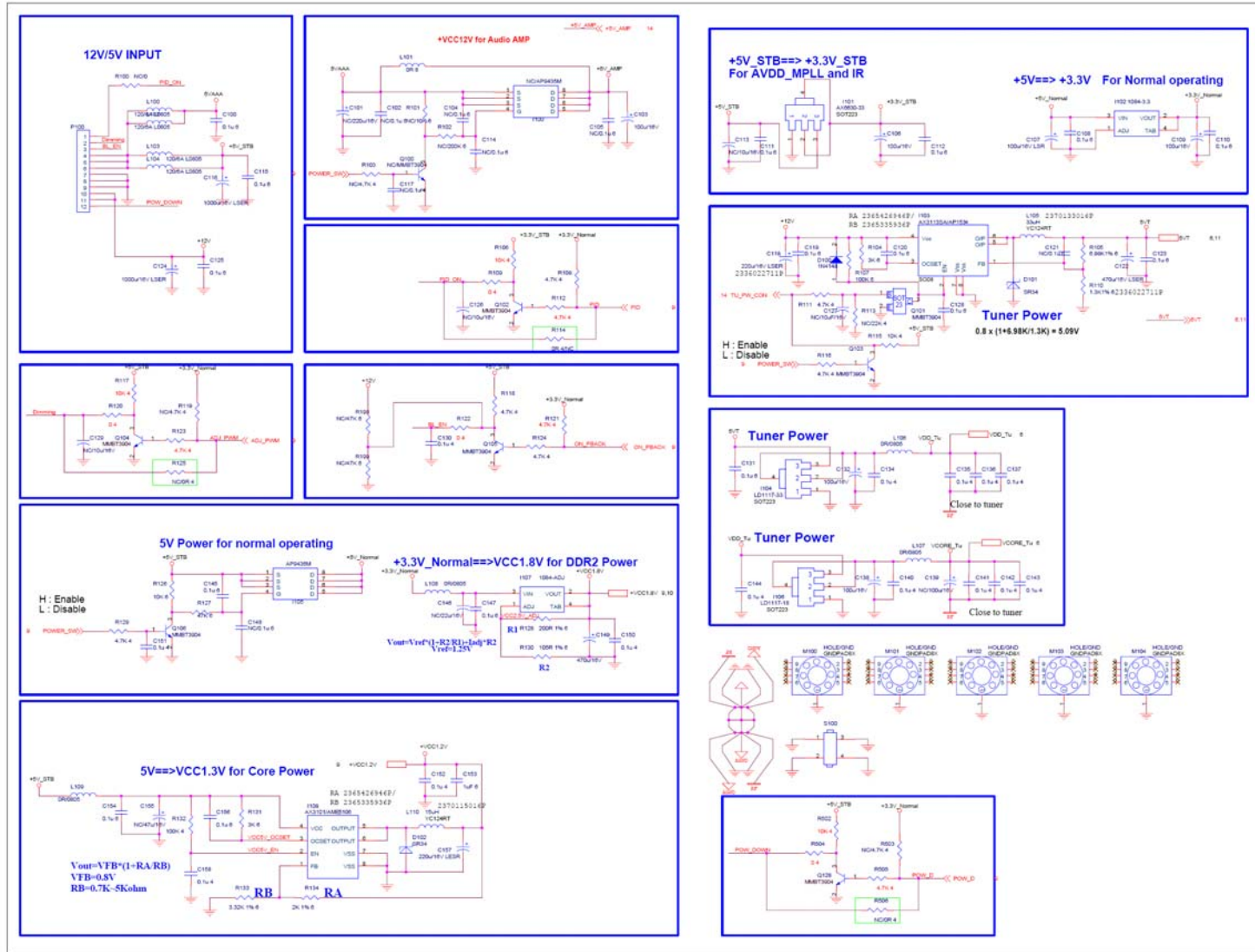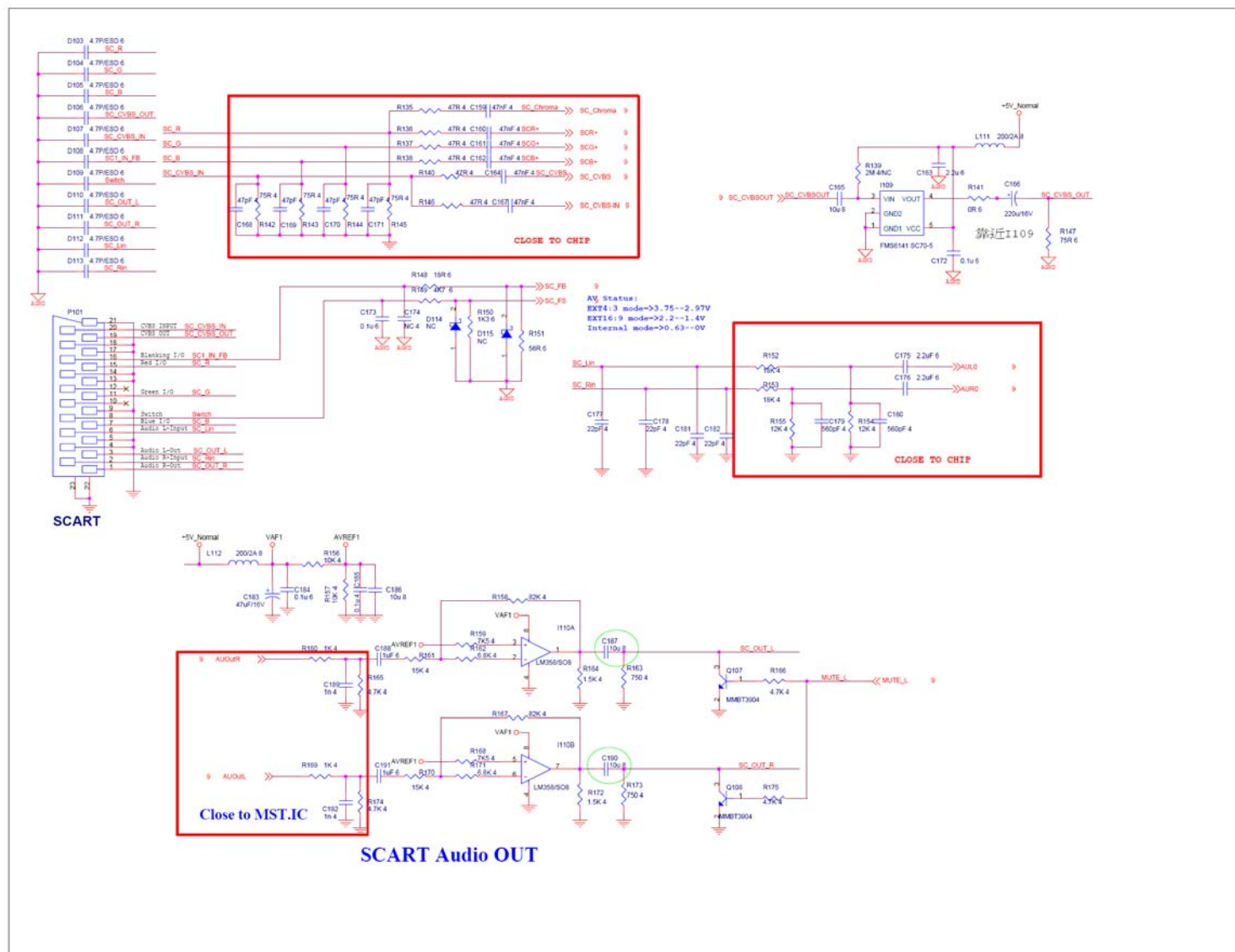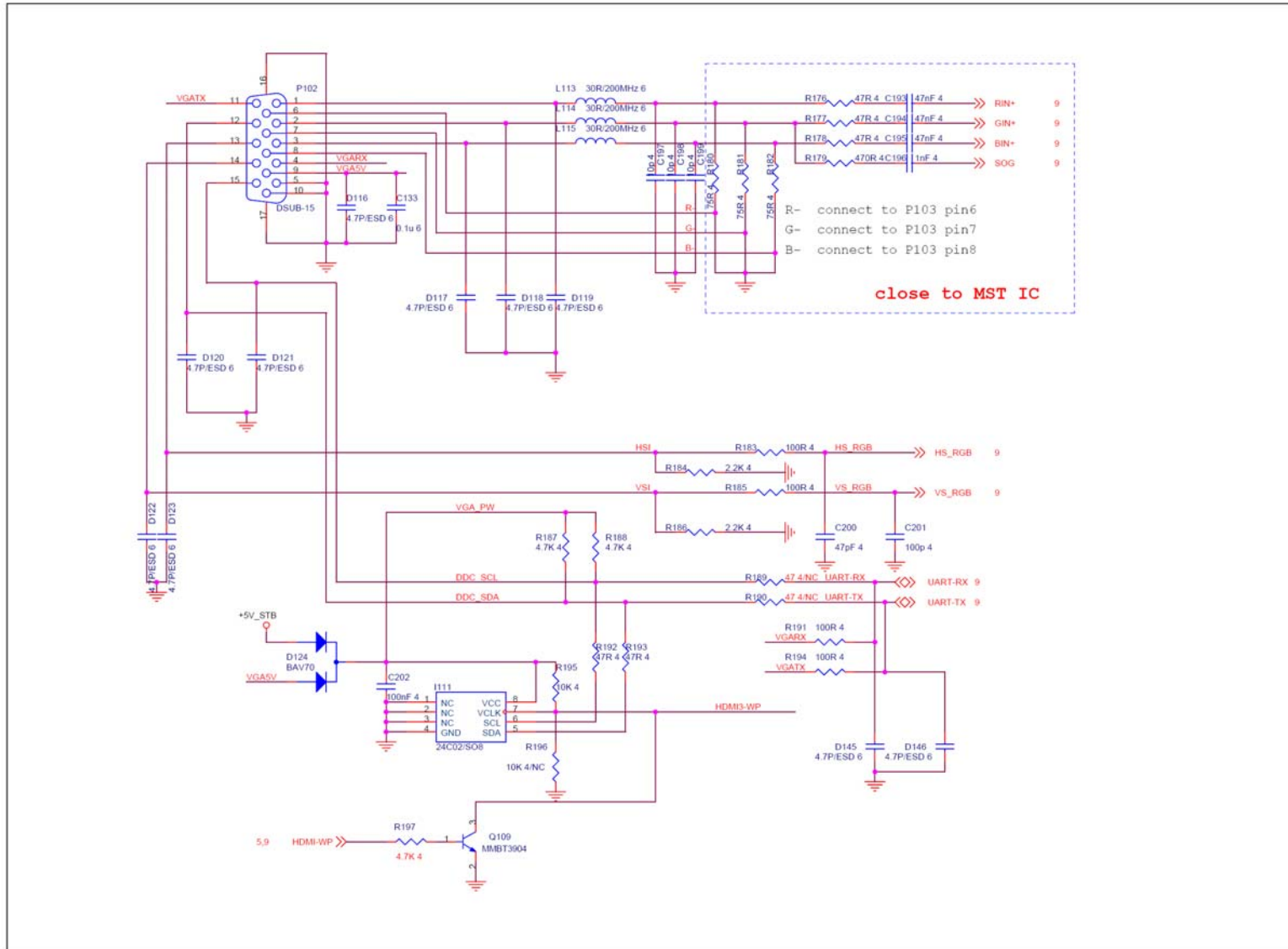
VGA Interface

HDMI Interface

TUNER

KEYPAD & IR

Combination key	
Burn-in	Enter/Menu
Button lock	V+/V-
Power lock	V+/Menu
Menu lock	CH+/CH-
Factory	Enter/CH+
similar timing	Menu/CH-
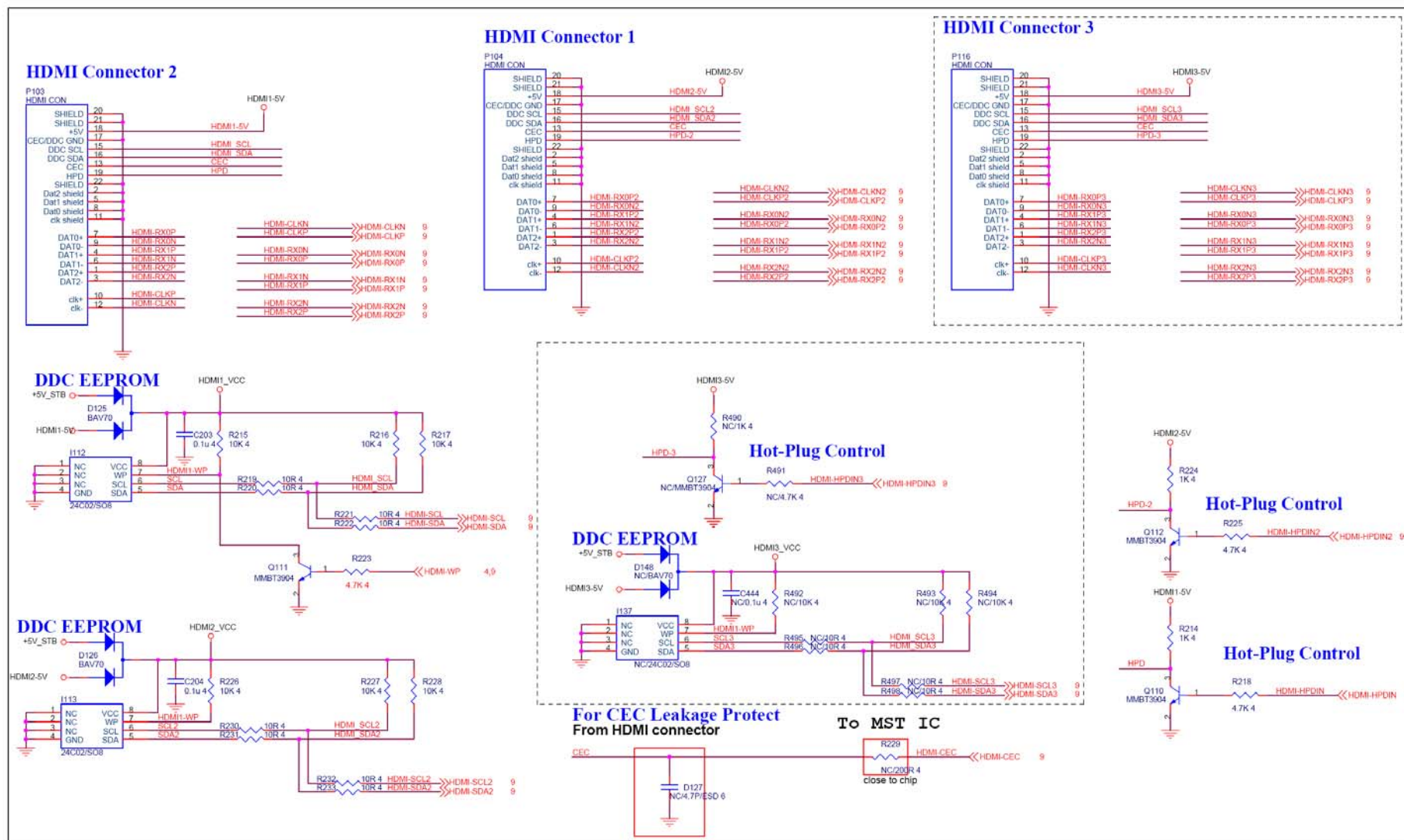
PCMCIA Interface

Scaler & Flash

DDR(32 Bits)

QIMONDA :DDR2/HYB18TC512160B2F-2.5
ELPIDA: EDE5116AJBG-8E-E
SAMSUNG: K4T1G164QQ-HC(L)F7
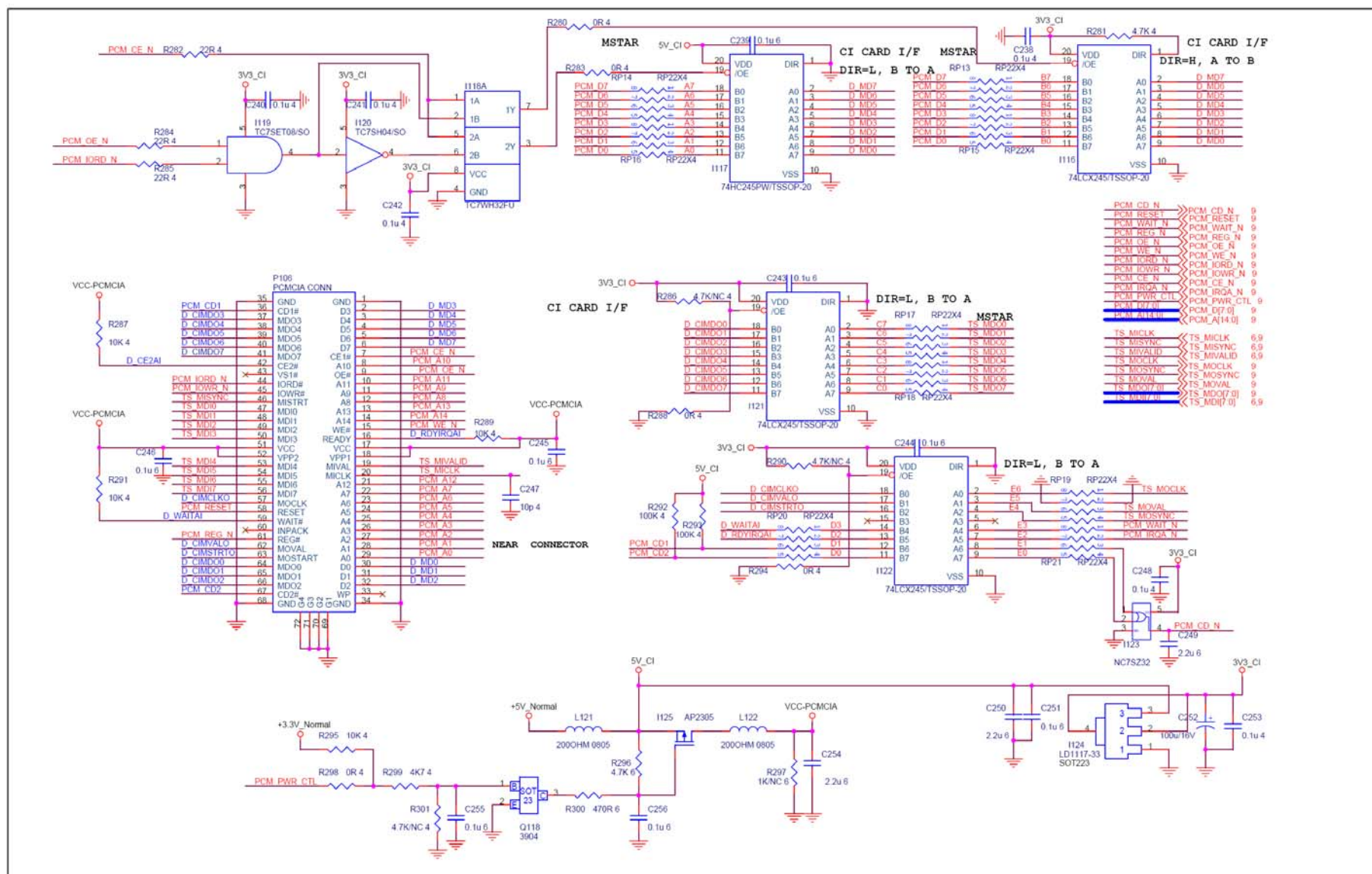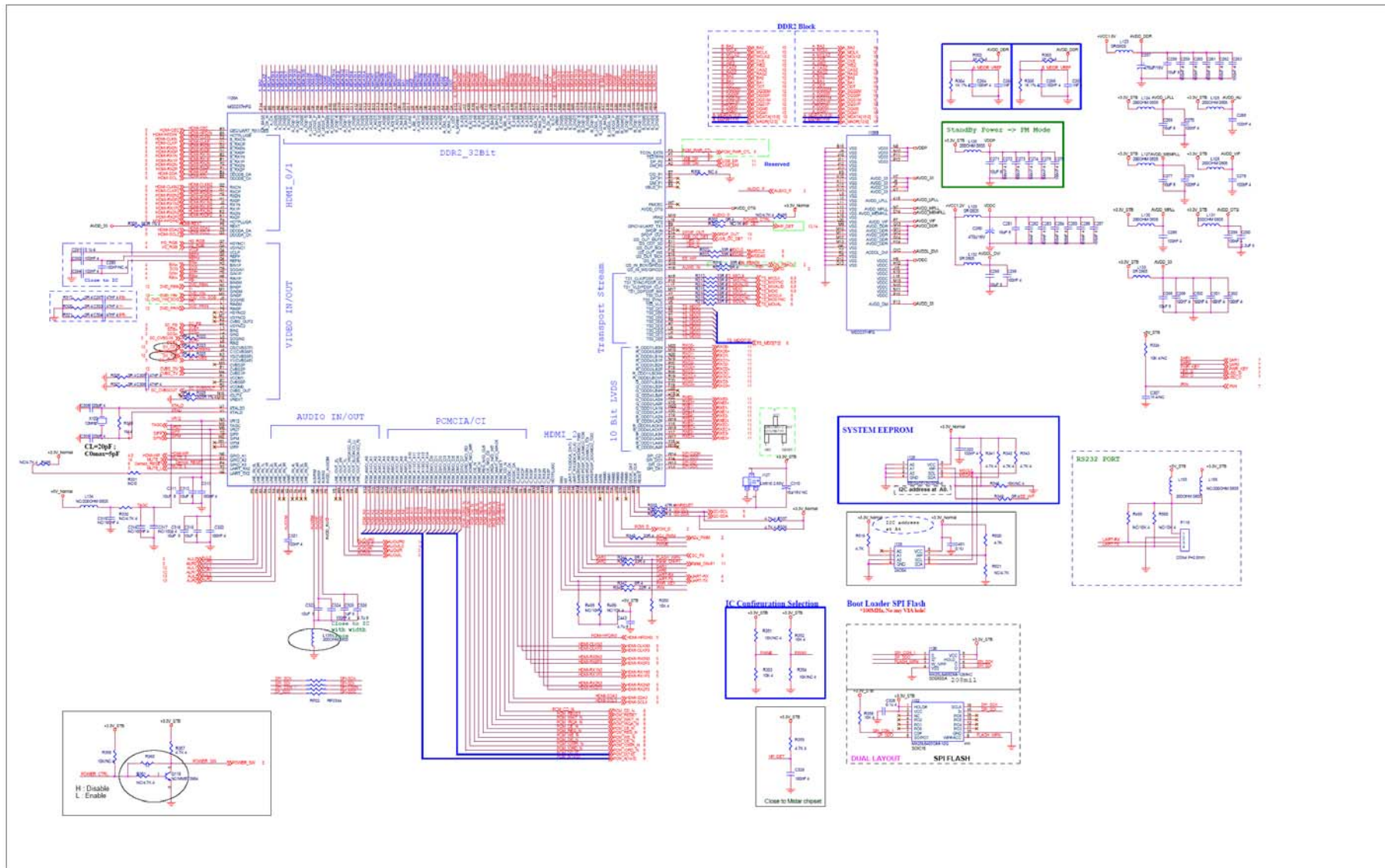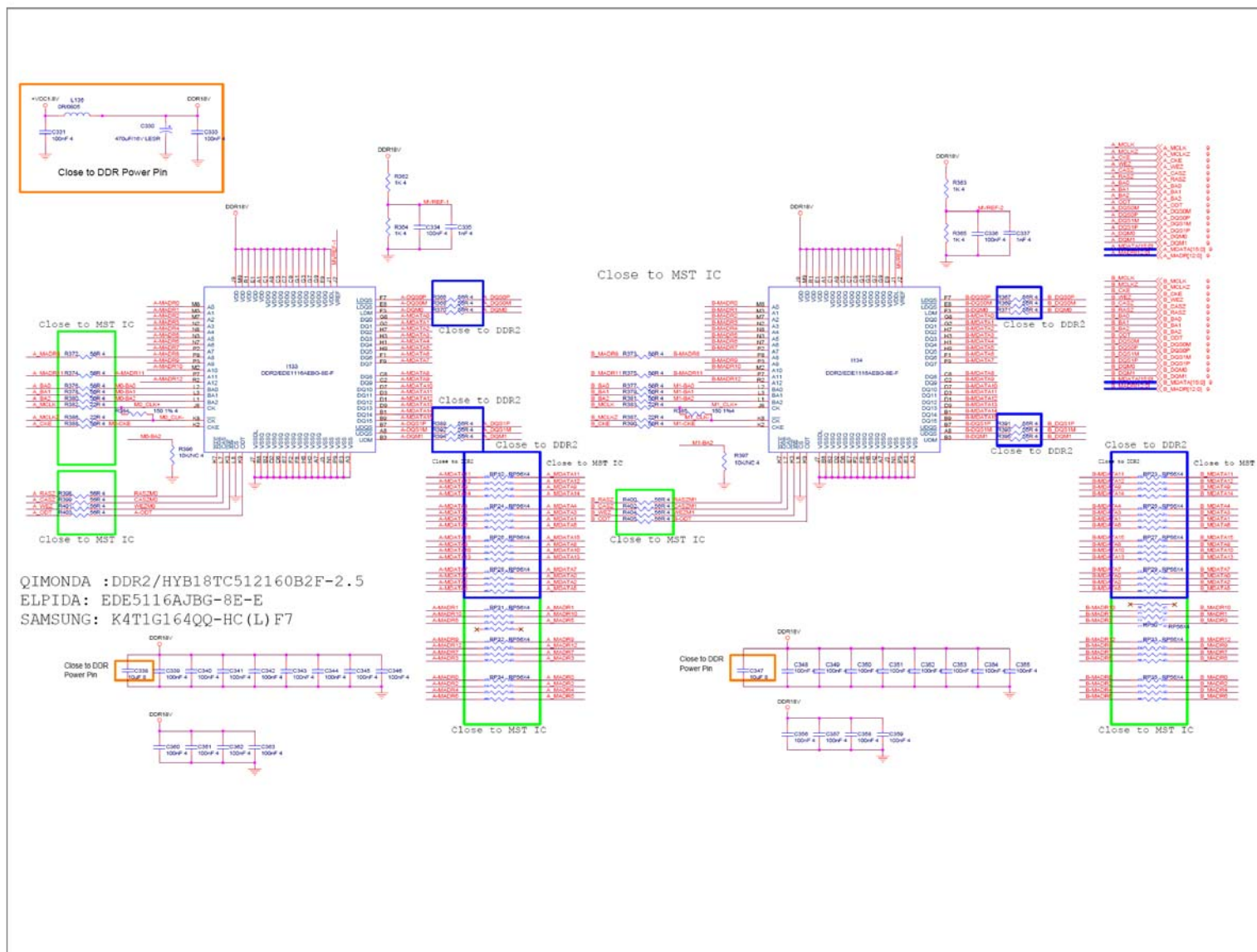
Panel & USB Interface
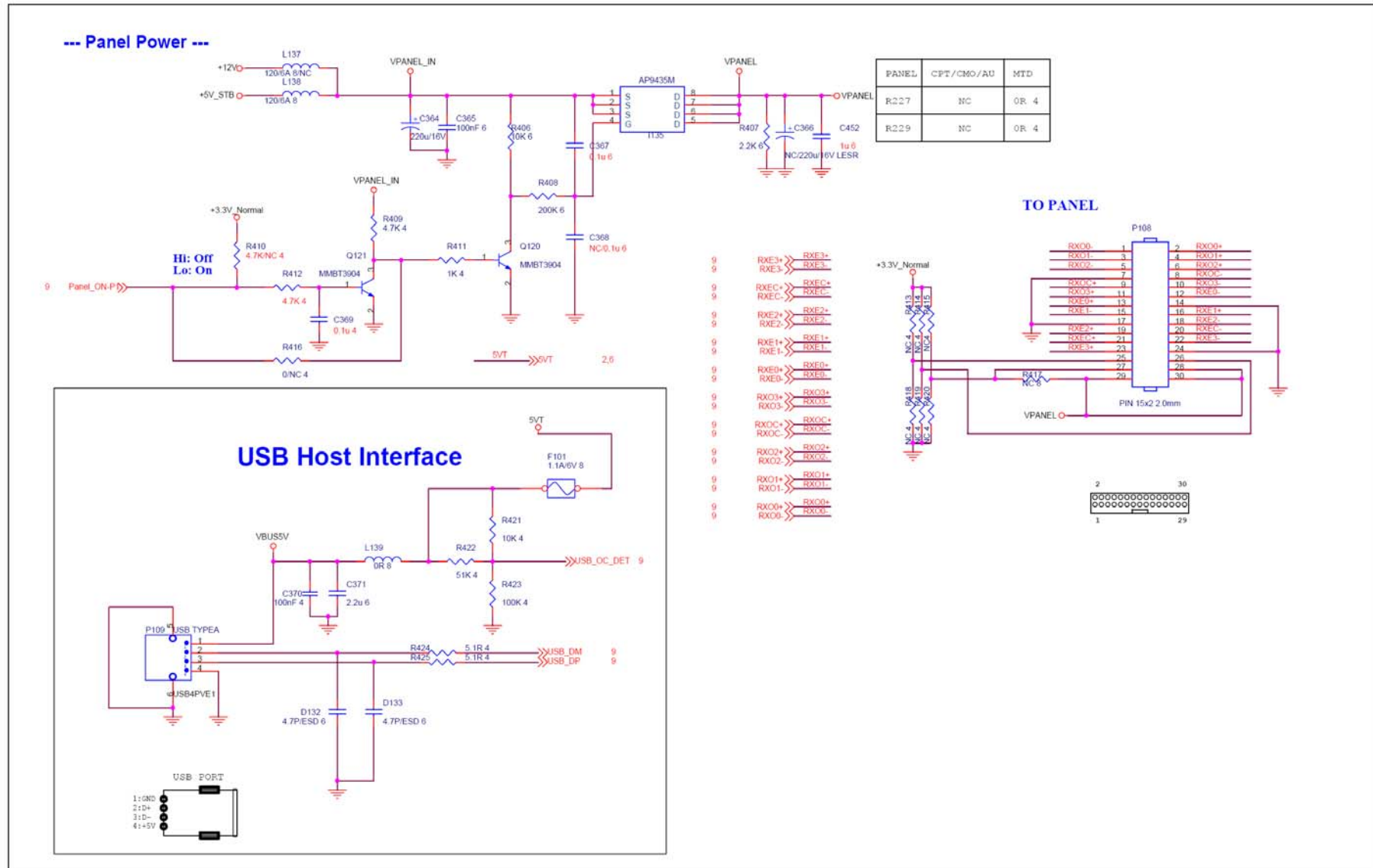
Video Interface
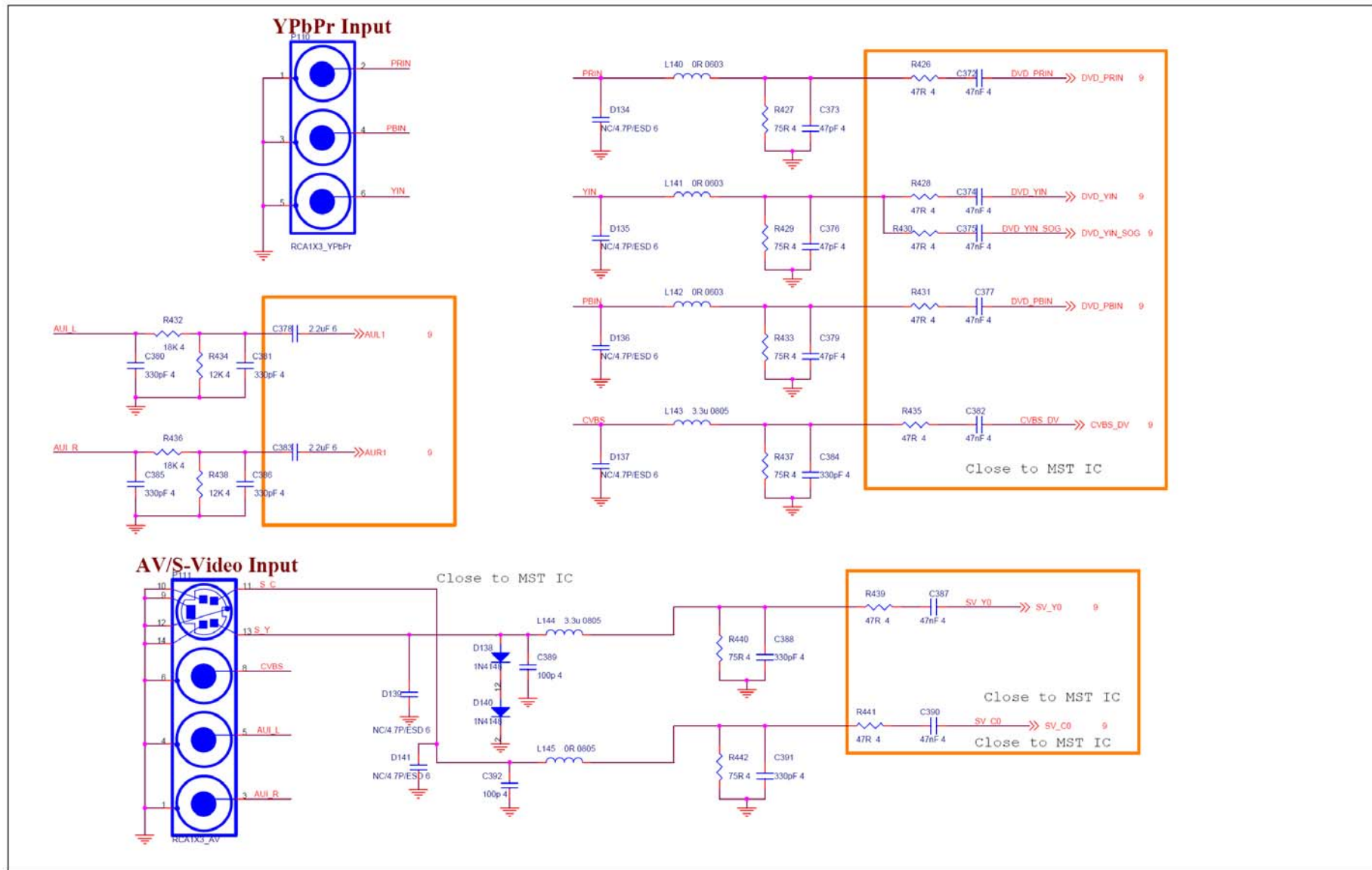
Audio Interface
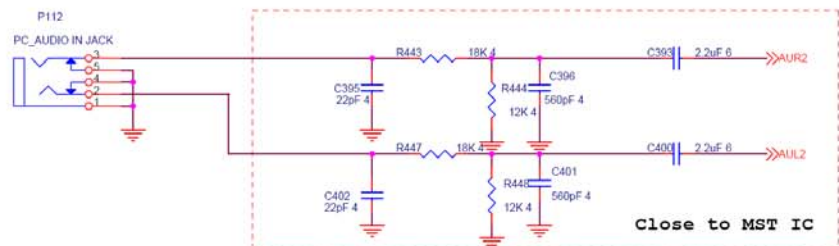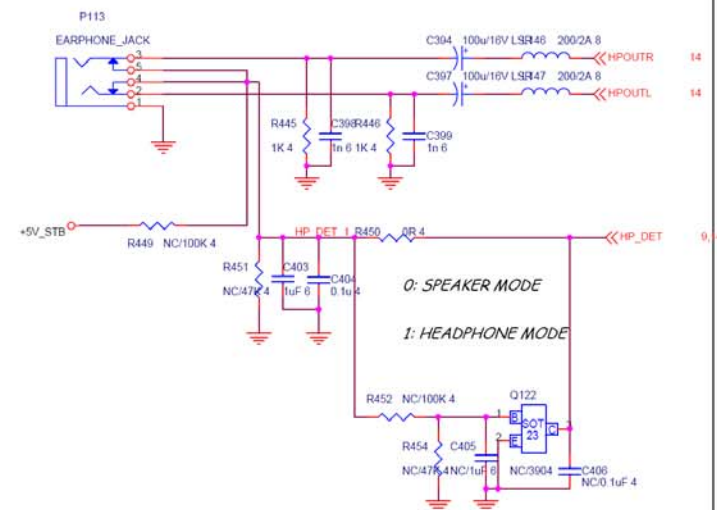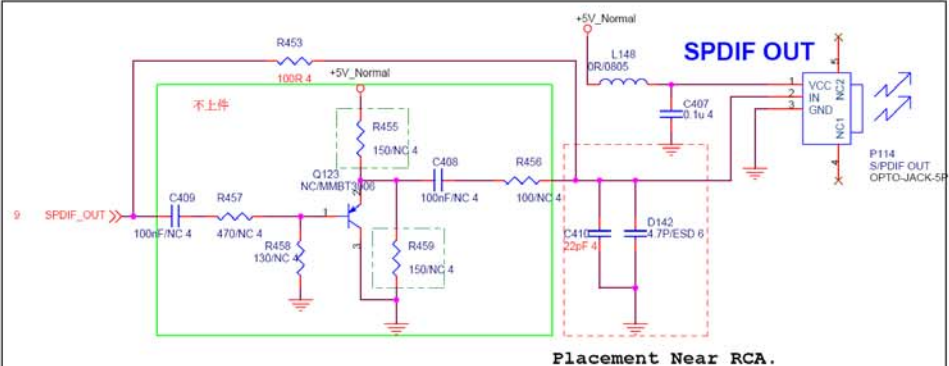
Audio AMP
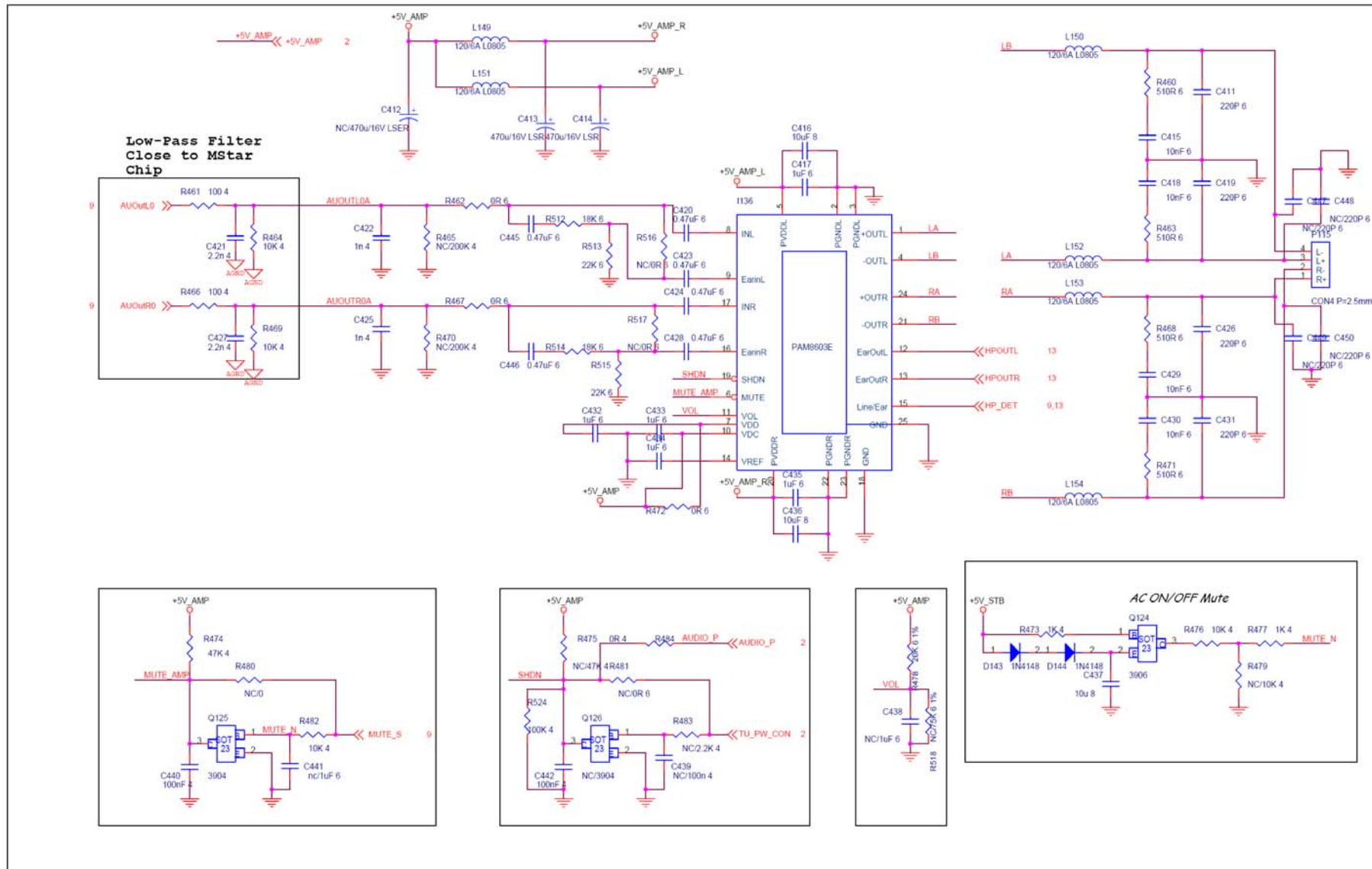
IR LED PAD

KEYPAD

LED

Power

JE240HW61ED-300

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RS232

pin1 To mainboard +5VSB or +3.3VSB
pin2 To GND
pin3 To U0RX
pin4 To U0TX
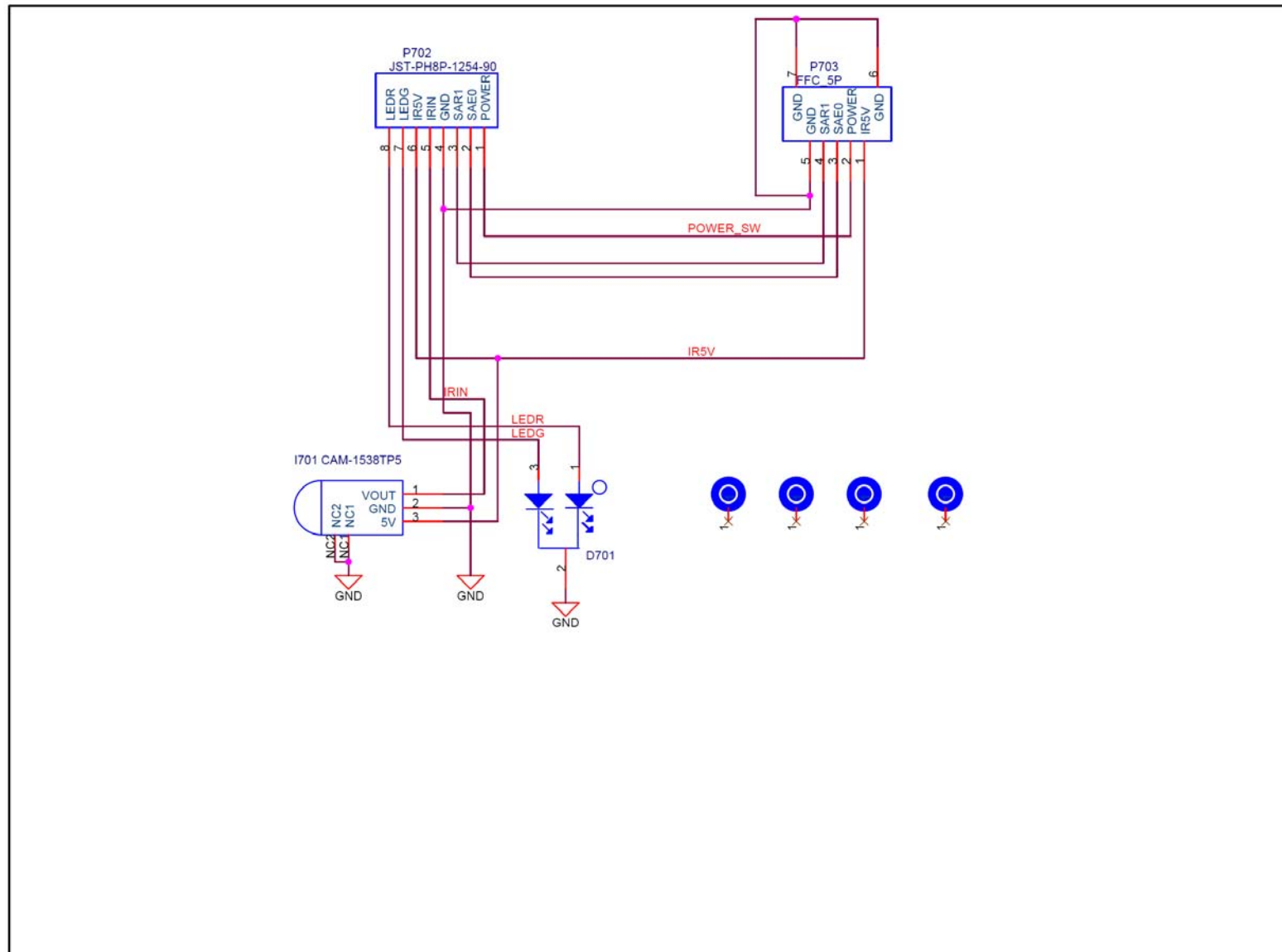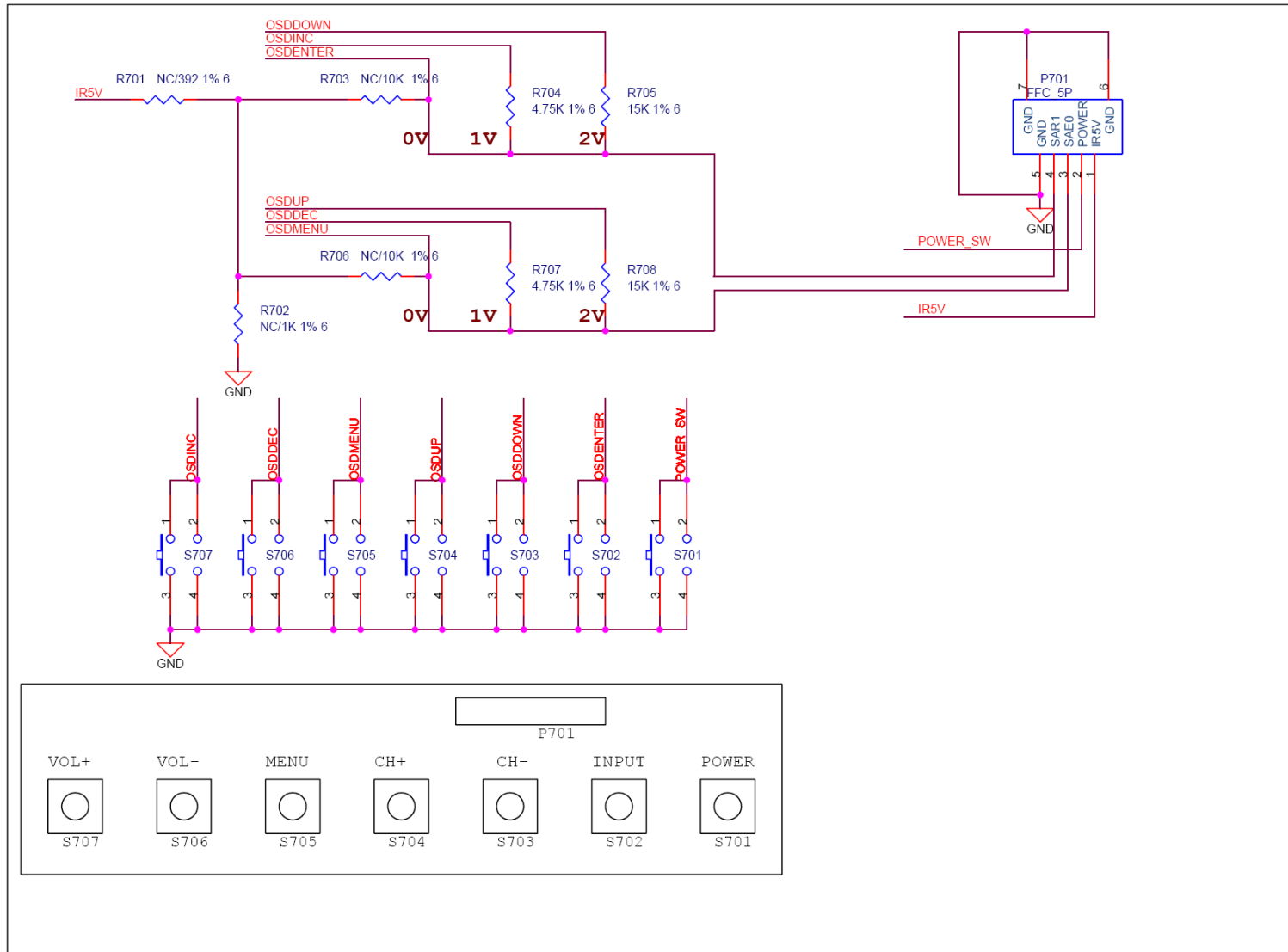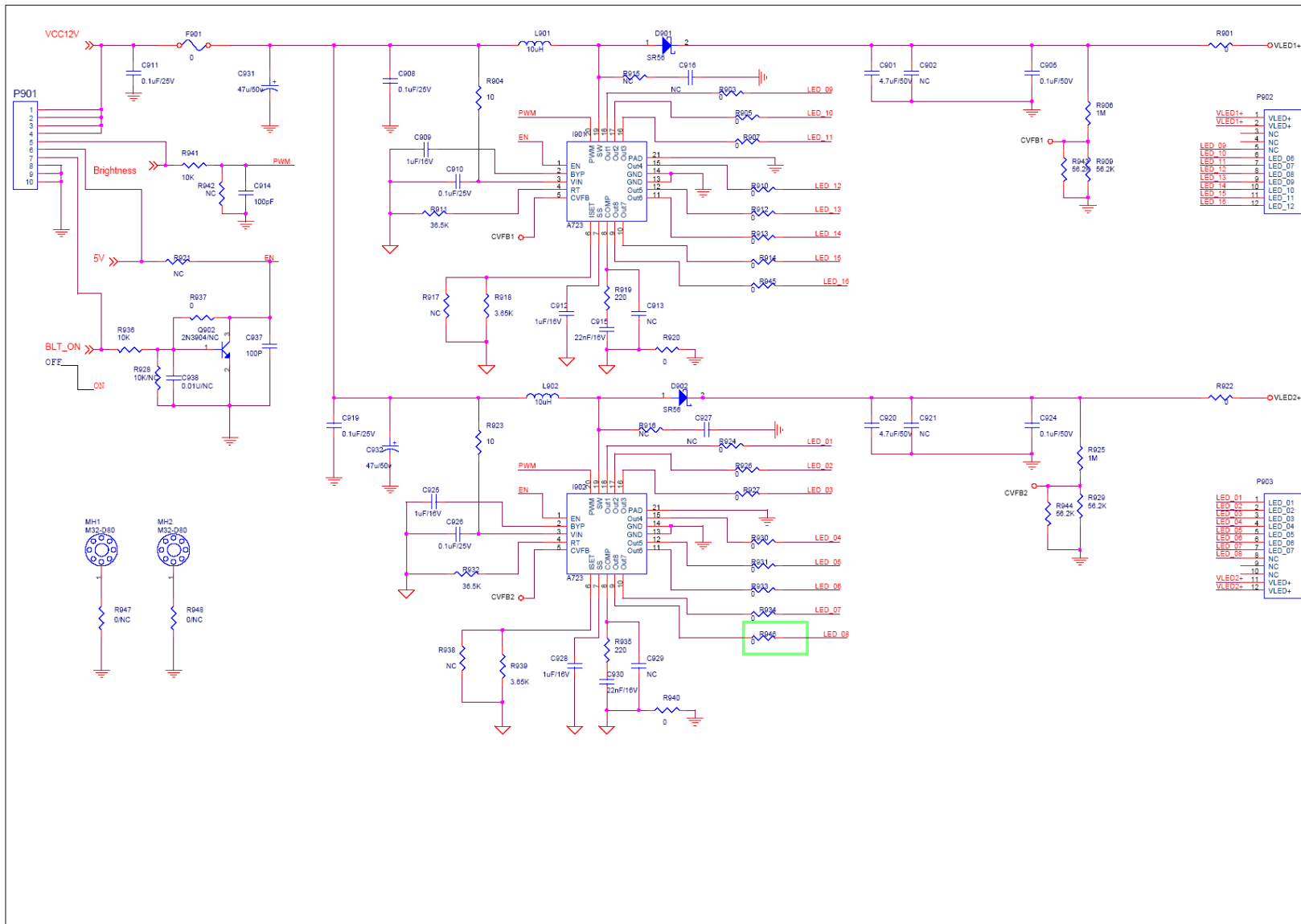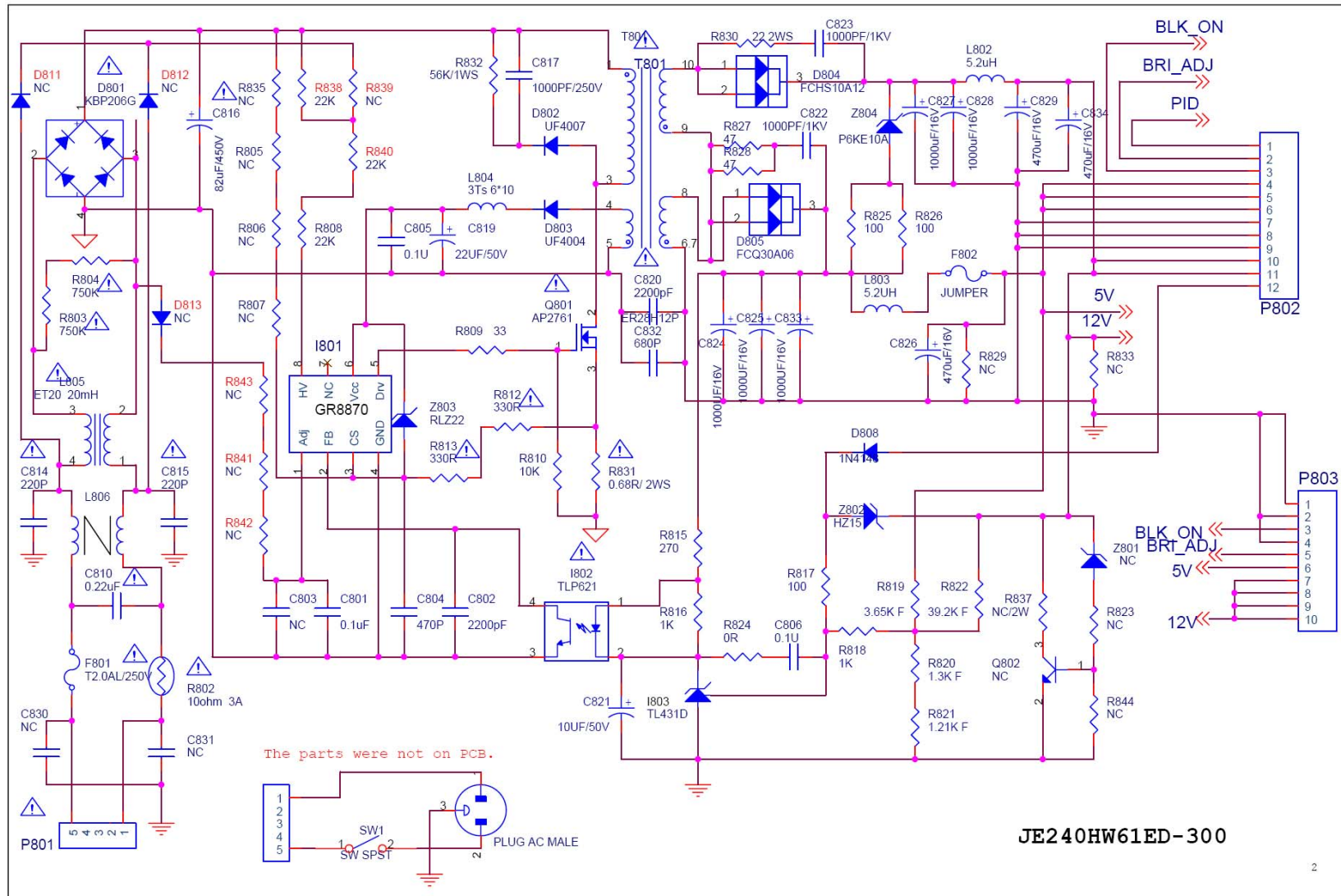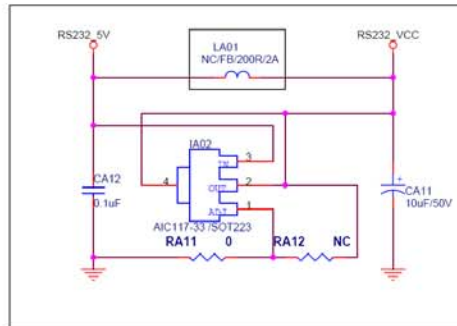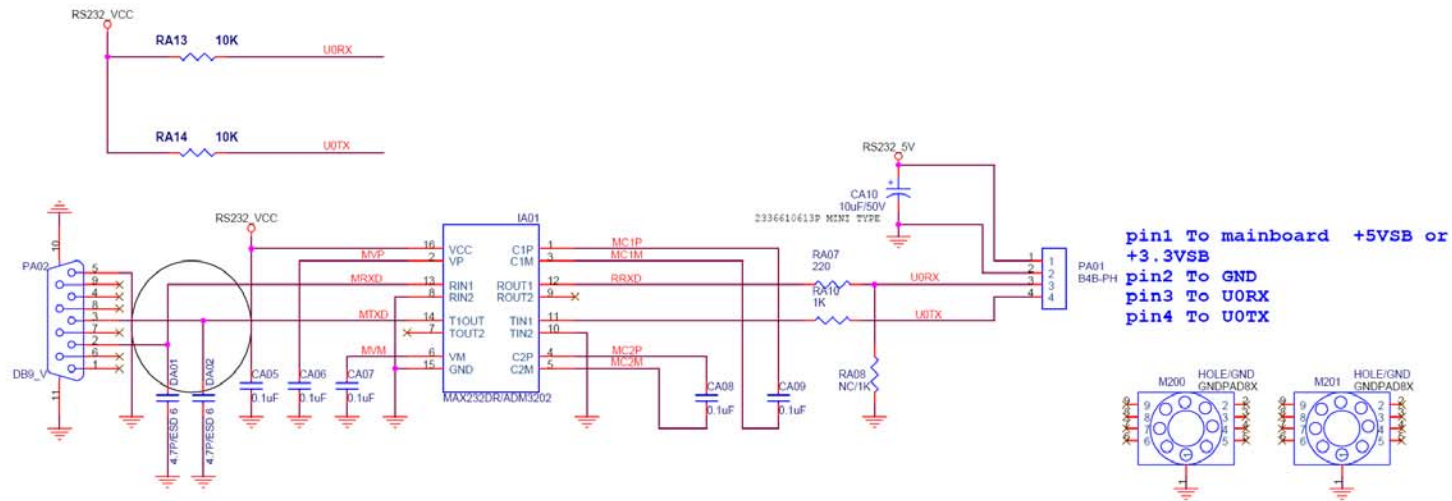
	ADM3202	MAX232DR
LA01	NC	FB/200R/2A
RA08	NC	1K
RA11	0	NC
RA12	NC	NC
IA02	AIC117-33 /SOT223	NC
CA11	10uF/50V	NC

PCB Artwork

MAIN PCB TOP VIEW
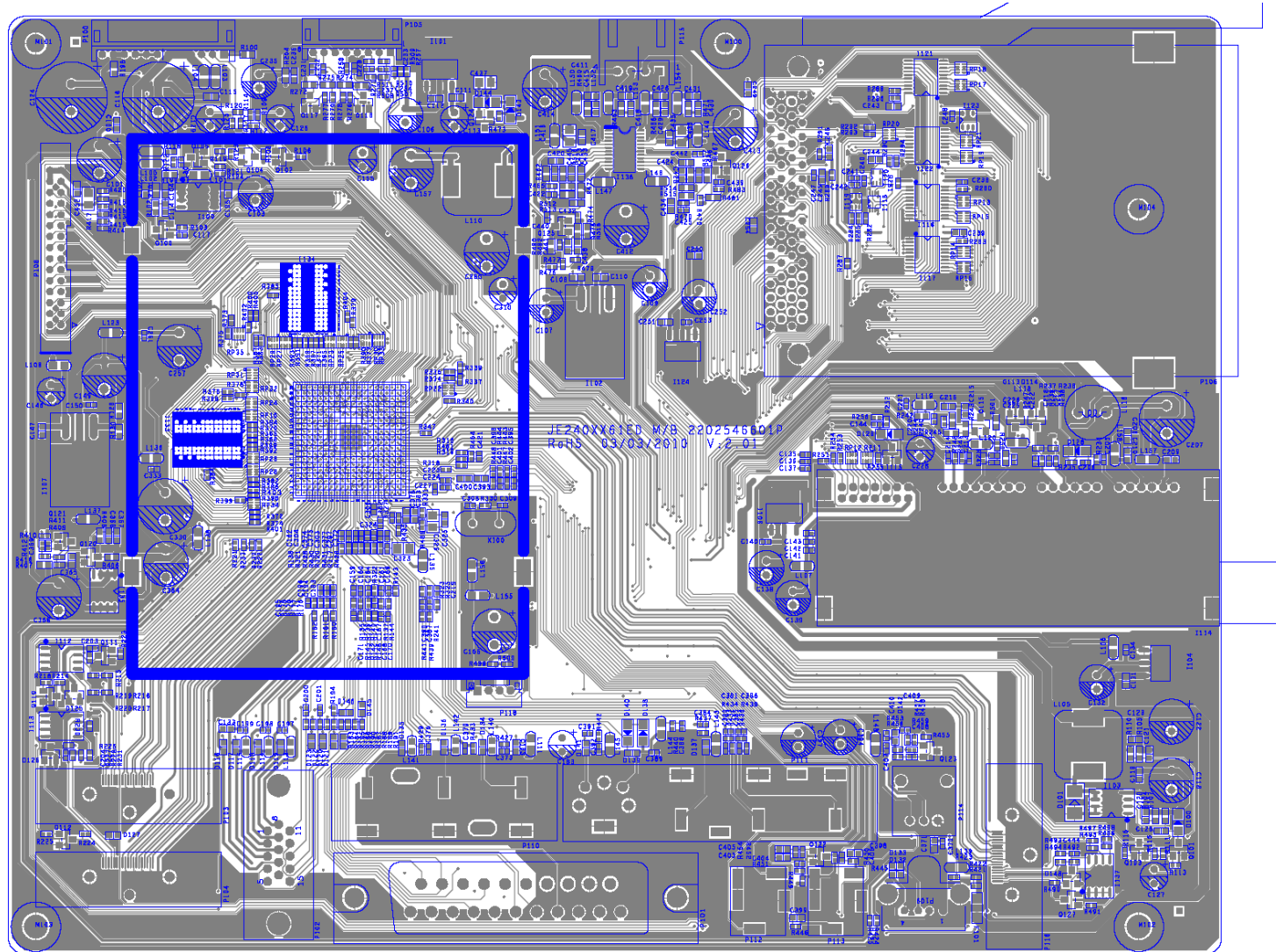
MAIN PCB BOTTOM VIEW

KEYPAD PCB TOP VIEW

100x12.2mm

JE240XX KEYPAD 2202550200P RoHS REV:1.00 DATE:02/23/2010

R704 R703   R702   R701   R708   R705
R707   R706

S707 VOL+   S706 VOL-   S705 MENU   S704 CH+   S703 CH-   S702 INPUT   S701 POWER
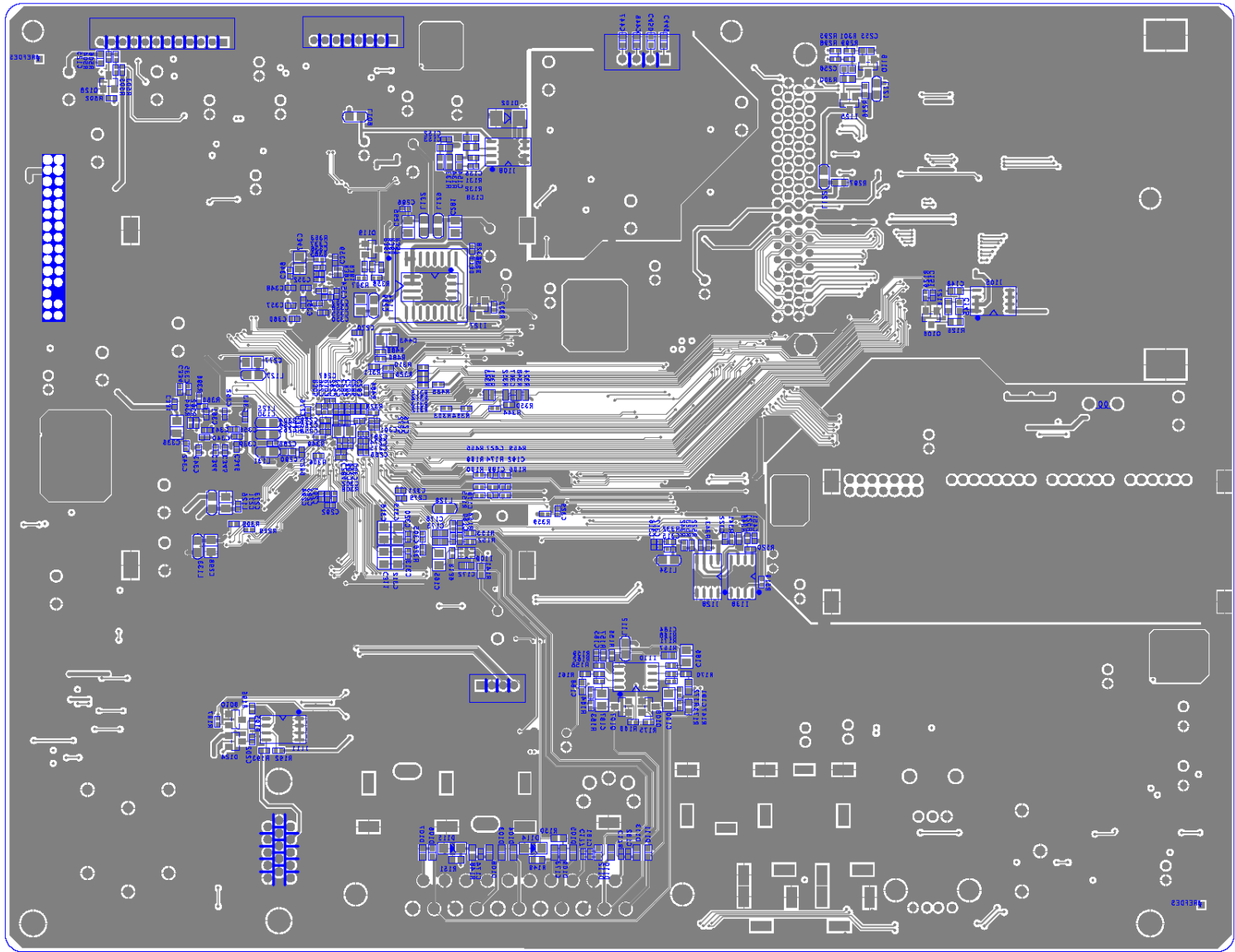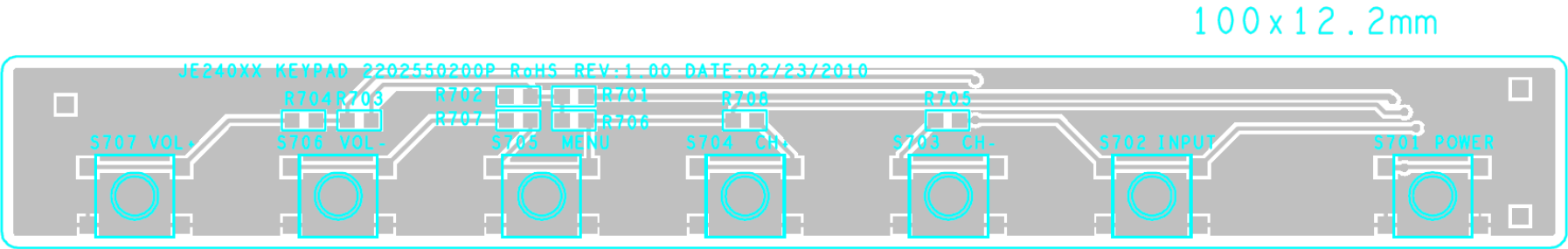
KEYPAD PCB BOTTOM VIEW

100x12.2mm

P701
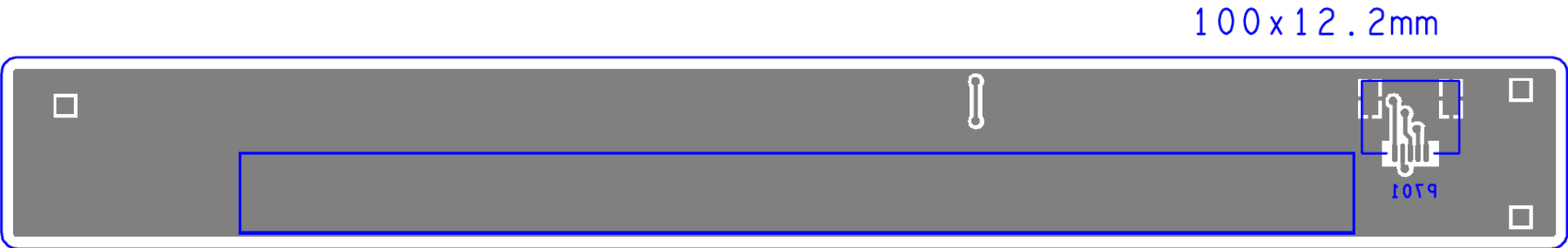
IR PCB TOP VIEW

IR PCB BOTTOM VIEW

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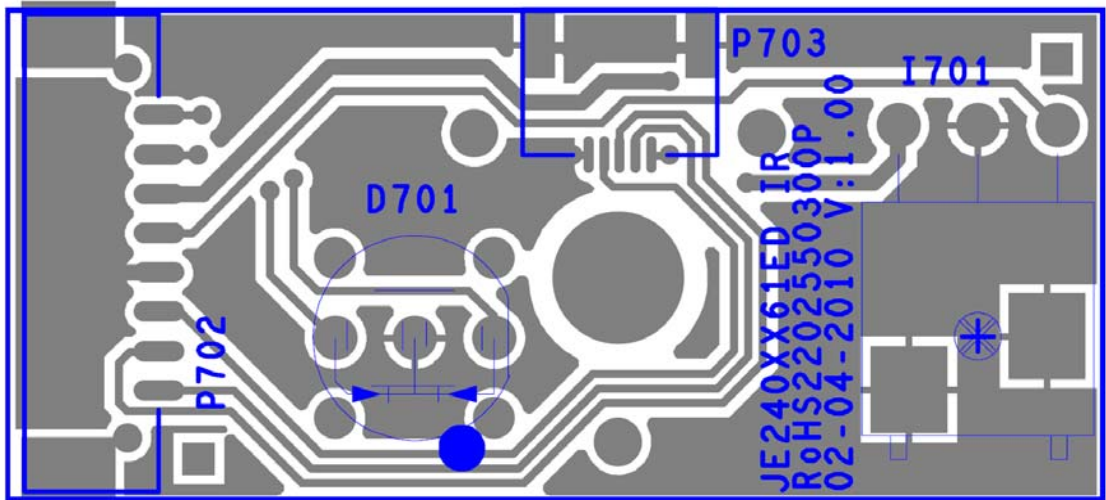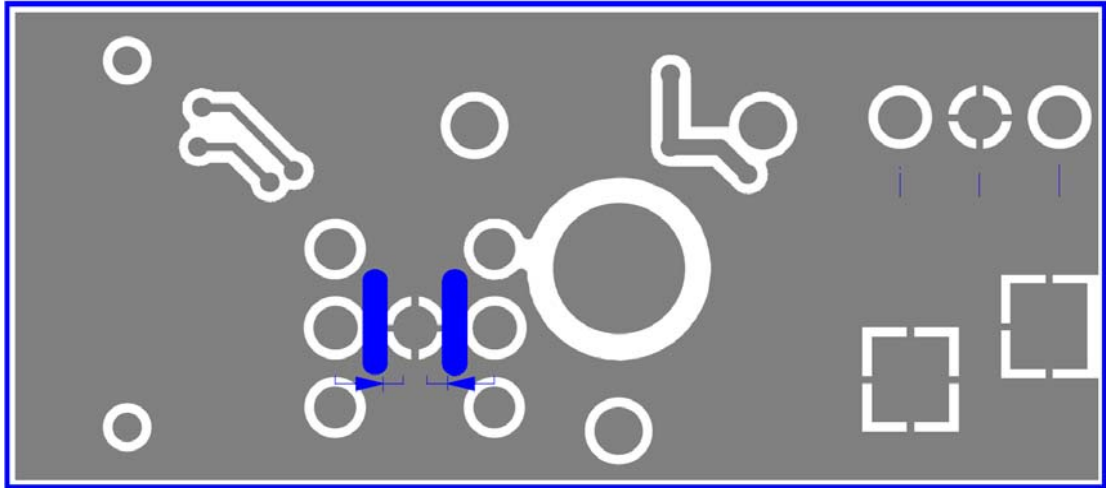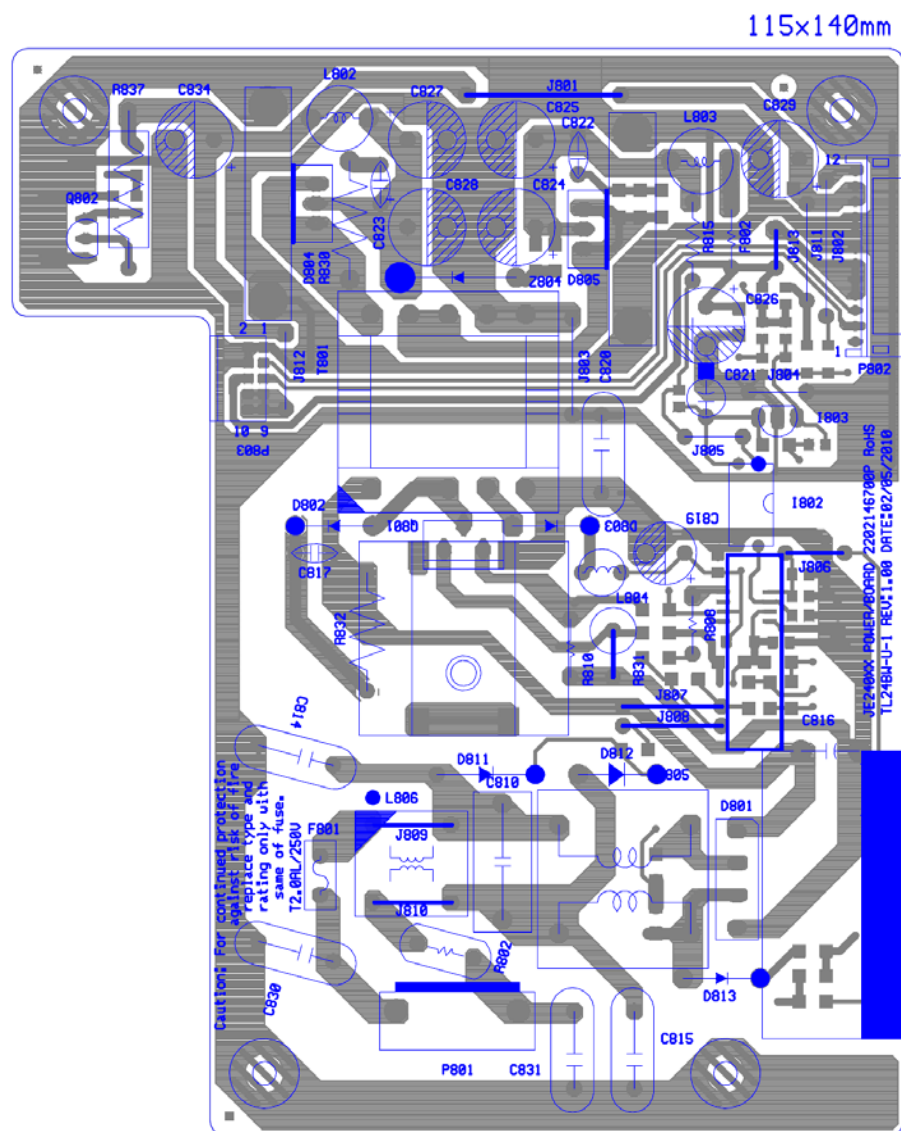
POWER PCB TOP VIEW

POWER PCB BOTTOM VIEW

115×140mm
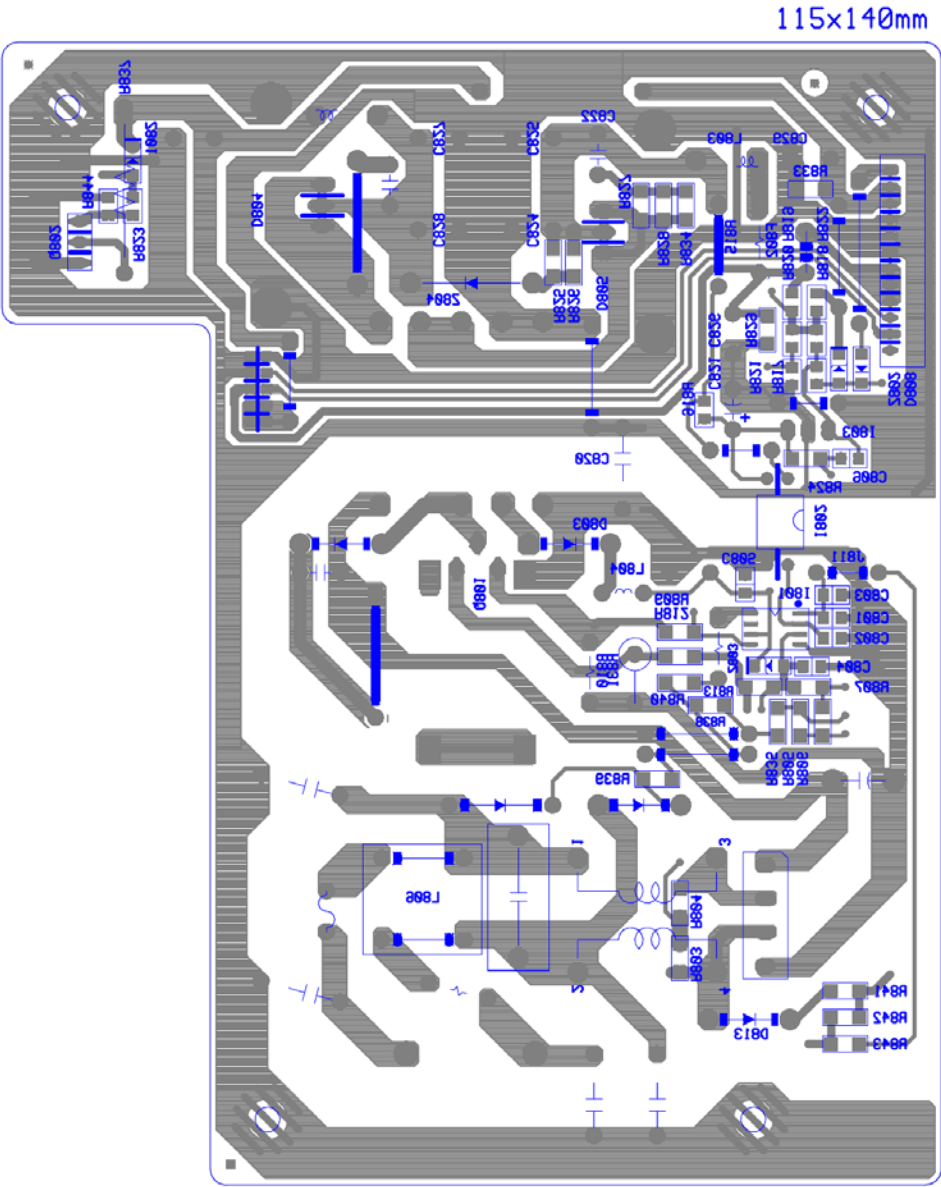
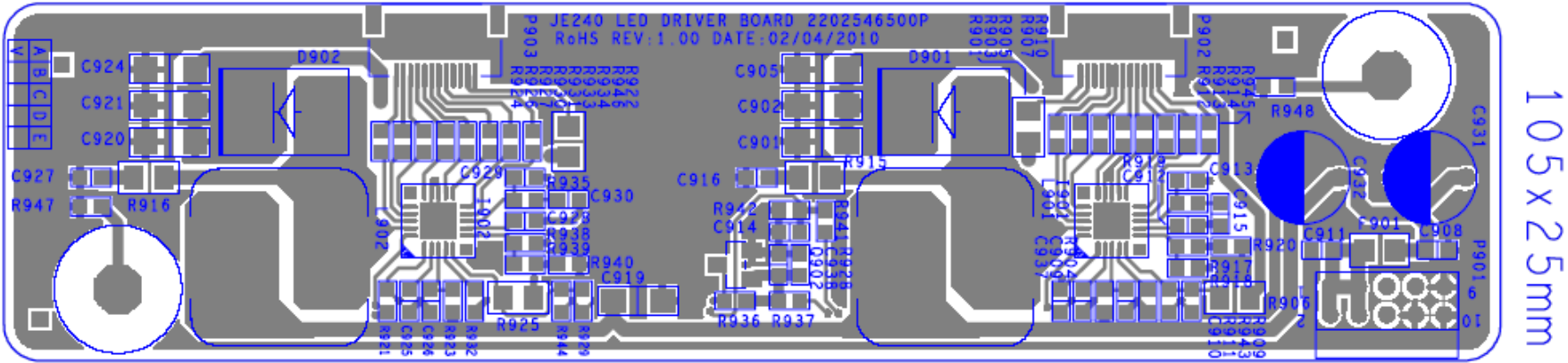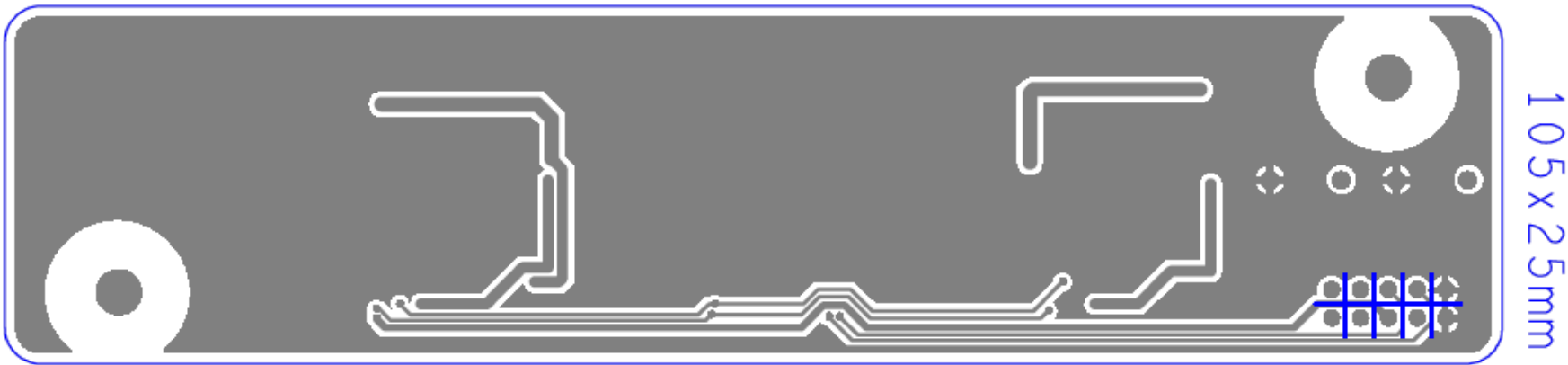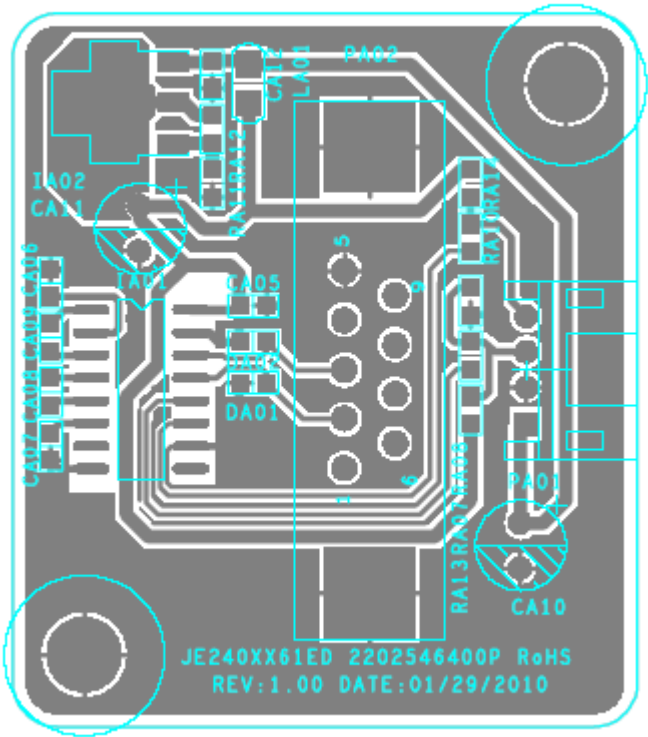**LED Driver PCB TOP VIEW**

LED Driver PCB BOTTOM VIEW

RS232 PCB TOP VIEW

35 x 40mm

RS232 PCB BOTTOM VIEW

35 x 40mm