

The diagram illustrates the PCB layout for the PDbPr Input section. It features two input connectors, X54 and X511, and their connections to various components.

Connector X54:

- Pin 1 (SIGNAL1) is connected to HD_Y.
- Pin 2 (GND1) is connected to GND.
- Pin 3 (SIGNAL2) is connected to HD_Pb.
- Pin 4 (GND2) is connected to GND.
- Pin 5 (SIGNAL3) is connected to HD_Pr.
- Pin 6 (GND3) is connected to GND.

Connector X511:

- Pin 1 (SIGNAL1) is connected to HD_L.
- Pin 2 (GND1) is connected to GND.
- Pin 3 (SIGNAL2) is connected to HD_R.
- Pin 4 (GND2) is connected to GND.

Resistors and Capacitors:

- RV03, RV04, and RV05 are resistors connected to the HD_Y, HD_Pb, and HD_Pr signals, respectively.
- RV04 and RV05 are resistors connected to the HD_L and HD_R signals, respectively.
- C194 and C195 are capacitors connected to the HD_LIN and HD_RIN signals, respectively.

Other Components:

- NC/VLC18S0015 is a component connected to the HD_Y, HD_Pb, and HD_Pr signals.
- NC/VLC18S0015 is a component connected to the HD_L and HD_R signals.

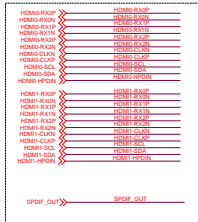
Notes:

- The text "Close to IC" is written in red, indicating the placement of the components relative to the IC.

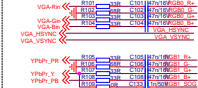
[illegible]

VGA Input

HDMI Interface



VGA & YPBP Interface



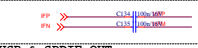
CVBS IN & OUT



AUDIO IN



DIF IN

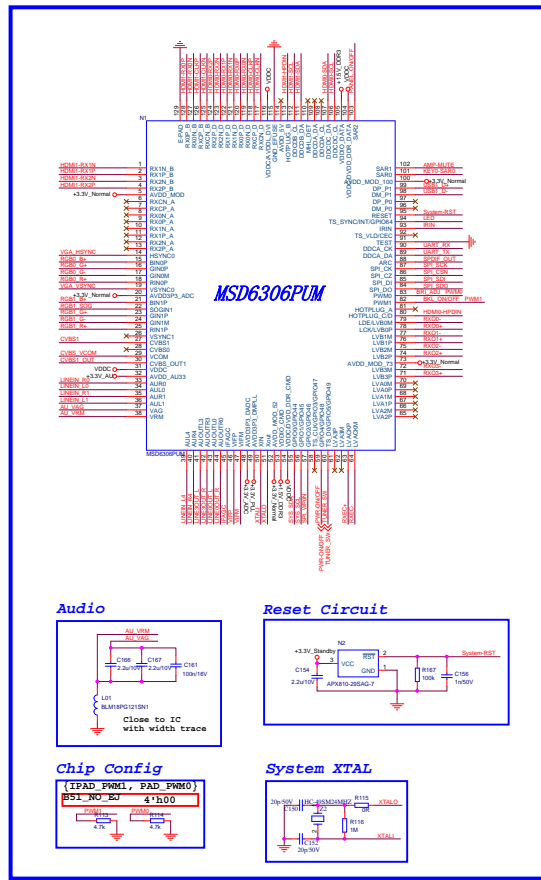
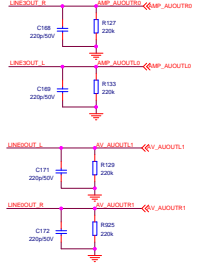


USB & SPDIF_OUT

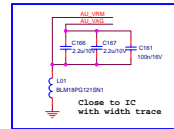


Audio Line Out

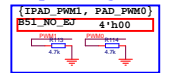
Near MST IC
Ground in the middle of the L/R



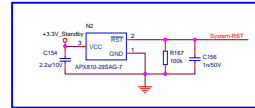
Audio



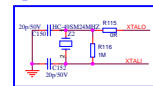
Chip Config



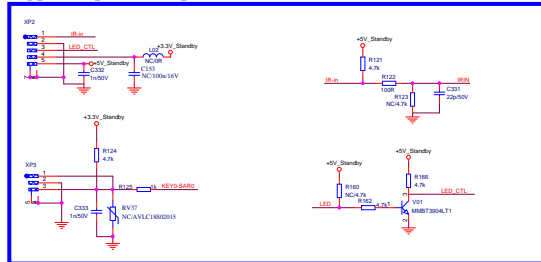
Reset Circuit



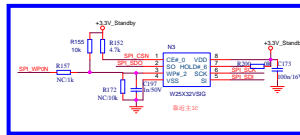
System XTAL



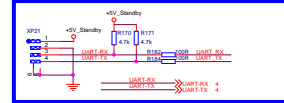
Keypad Input/IR Input/LED Ctrl



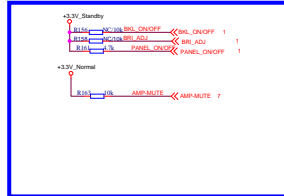
Serial Flash



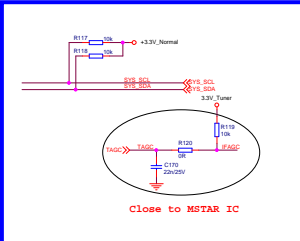
Debug port



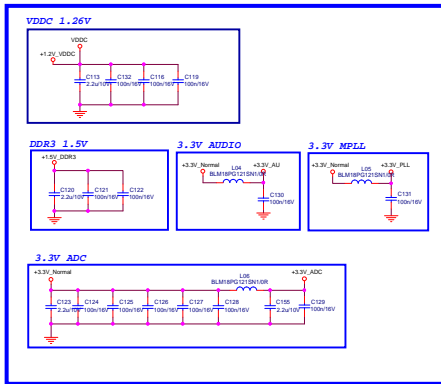
Pull Up/Down



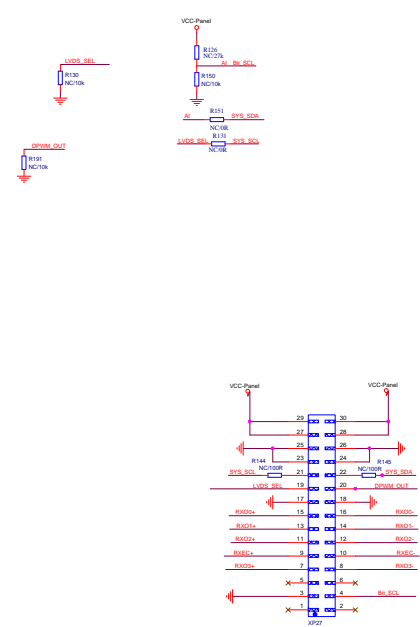
Tuner IIC&AGC



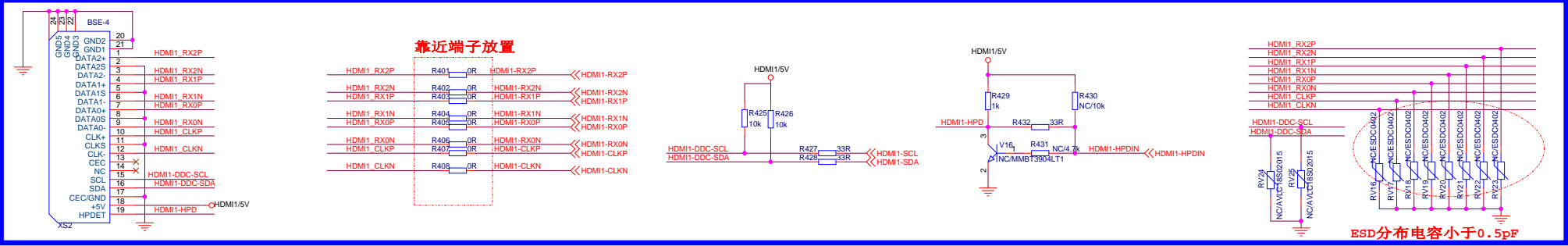
IC Power



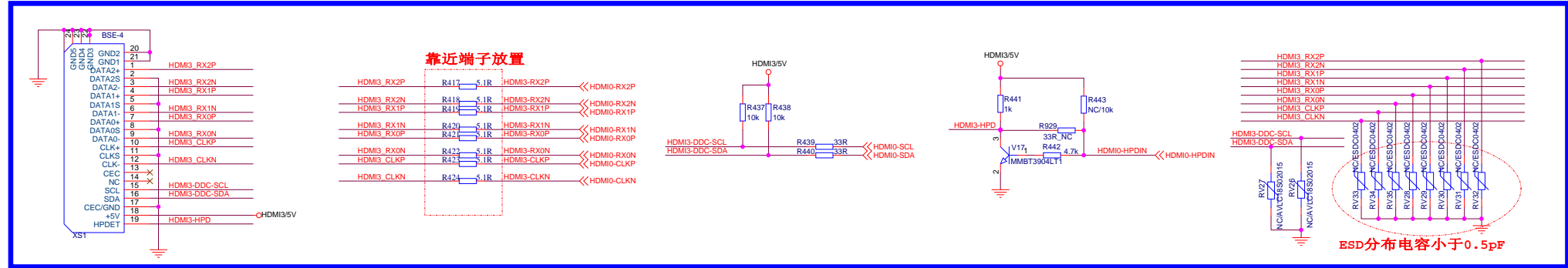
LVDS Connector



HDMI1



HDMI3



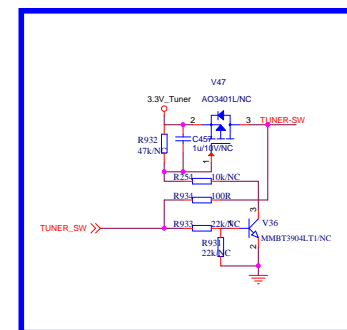
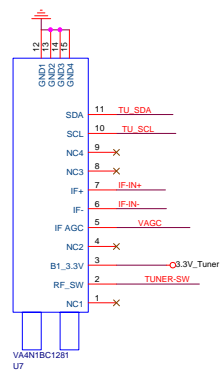
The image contains two circuit diagrams for the BPF and TUNER sections of the MST IC.

Left Diagram (BPF Section):

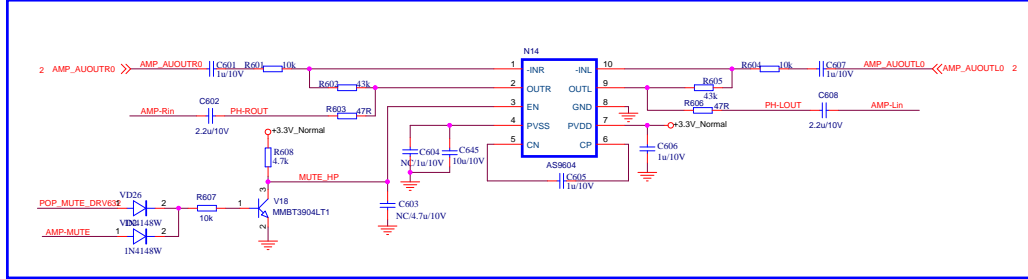
- Input: **IF-IN** (red line) connects to the **BPF_IN** pin (blue) of the MST IC.
- Output: **BPF_OUT-** (red line) connects to the **VIFM** pin (blue) of the MST IC.
- Output: **BPF_OUT+** (red line) connects to the **VIFP** pin (blue) of the MST IC.
- Resistors: **R501** (blue) and **R602** (blue) are connected between the BPF_OUT- and BPF_OUT+ pins and ground.
- Capacitors: **C512** (blue) and **C511** (blue) are connected between the BPF_IN and BPF_OUT+ pins and ground.
- Values: **C512** is **NC/20p/50V** and **C511** is **NC/20p/50V**.
- Text: **Close to MST IC with wide trace** (blue text).

Right Diagram (TUNER Section):

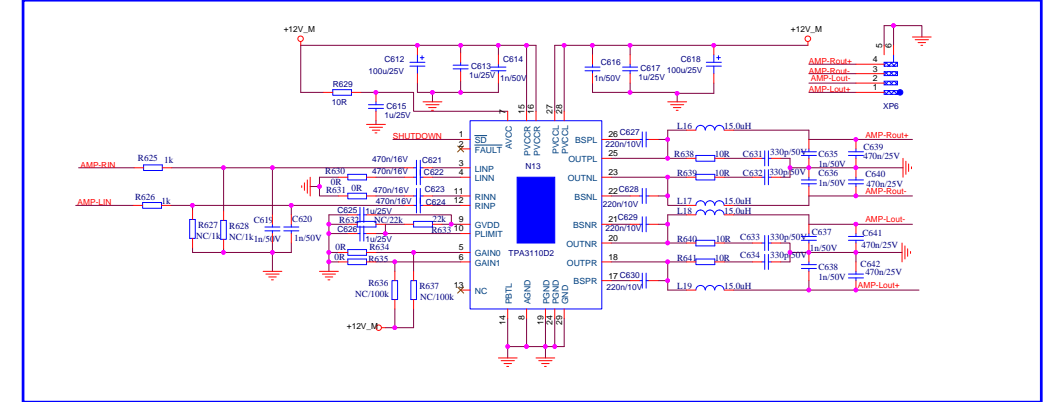
- Input: **TU_SDA** (red line) connects to the **TUNER_SDA** pin (blue) of the MST IC.
- Input: **TU_SCL** (red line) connects to the **TUNER_SCL** pin (blue) of the MST IC.
- Resistors: **R927** (blue) and **R928** (blue) are connected between the TUNER_SDA and TUNER_SCL pins and ground.
- Capacitors: **C911** (blue) and **C912** (blue) are connected between the TUNER_SDA and TUNER_SCL pins and ground.
- Values: **C911** is **10p/50V** and **C912** is **10p/50V**.
- Text: **TUNER AGC, Close TUNER** (blue text).



Headphone Out



Audio AMP



Mute

