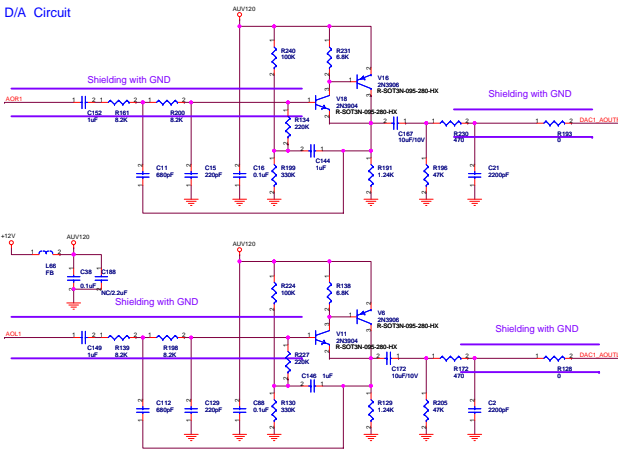
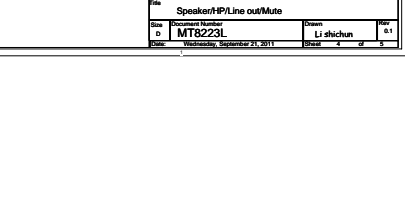
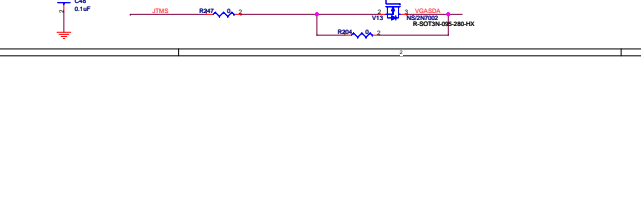
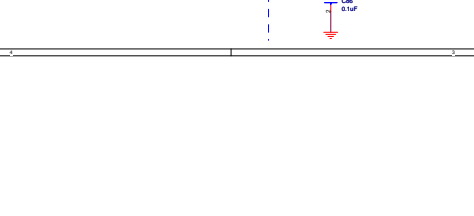
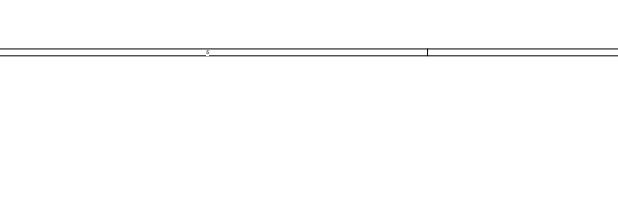
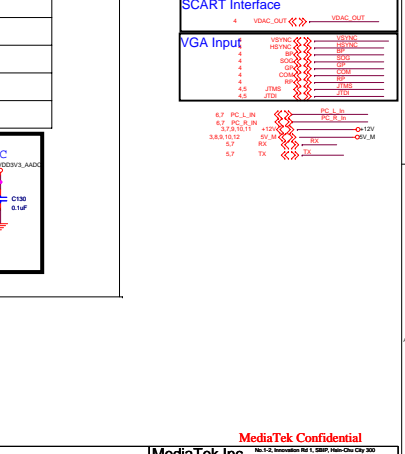
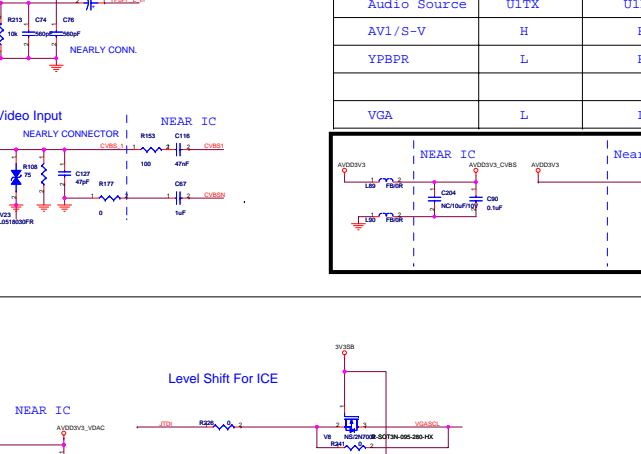
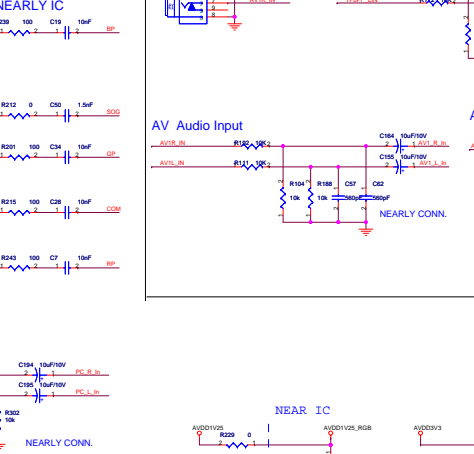
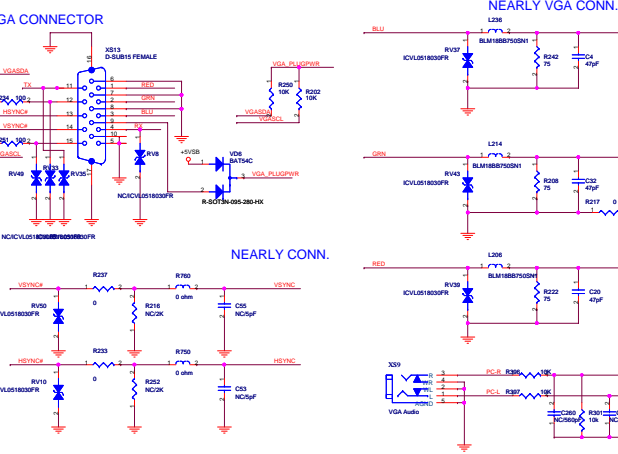
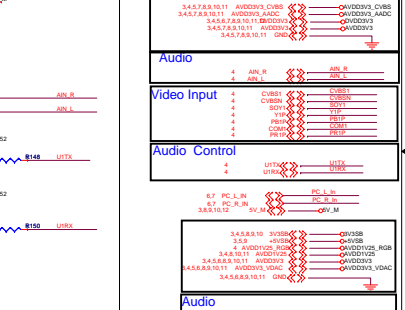
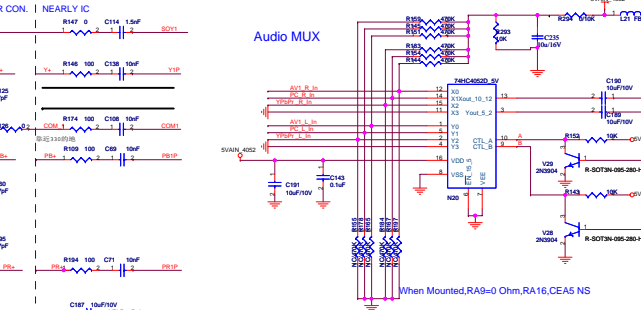
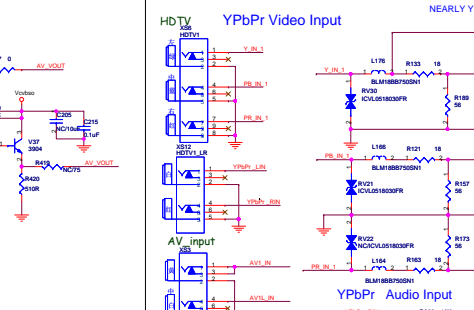
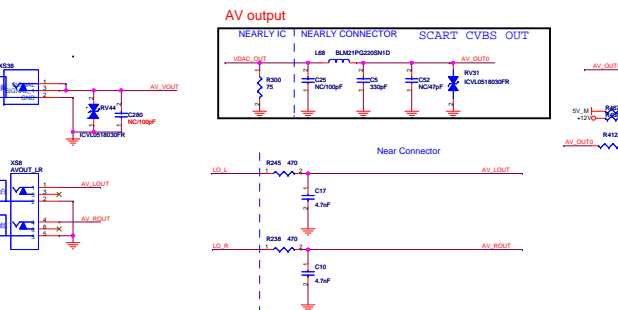
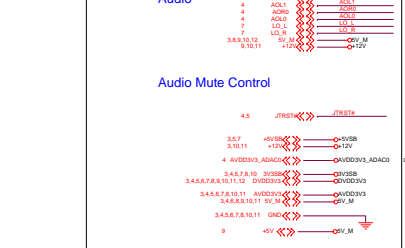
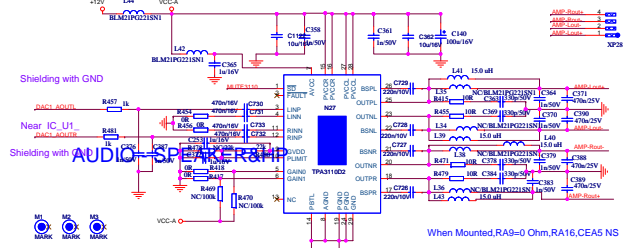
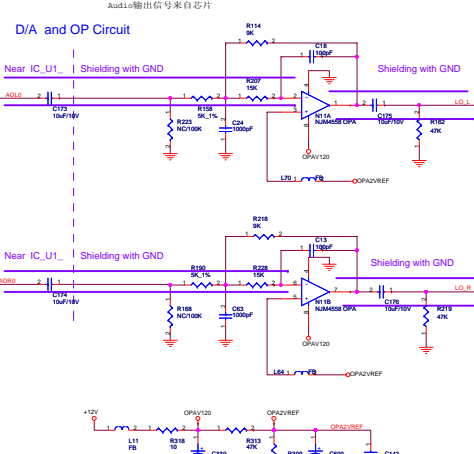




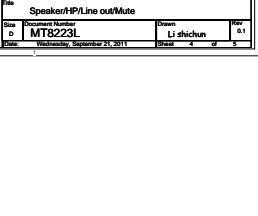
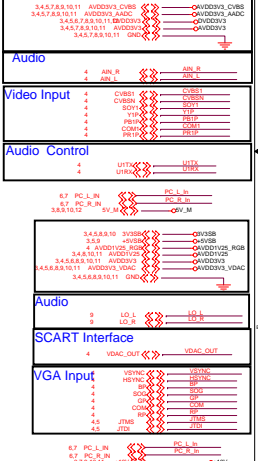
D/A Circuit



D/A and OP Circuit



Audio Source	UI1TX	UI1RX
AV1/S-V	H	H
YPbPr	L	H
VGA	L	L



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Speaker/HP Line out Mute  
Date: Wednesday, September 27, 2011  
Author: Li shichun  
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