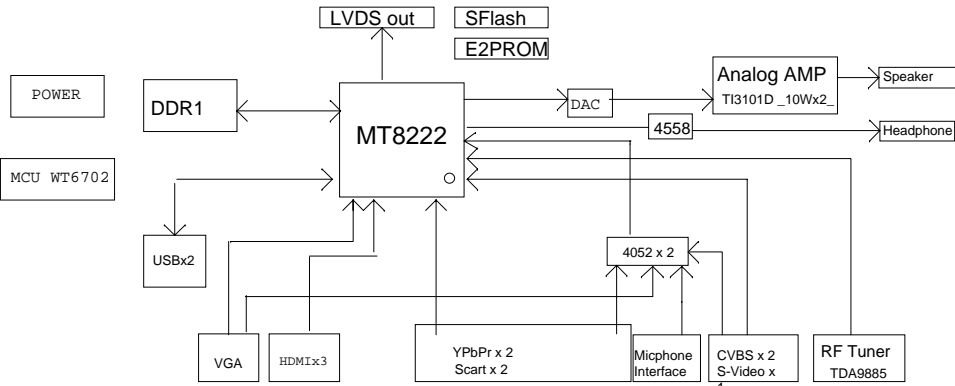


MT8222_P1V1 _DDR1_ VERSION V1.0

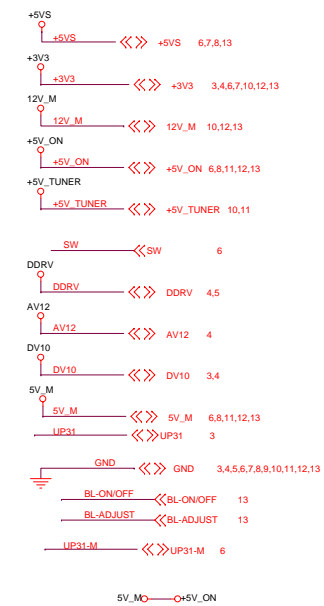
_ DDR1 WITH TERMINATION _

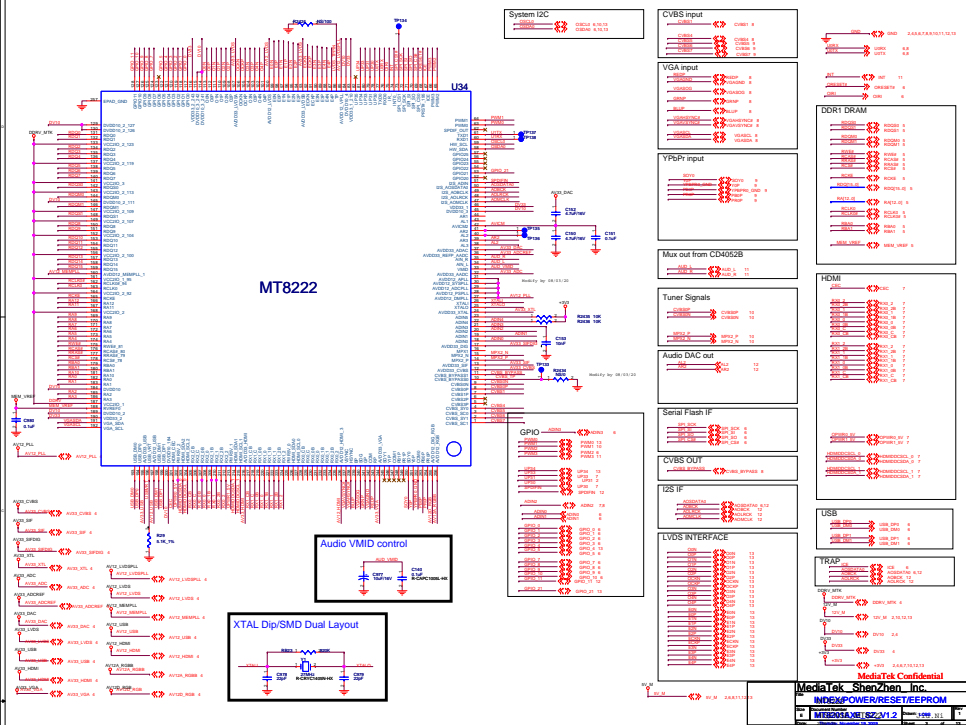
GPIO usage

GPIO	Definition	Function define
ADIN0	SCART FS	
ADIN1	SCART FS	
ADIN2	4052 SWITCH	
ADIN3	KEYPAD	
ADIN4	KEYPAD	
ADIN5	HDMI/VGA EDID E2PROM WP	High = WP disable
GPIO_20	Audio MUX	
GPIO_21	AMP_MUTE CONTROL	High = Mute on
GPIO_22	GAME	
GPIO_23	GAME	
GPIO_24	GAME	
GPIO_25	GAME	
PWM0	DIMMING CONTROL	
PWM1	Power conversion to 33V	
PWM2	SYSTEM E2PROM WP	High = WP disable
PWM3	Audio MUX	
UP30	BL ON/OFF CONTROL	Low = Backlight on
UP31	NORMAL POWER ON/OFF	Low = Normal power on
UP33	HMDI 0 HPLUG DETECT	
UP34	HMDI 1 HPLUG DETECT	
UP35	HMDI 2 HPLUG DETECT	
GPIO_0	MICPHOTO RESERVE	
GPIO_1	LVDS RESERVED	
GPIO_2	FCI	
GPIO_3	FCI	
GPIO_4	LVDS POWER ON/OFF	Hi = LVDS power on
GPIO_5	AMP_SD/FCI	
GPIO_6	SCART1 VIDEO OUTPUT SW/FCI	
GPIO_7	FCI	
GPIO_8	FCI	
GPIO_9	FCI	
GPIO_10	FCI	
GPIO_11	4052 SWITCH	Low = HP insert
INT	Audio MUX select bit 0	
SPDIFIN	LVDS RESERVED	
AOSDATA0	USB0 OC TAG	
AOLRCK	USB0 PWR ENABLE	
AOBCLK	USB1 PWR ENABLE	
AOMCLK	USB1 OC TAG	

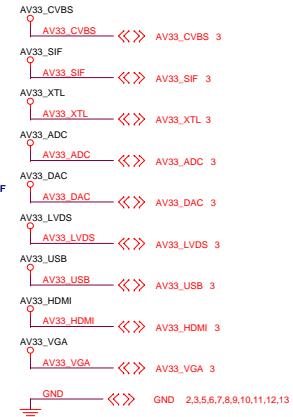
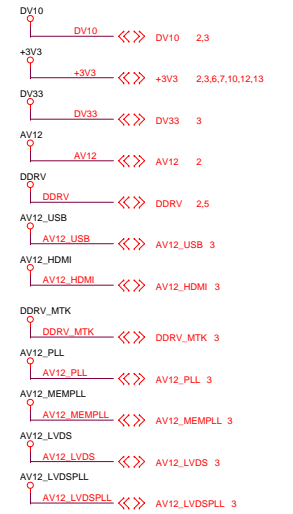
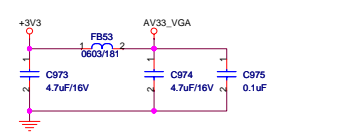
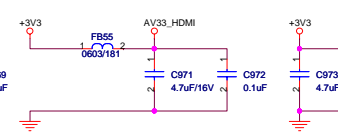
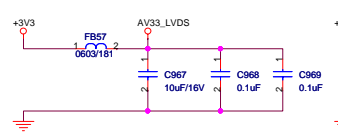
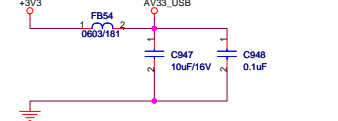
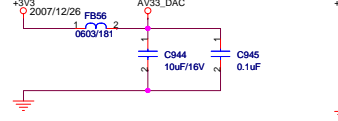
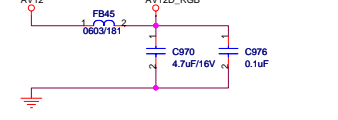
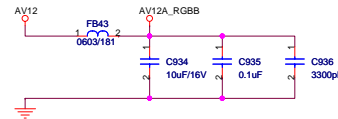
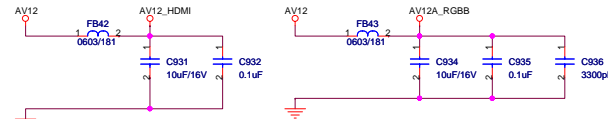
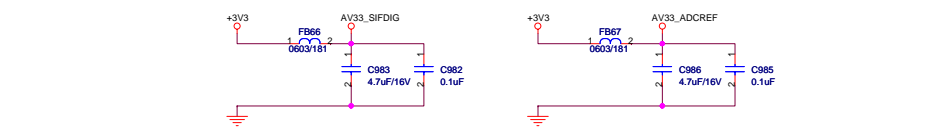
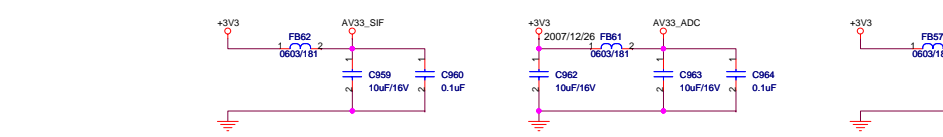
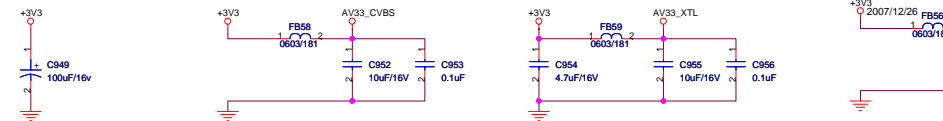
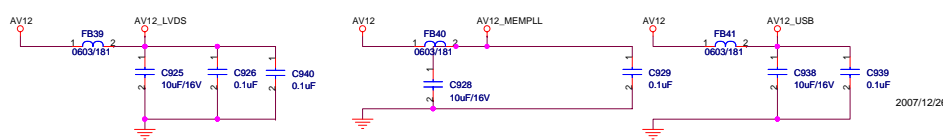
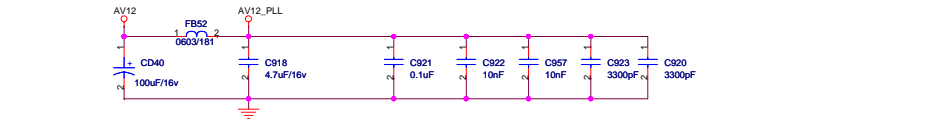
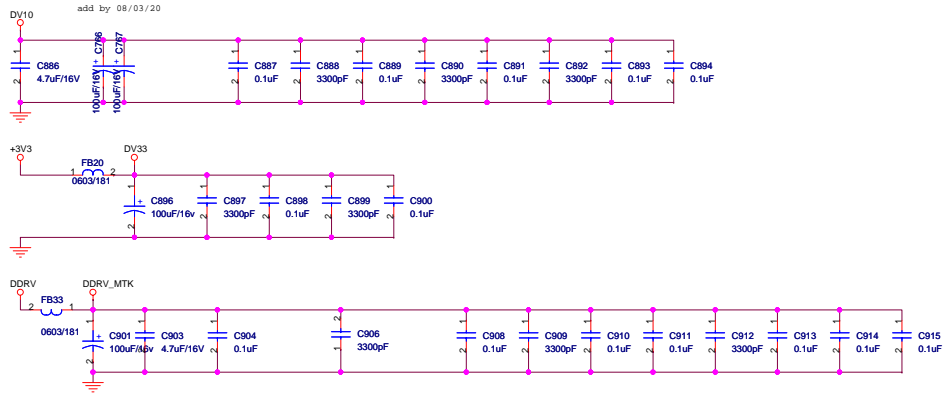


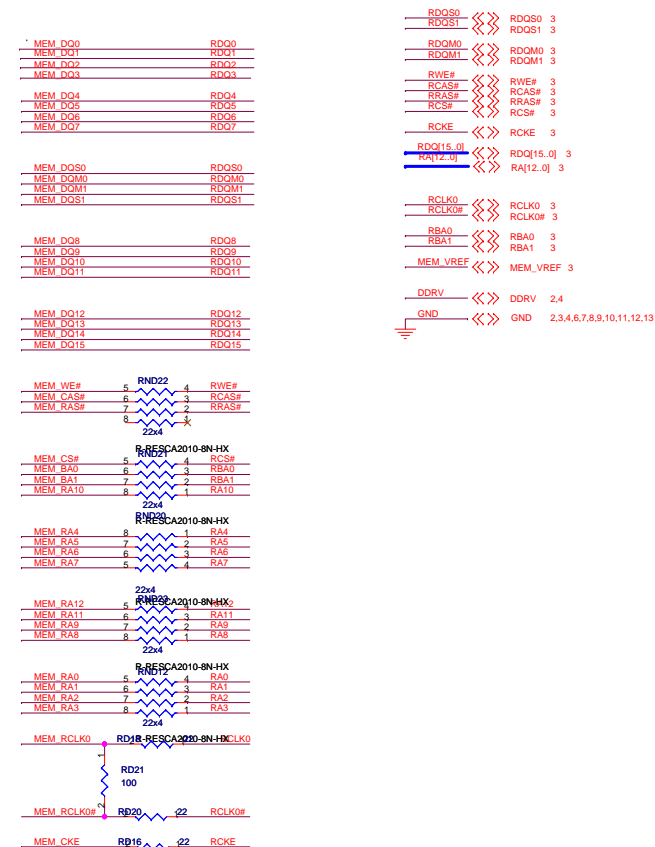
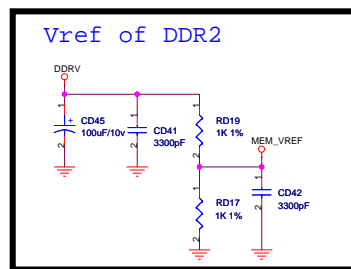
Power Distribution



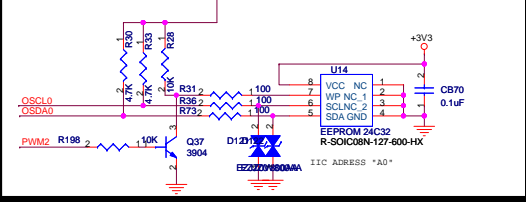


_ Bypass CAPs around MT8222 _

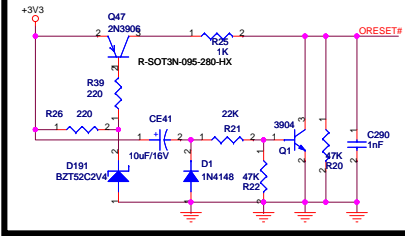




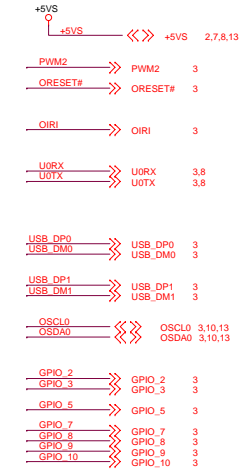
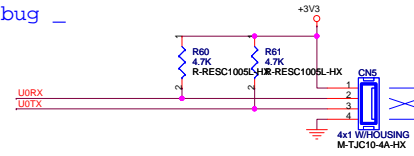
System E2PROM



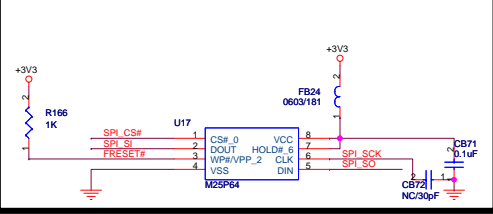
System Reset# 上电和掉电都应该产生低电平复位。



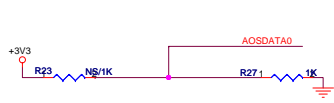
1st UART IF
_ For Code
download and
Debug _



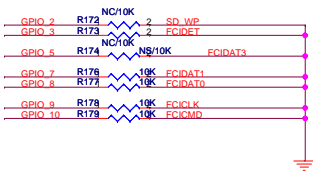
Serial Flash



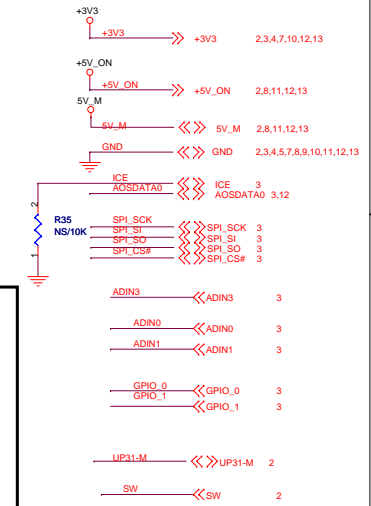
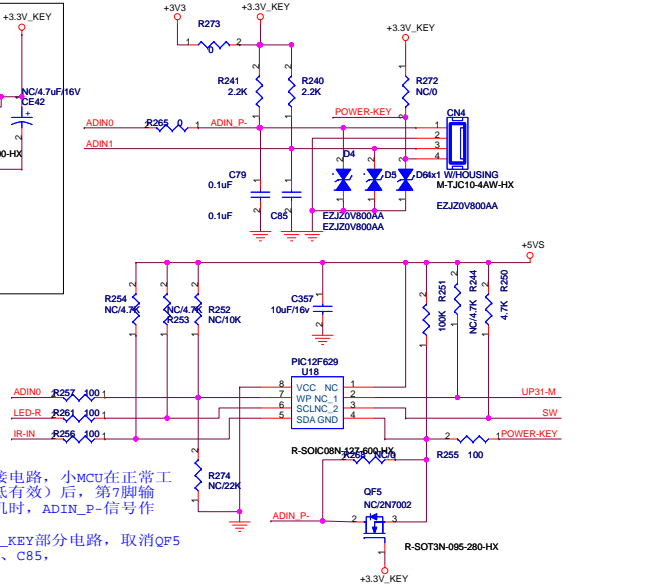
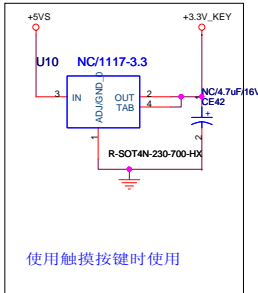
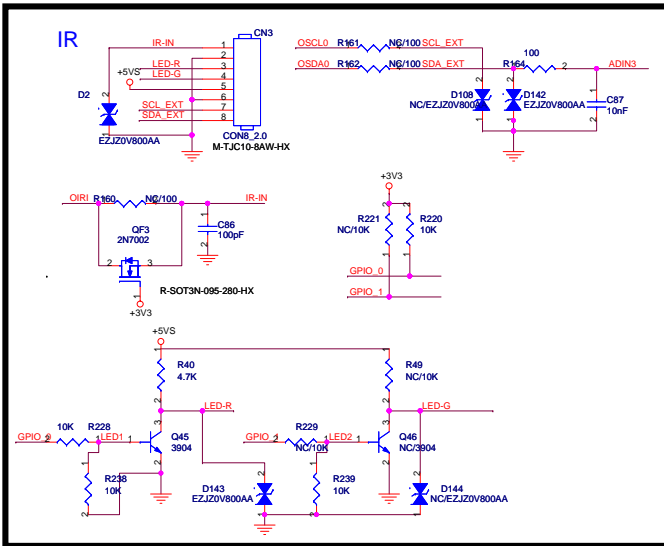
MT8222 Trapping



MEMORY CARD I/F GPIO RESERVED



ADC KEYPAD



MediaTek Confidential

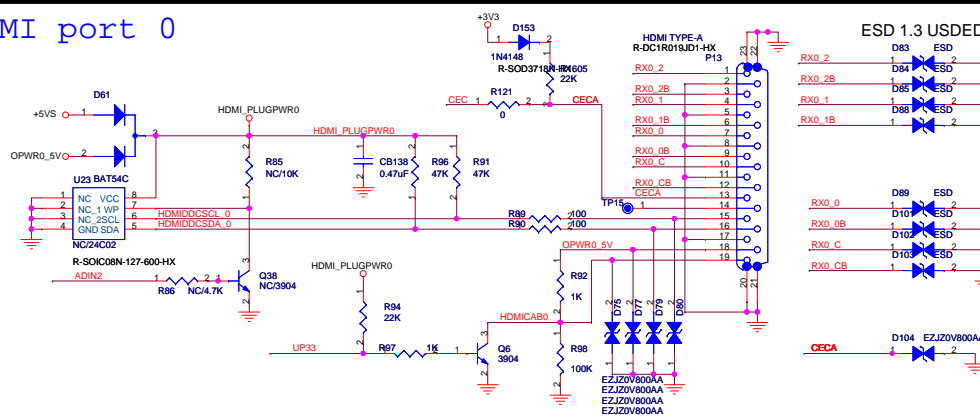
MediaTek ShenZhen Inc.

INDEX/POWER/RESET/EEPROM

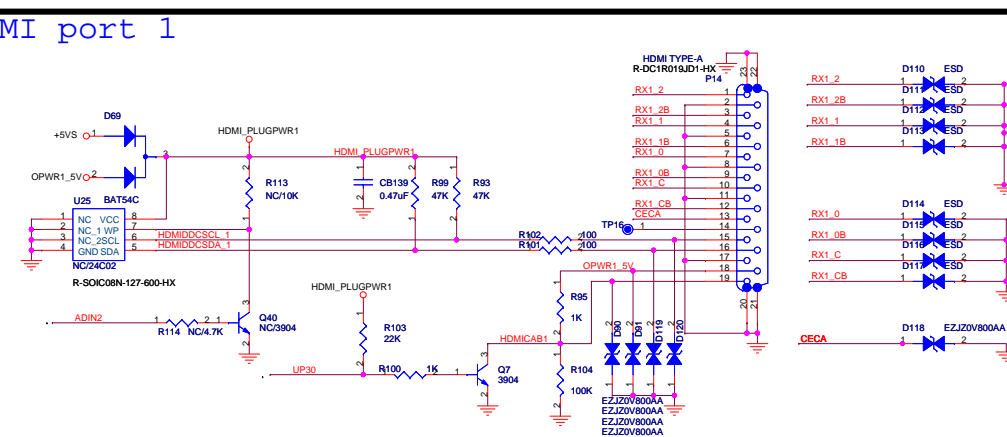
Size	Document Number	Drawn: +GBS
C	MTS2001-YE-67-1/1 2	

Date:	Thursday, November 19, 2009	Sheet	6	of
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HDMI port 0



HDMI port 1



+3V3 <<> +3V3 2,3,4,6,10,12,13

CEC <<> CEC 3

RX0_2 <<> RX0_2 3
RX0_2B <<> RX0_2B 3
RX0_1 <<> RX0_1 3
RX0_1B <<> RX0_1B 3
RX0_0 <<> RX0_0 3
RX0_0B <<> RX0_0B 3
RX0_C <<> RX0_C 3
RX0_CB <<> RX0_CB 3

RX1_2 <<> RX1_2 3
RX1_2B <<> RX1_2B 3
RX1_1 <<> RX1_1 3
RX1_1B <<> RX1_1B 3
RX1_0 <<> RX1_0 3
RX1_0B <<> RX1_0B 3
RX1_C <<> RX1_C 3
RX1_CB <<> RX1_CB 3

OPWR0_5V <<> OPWR0_5V 3
OPWR1_5V <<> OPWR1_5V 3

HDMIDDCSCL_0 <<> HDMIDDCSCL_0 3
HDMIDDCSDA_0 <<> HDMIDDCSDA_0 3
HDMIDDCSCL_1 <<> HDMIDDCSCL_1 3
HDMIDDCSDA_1 <<> HDMIDDCSDA_1 3

UP30 <<> UP30 3
UP33 <<> UP33 3

— HDMI Hot Plug Detect —

ADIN2 <<> ADIN2 3,8

GND <<> GND 2,3,4,5,6,8,9,10,11,12,13

+5V_ON <<> +5V_ON 2,6,8,11,12,13

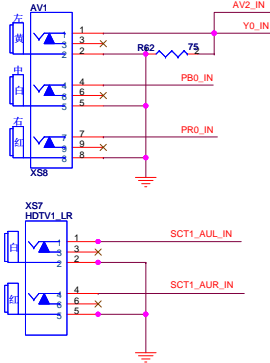
+5VS <<> +5VS 2,6,8,13

1. E2P供电更改
2. 各信号上ESD器件更改

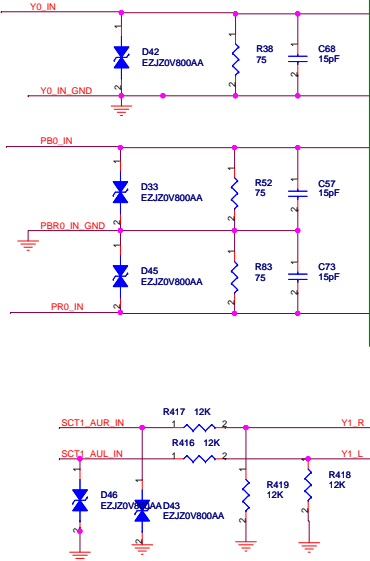
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MediaTek ShenZhen Inc.				
INDEX				
Size	Document Number	Drawn: J.008	Rev	1
C	MT8203B MT8220 V1.2	U.0.0.0		
Date:	2013/11/18	Sheet	7	of 13

YPbPr0/AV2 port



NEAR CONNECTOR



NEAR IC

