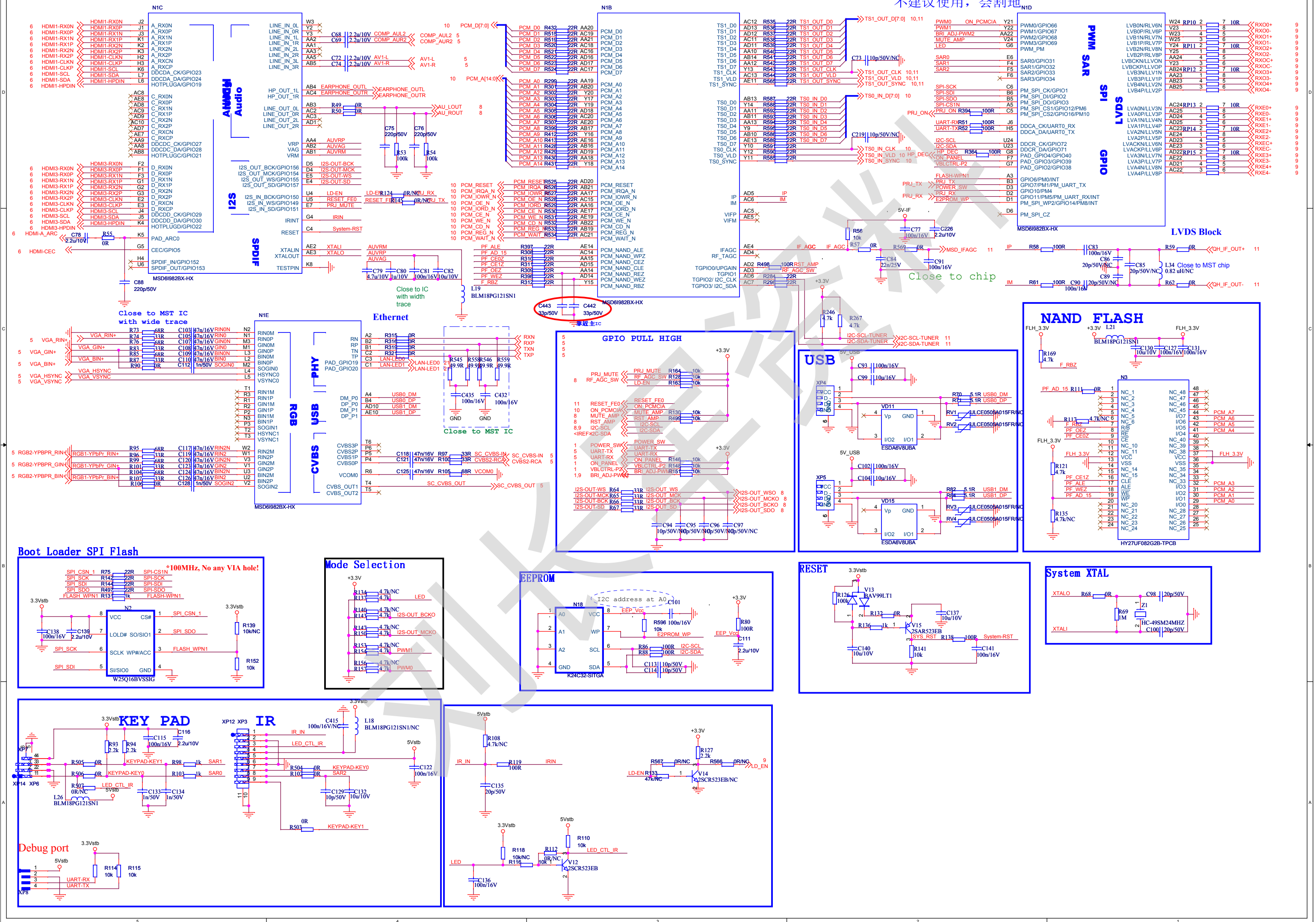
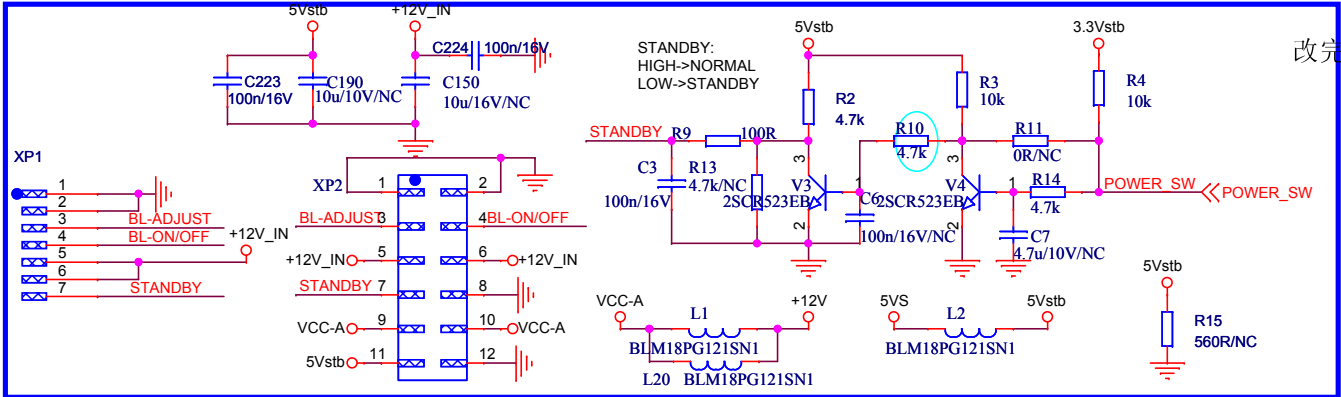


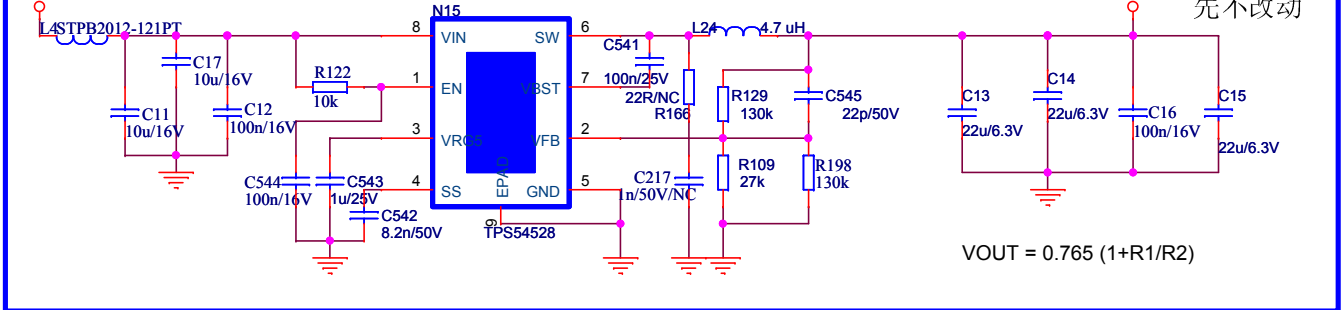
1.Ball G7, Ball G8, Ball F7, Ball D6,
Ball E7 ONLY SOCKET BOARD HAVE,两层板
不建议使用, 会割地



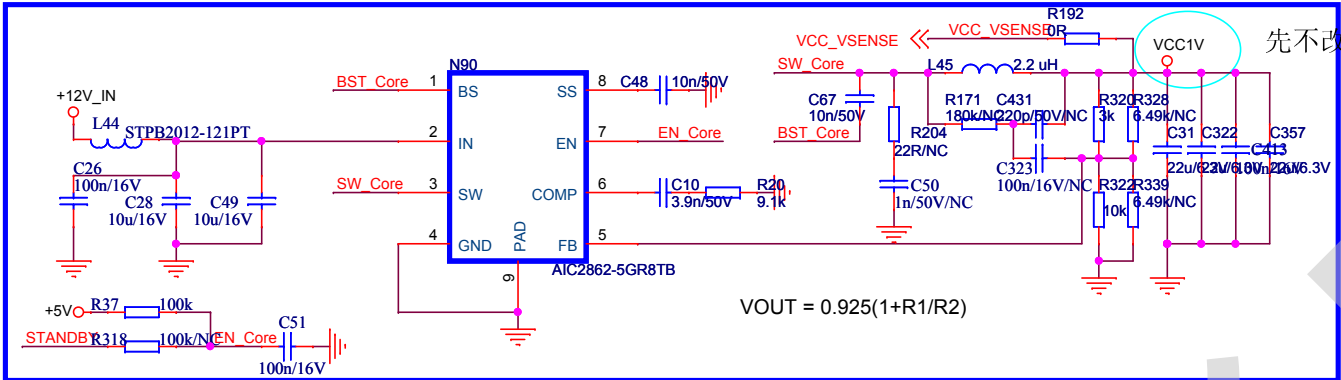
Power Input



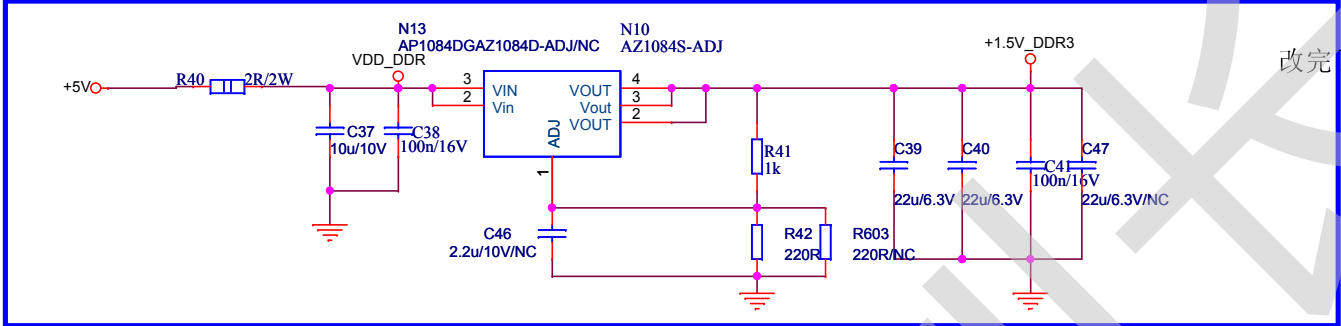
+5V FOR SYS



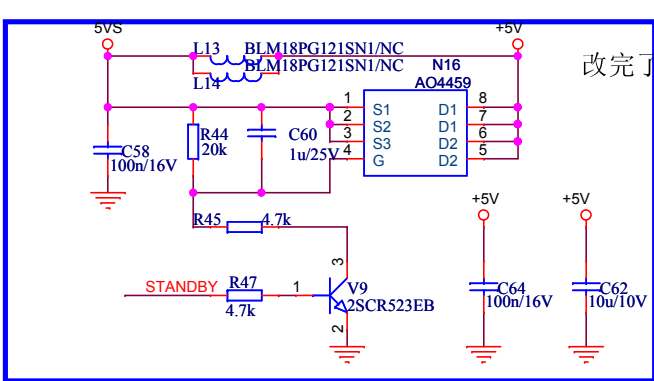
1.2V FOR 61982 CORE POWER



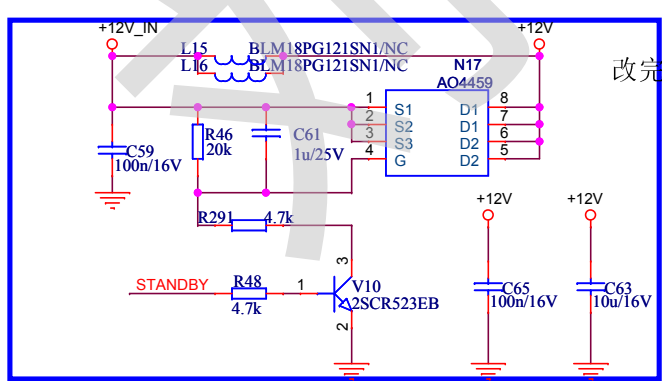
1.5V Power_DDR3(1105681)



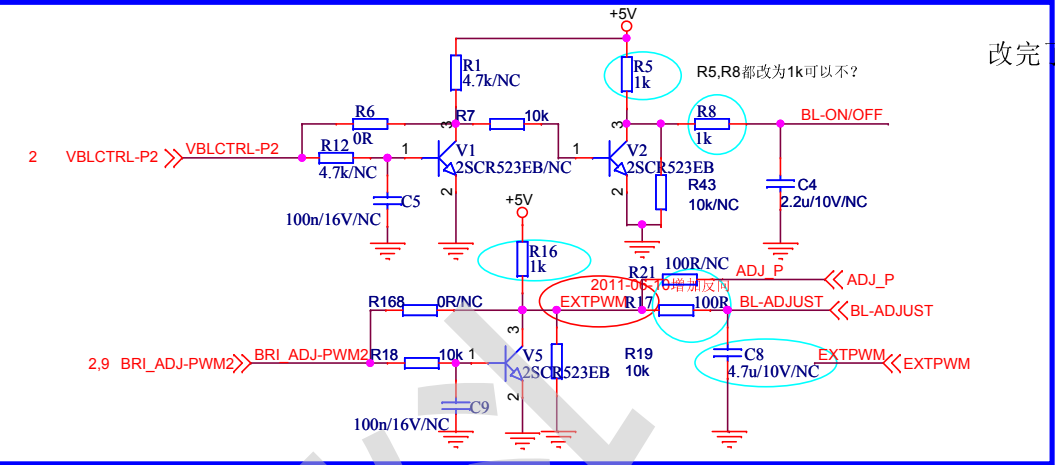
+5V POWER



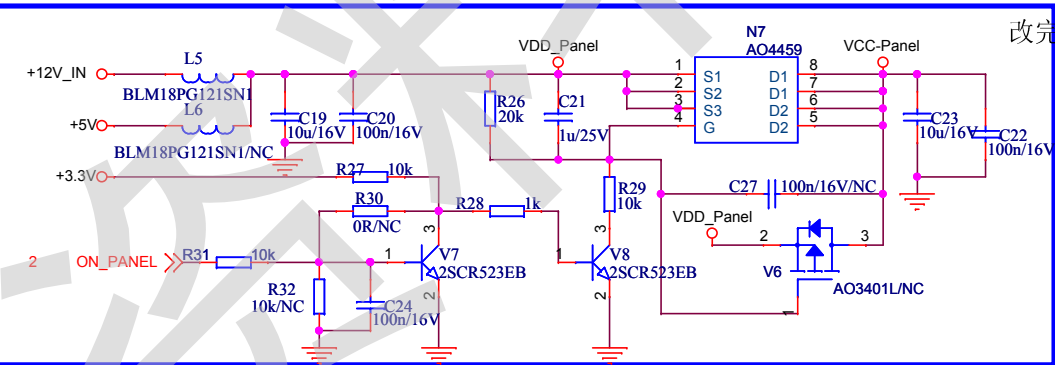
+12V POWER



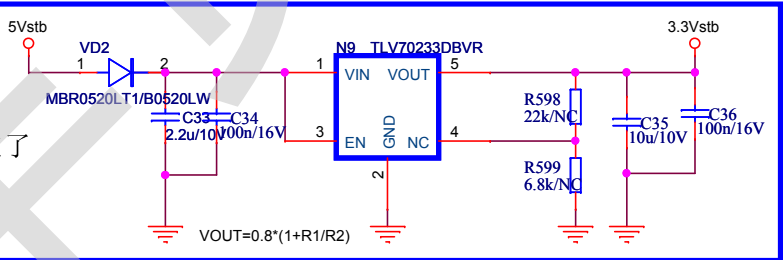
TO Inverter Board



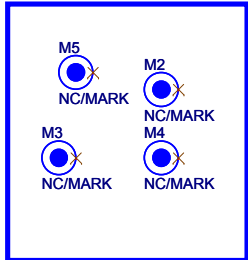
Power for Panel



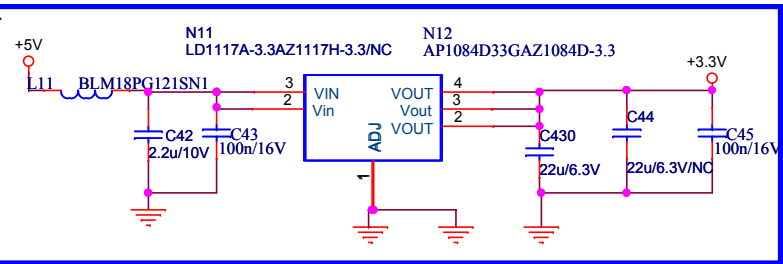
3.3V Power_Standby only for AVDD_MPLL and IR



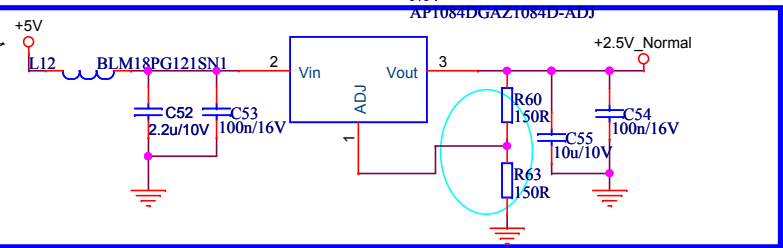
Mark



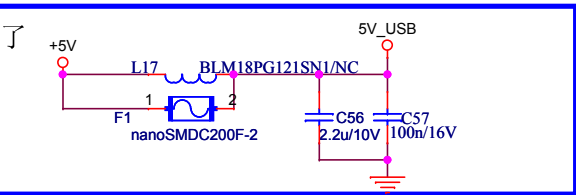
3.3V Power_Normal



2.5V FOR 61982



USB POWER



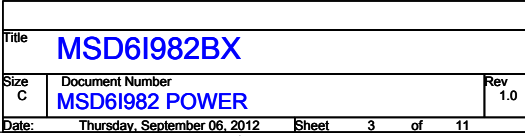
Title			MSD61982BX
Size	Document Number	Rev 1.0	
Customer	System Power		
Date:	Friday, September 07, 2012	Sheet	1 of 11

- 1、GPIO7/PM1/PM_UART_TX原来为ON_PANEL,改为TX,ON_PANEL改到GPIO39
- 2、VBLCTRL-P2也做同样更改，要注意这两项影响时序，要仔细重新调整
- 3、增加串口电平转换芯片，注意是用PM口(PRJ_ON)，同时用一个PM口控制该芯片是否上电，注意要求该口可以在工厂内控制
- 4、去掉同轴
- 5、增加siliicon onboard
- 6、增加工程机功放，注意增加开关机静音电路
- 7、耳机检测从PM10改到GPIO40
- 8、增加工程机功放静音，用原3D-EN脚
- 9、去掉3D部分

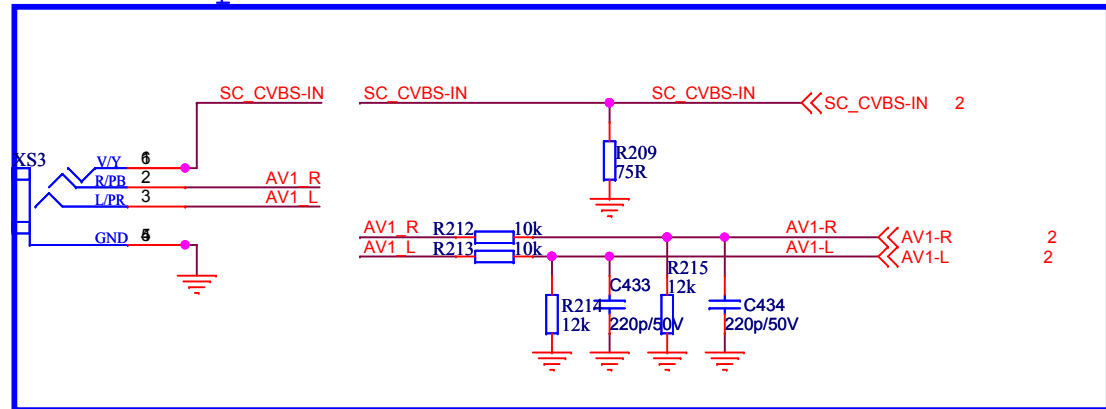
The schematic diagram illustrates the power supply section for the ATmega328P, showing various voltage regulators and decoupling capacitors. The components are organized into several functional blocks:

- 2.5V Regulator:** A 2.5V voltage regulator (L23) is shown, connected to the +2.5V_Nominal input. It provides power to the AVDD_ADC25, AVDD_REF25, AVDD_LAN, and AVDD_MOD pins. Decoupling capacitors C152 (2.2u/16V), C153 (100n/16V), C154 (100n/16V), C163 (100n/16V), and C165 (100n/16V) are connected to ground.
- Standby Power 3.3V:** A 3.3V standby power regulator (L23) is shown, connected to the 3.3Vstb input. It provides power to the AVDD_ALIVE, AVDD_DVI, AVDD_DMPL, and AVDD_MPLL pins. Decoupling capacitors C166 (100n/16V), C156 (2.2u/16V), C157 (100n/16V), and C423 (100n/16V) are connected to ground. A note indicates that C156 is close to AVDD_DVI.
- Normal Power 3.3V:** A 3.3V normal power regulator (L23) is shown, connected to the +3.3V input. It provides power to the VDDP, AVDD_AU, AVDD_LP, and AVDD_EAR pins. Decoupling capacitors C421 (2.2u/10V), C420 (100n/16V), C422 (100n/16V), and C167 (100n/16V) are connected to ground.
- DDR3 Power:** A 1.5V DDR3 power regulator (L23) is shown, connected to the +1.5V_DDR3 input. It provides power to the VDDIO_0 and VDDIO_1 pins. Decoupling capacitors C158 (2.2u/10V), C161 (2.2u/10V), C159 (100n/16V), C160 (100n/16V), and C164 (100n/16V) are connected to ground.

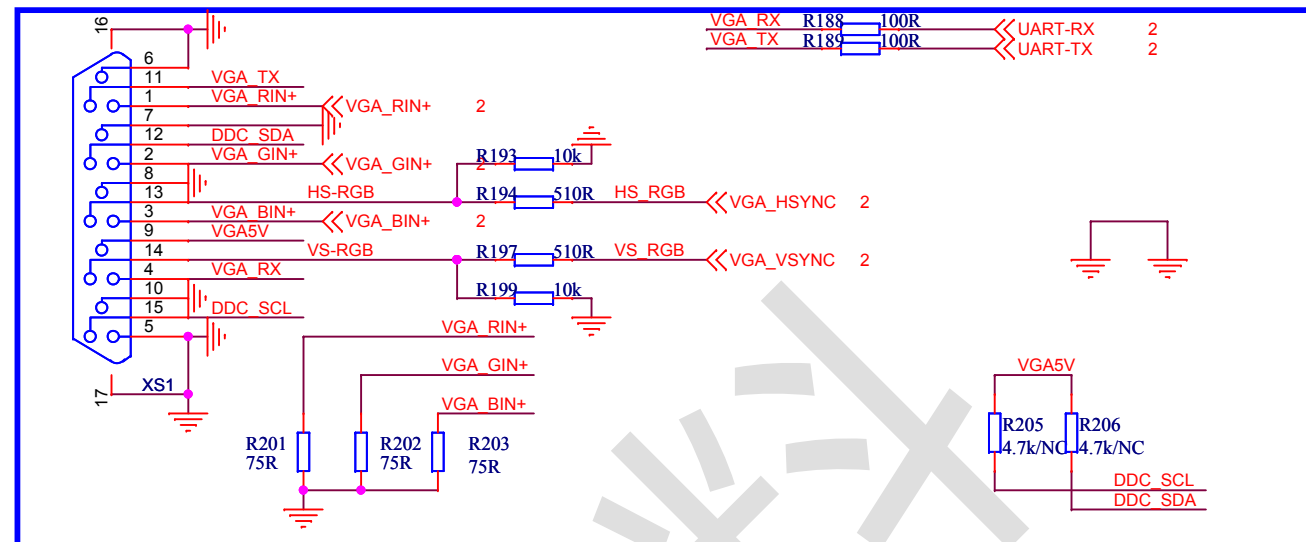
The diagram also shows the connection of the ATmega328P pins to the power supply rails, including VCC1V, VDDQ, VDD, AVDDL_MOD, AVDDL_USB, AVDDL1_ETH, AVDDL1P1_ADC, and AVDDL1P1_ADC.



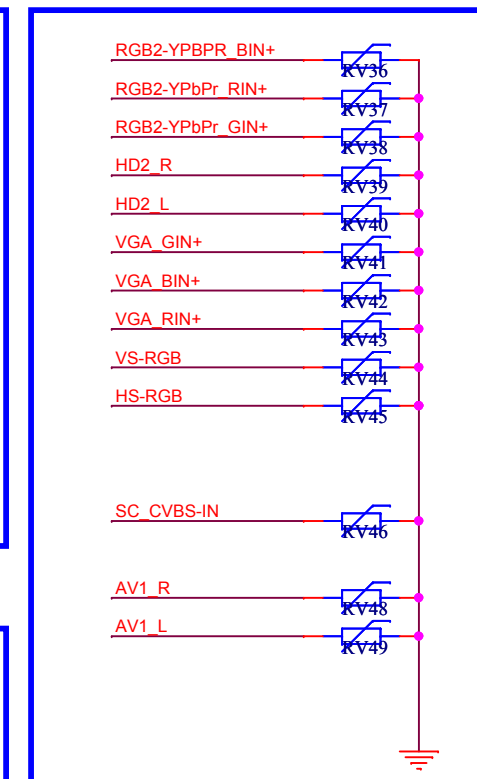
AV1 Input



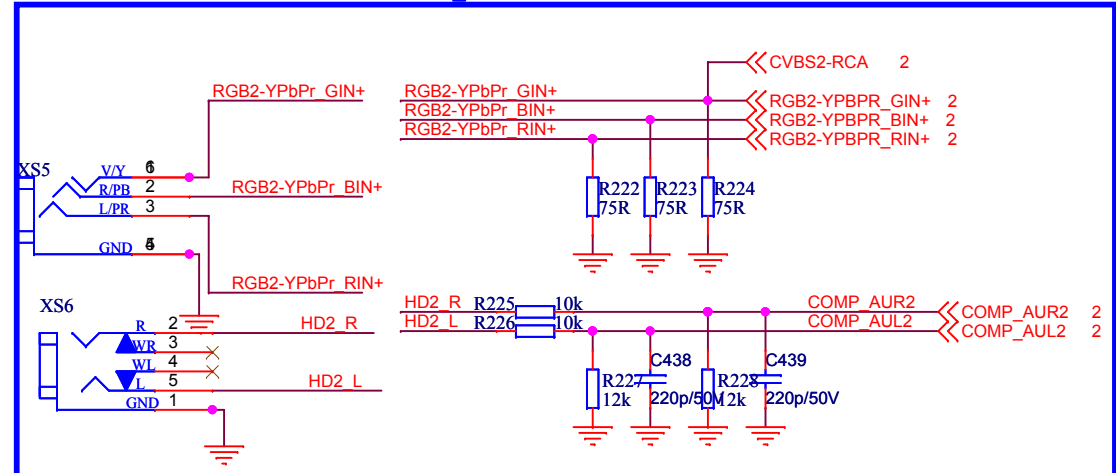
VGA



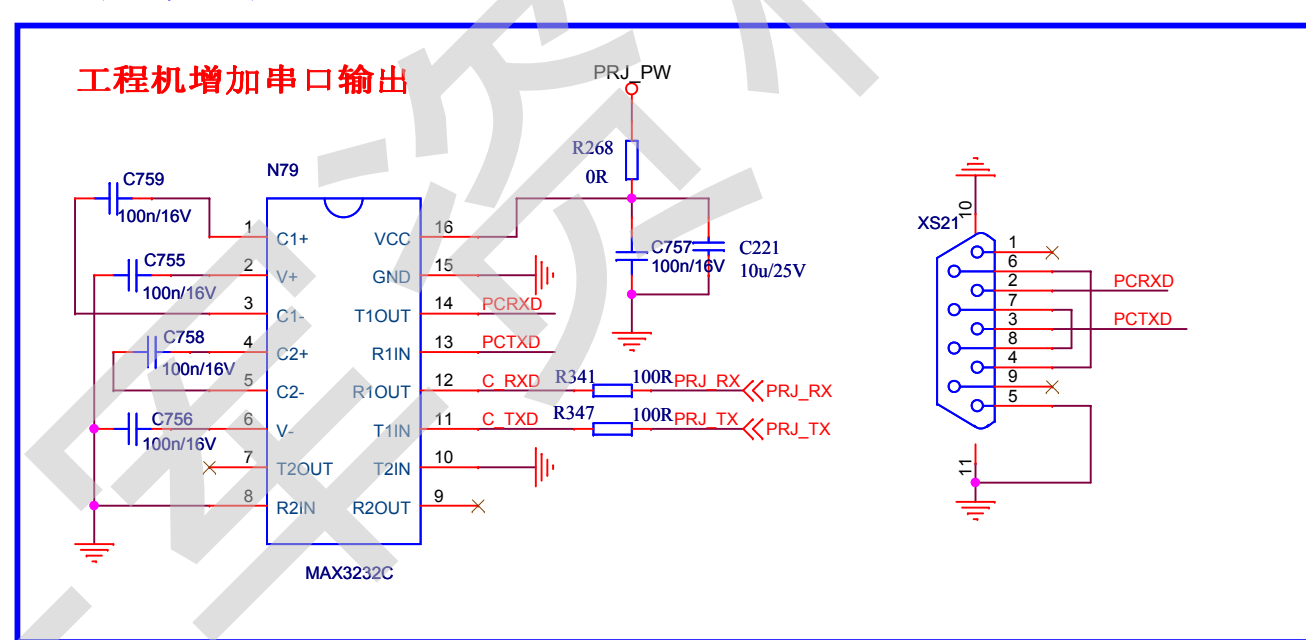
ESD



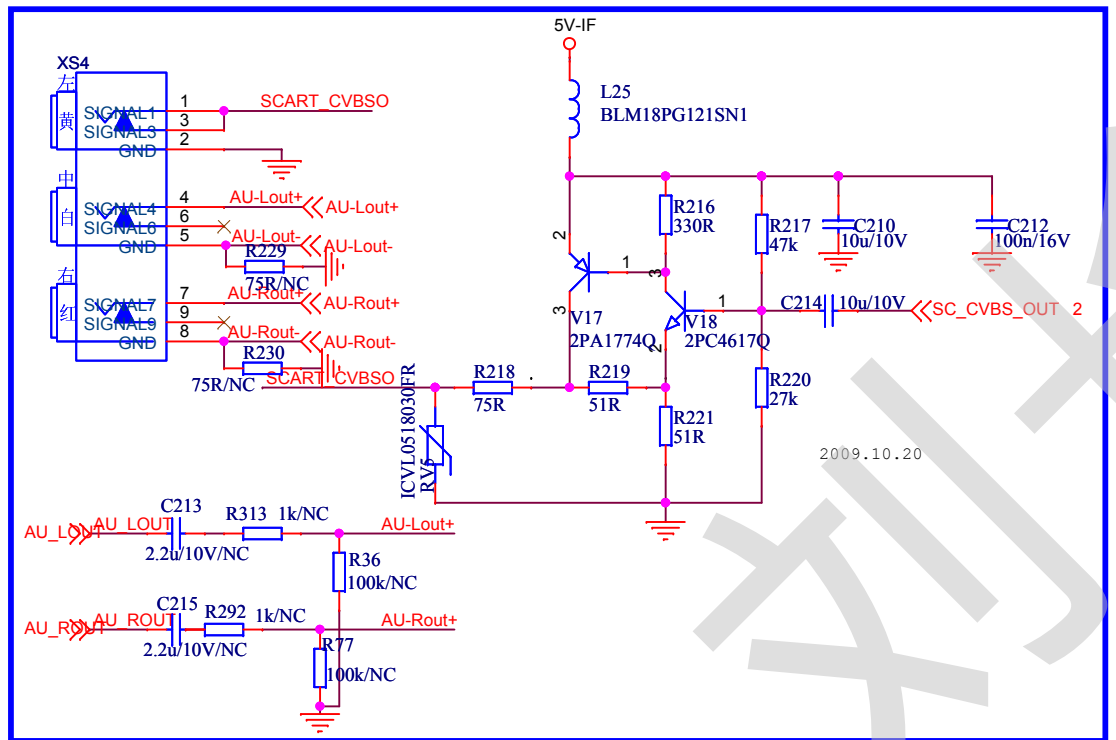
HDTV & AV2 Input



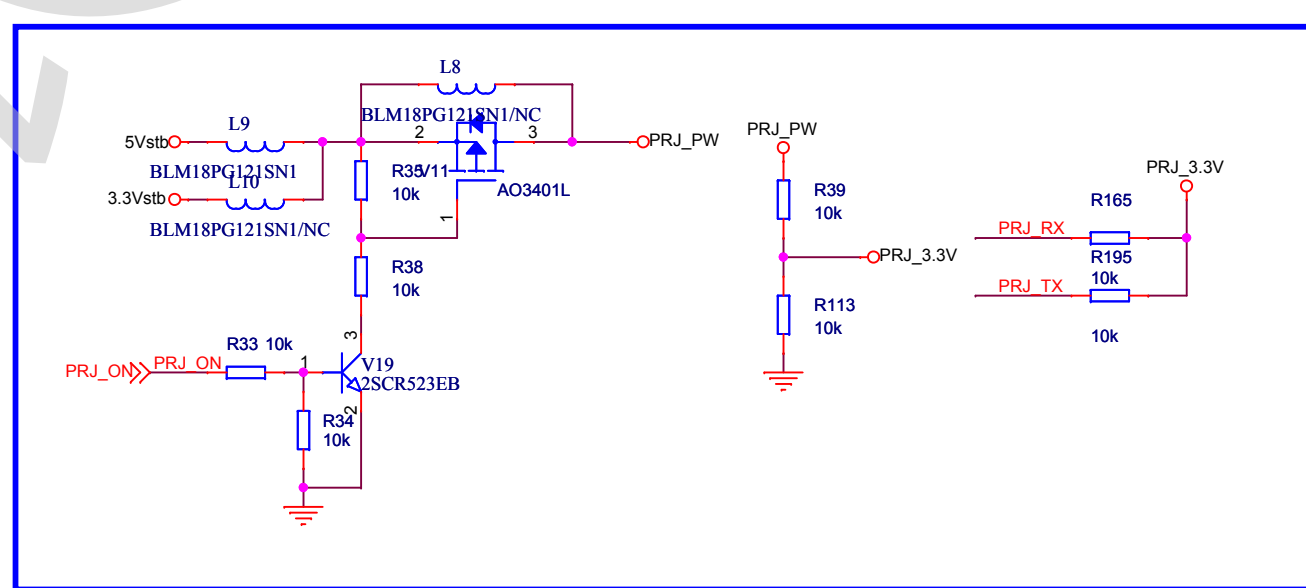
工程机串口



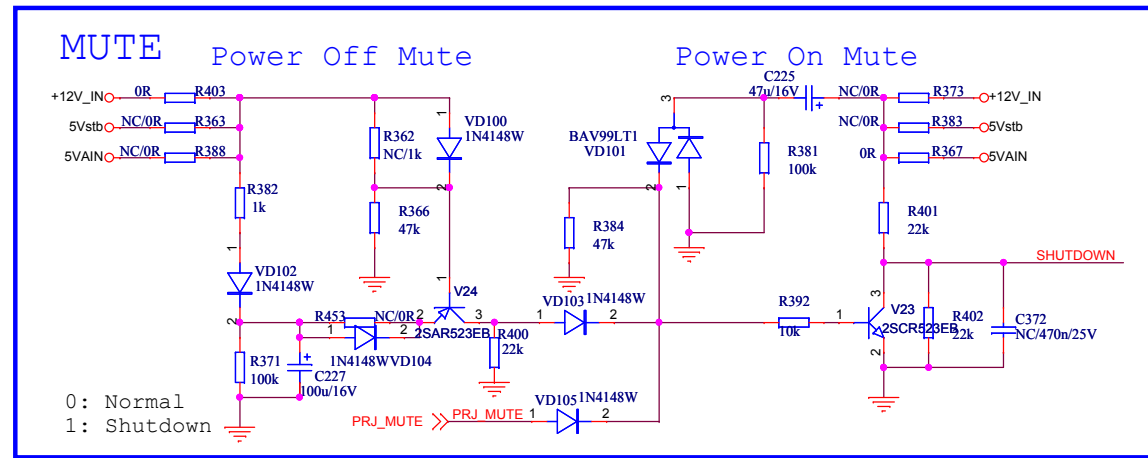
AV OUTPUT



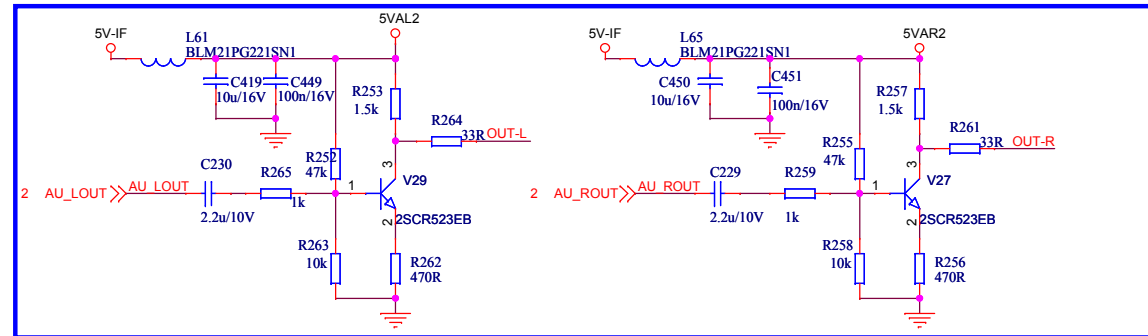
串口电源



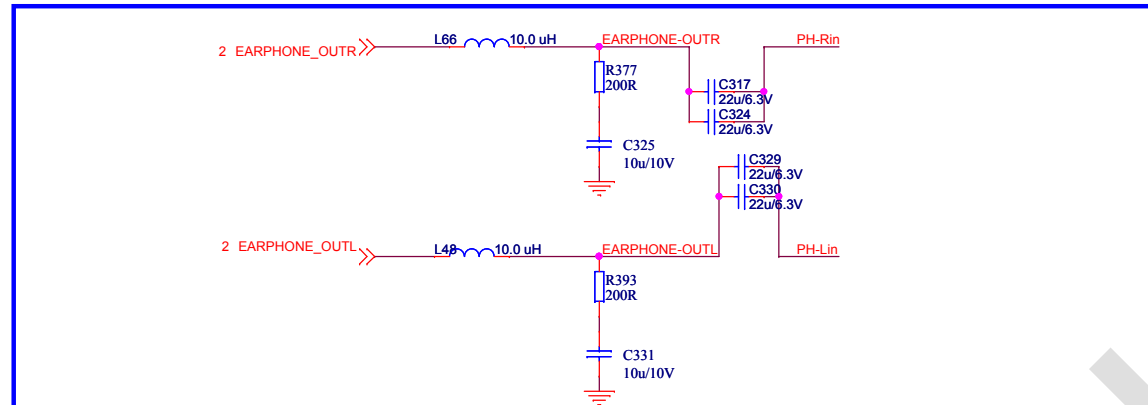
静音电路



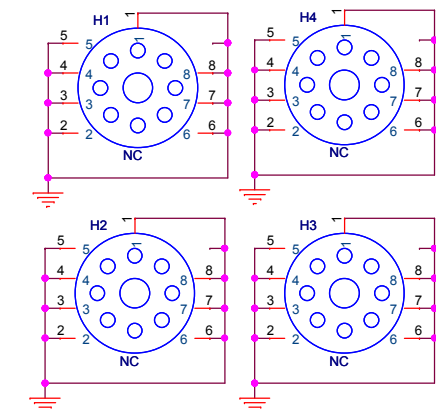
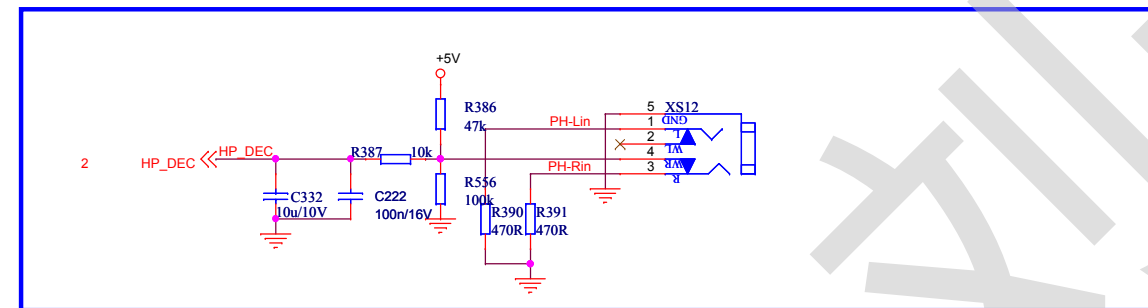
声音预放大



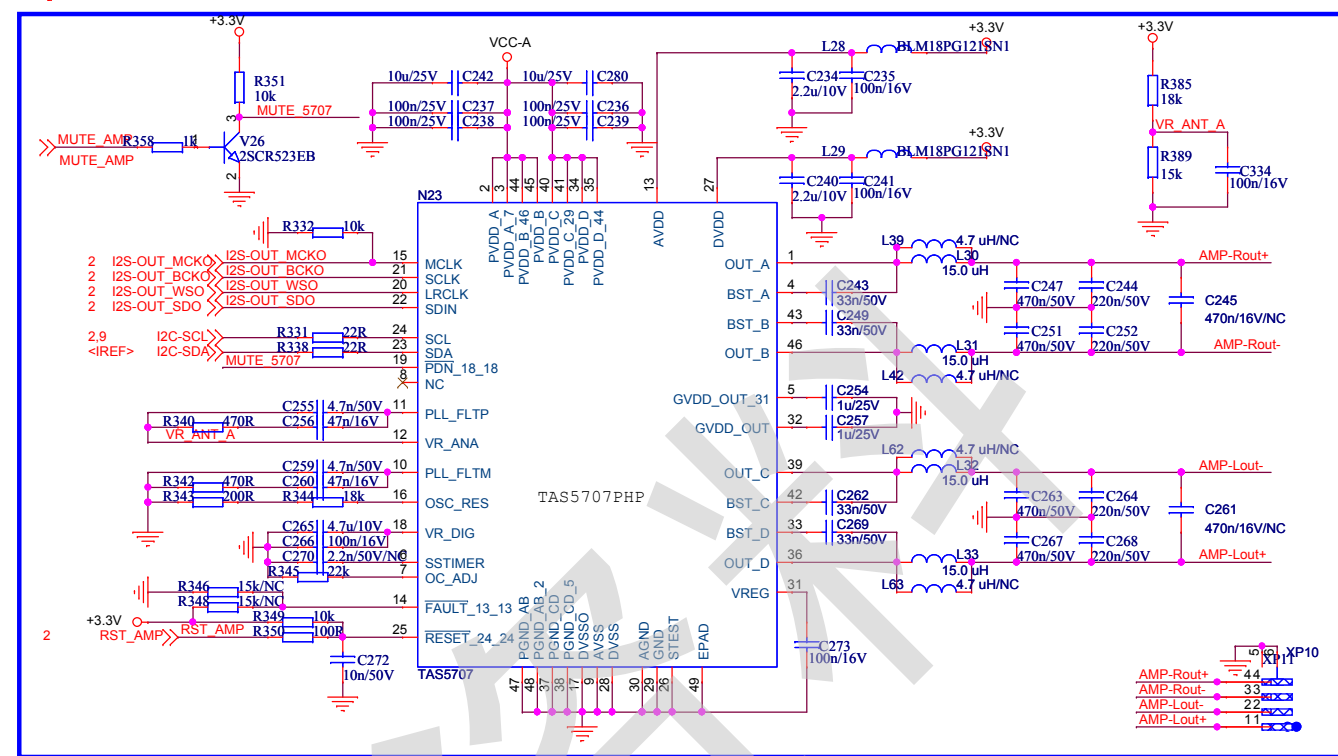
耳机滤波电路



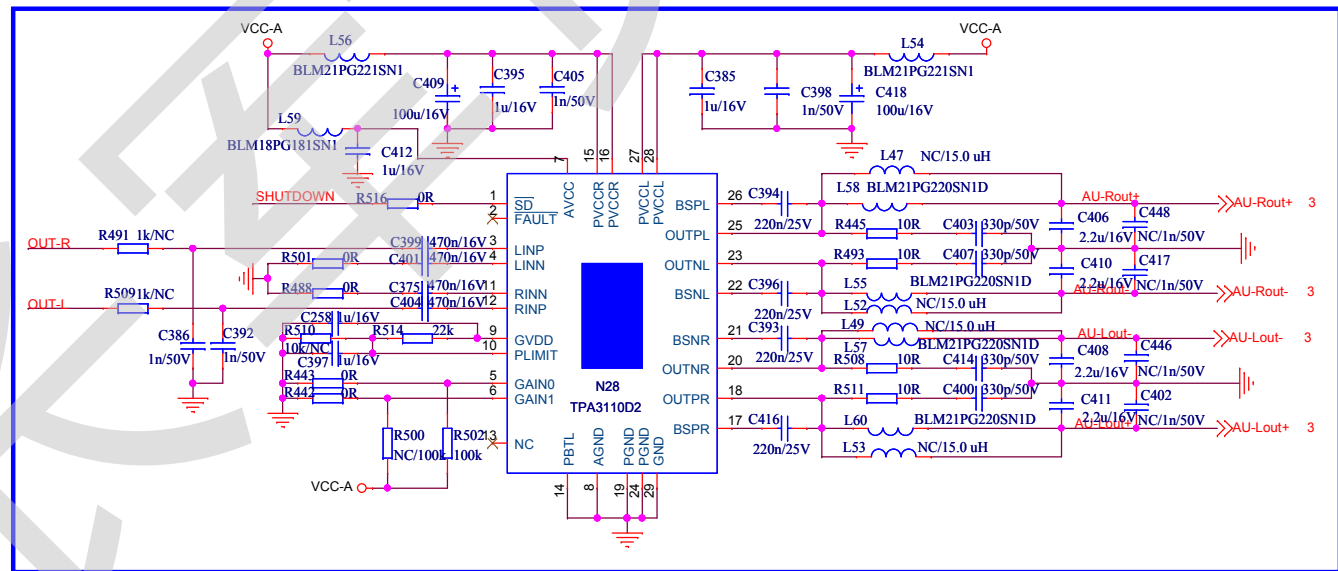
耳机输出端子



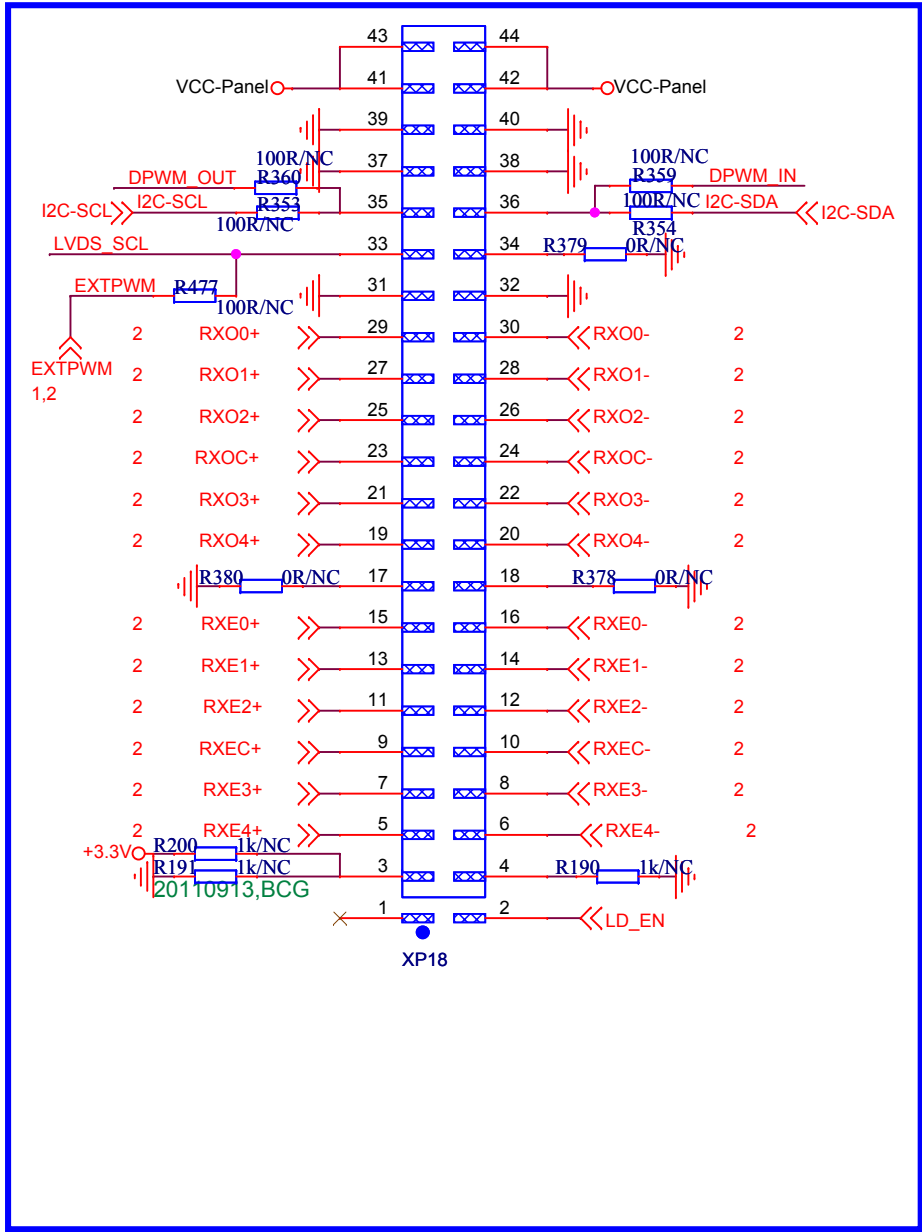
整机功放



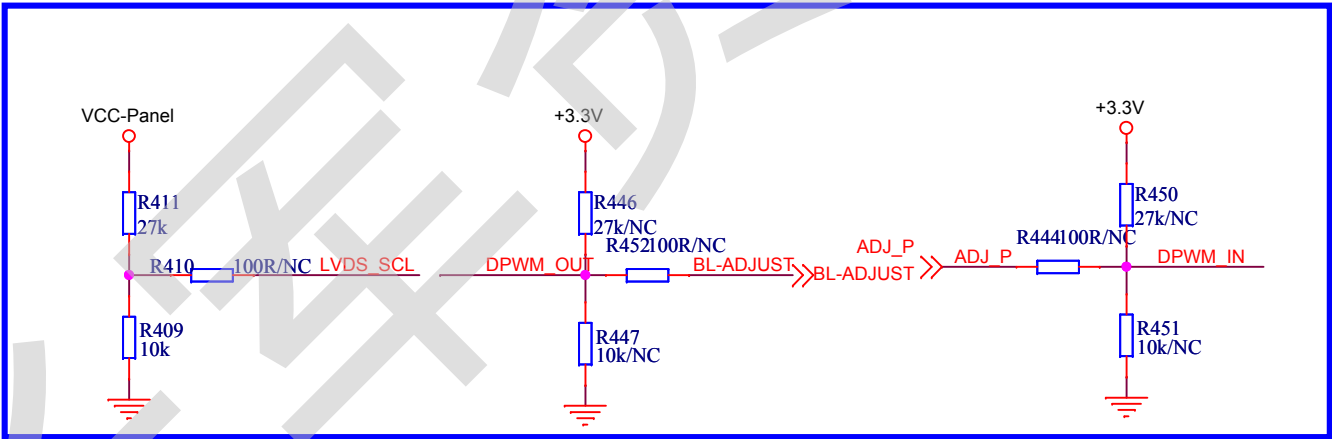
工程机功放



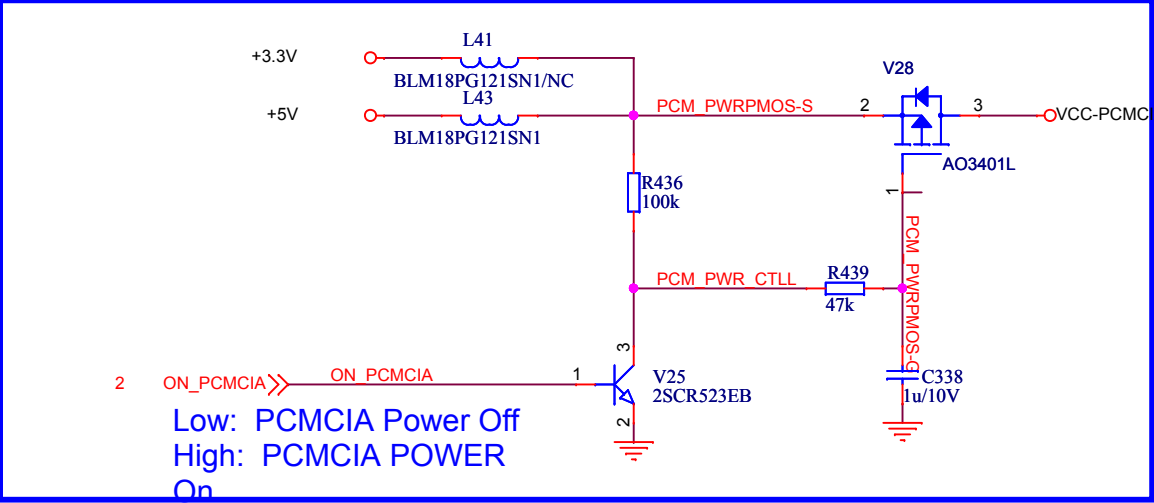
Title					MSD6I982BTX				
Size	Document Number								Rev
Custom	Audio Amplifier								1.0
Date:	Friday, September 07, 2012				Sheet	8	of	11	



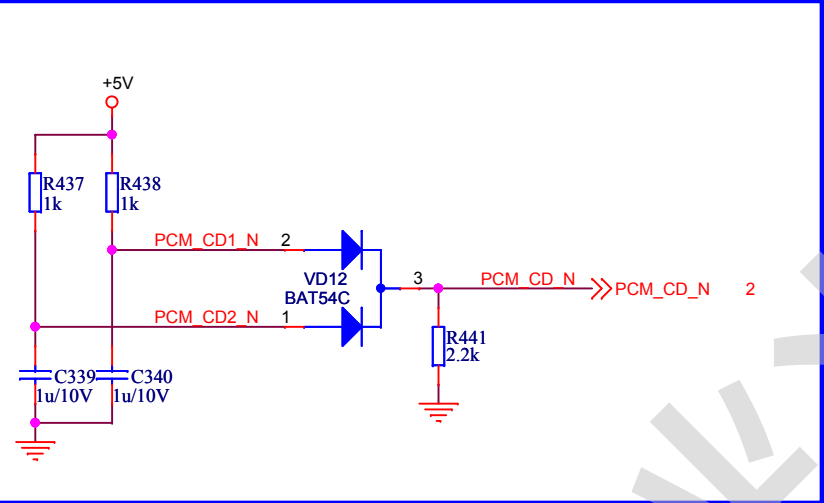
RRXE4+	RXE4+
RRXE4-	RXE4-
RRXE3+	RXE3+
RRXE3-	RXE3-
RRXEC+	RXEC+
RRXEC-	RXEC-
RRXE2+	RXE2+
RRXE2-	RXE2-
RRXE1+	RXE1+
RRXE1-	RXE1-
RRXE0+	RXE0+
RRXE0-	RXE0-
RRXO4+	RXO4+
RRXO4-	RXO4-
RRXO3+	RXO3+
RRXO3-	RXO3-
RRXOC+	RXOC+
RRXOC-	RXOC-
RRXO2+	RXO2+
RRXO2-	RXO2-
RRXO1+	RXO1+
RRXO1-	RXO1-
RRXO0+	RXO0+
RRXO0-	RXO0-



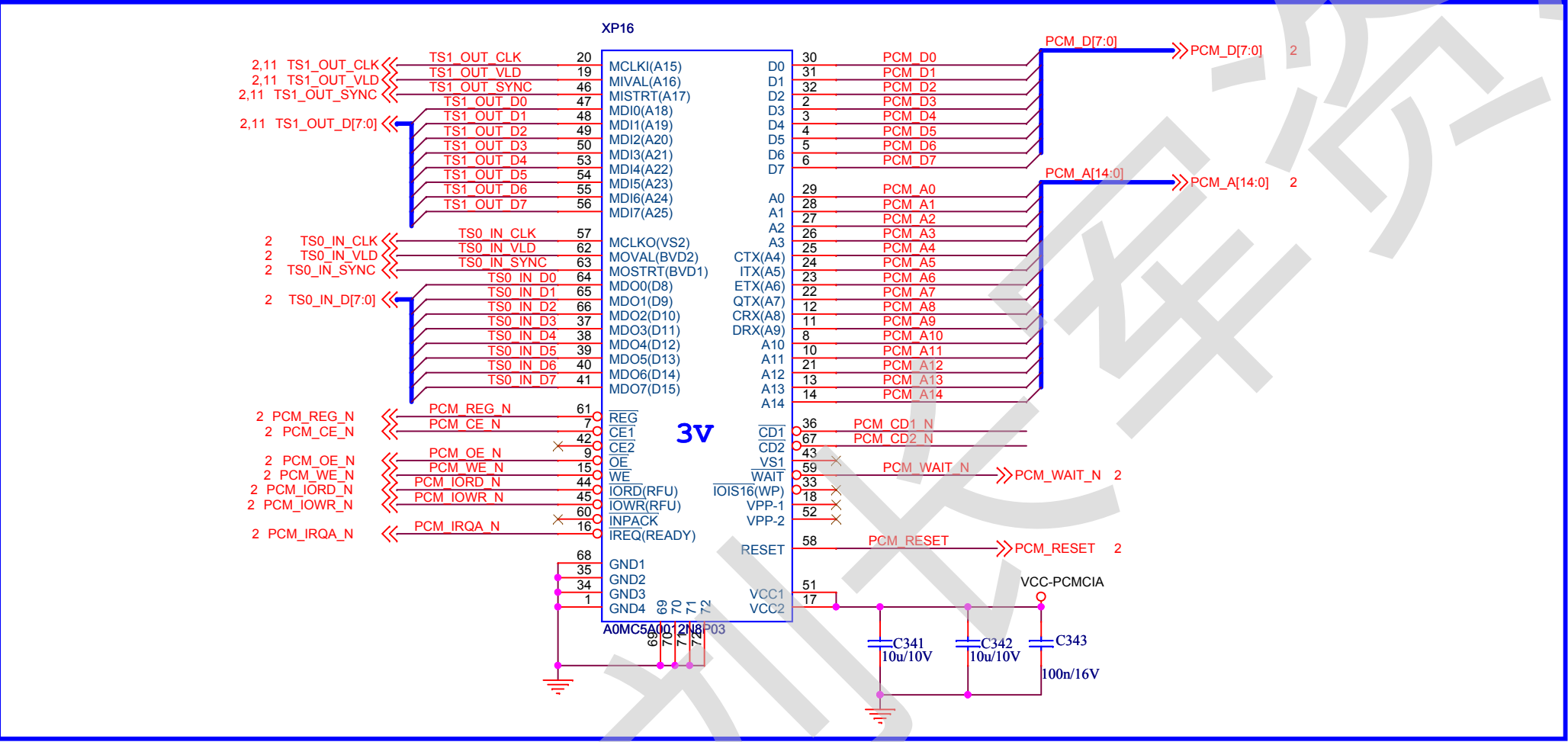
PCMCIA Power Control



PCMCIA Card Detect



PCMCIA Card



1



1