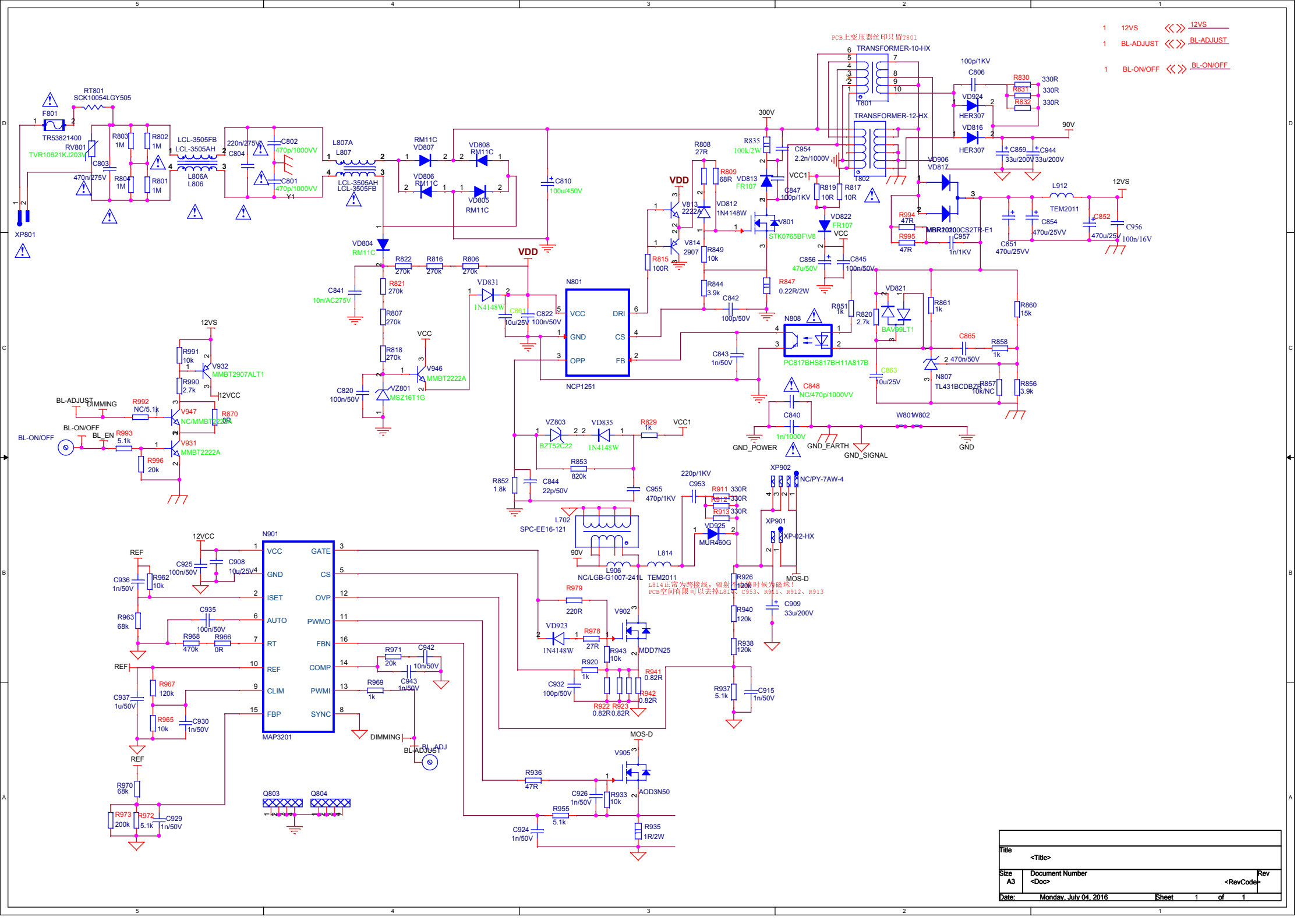


date	change list
	<div>4.21标注NC N32,L9 TCON 电路检查 IR wifi 插座改分开</div> <div>4.24 新APN1.2修改DCDC 4.28分量1分3端子，耳机细端子，预留uart耳机</div> <div>IR，key，wifi合一插座。加工程机预留。</div> <div>5.9 发板A 5.13 1. 删除 ，BT预留 2. tuner改VIF： R453，R456改L139,,L140，加C312,C313 3. L39，L59改</div>

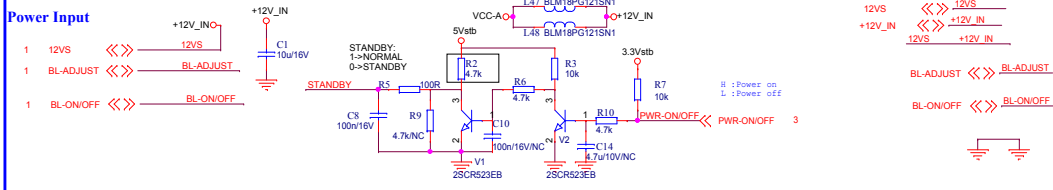
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MSxxx			
Size	Document Number		Rev
A	Revision History		1.0
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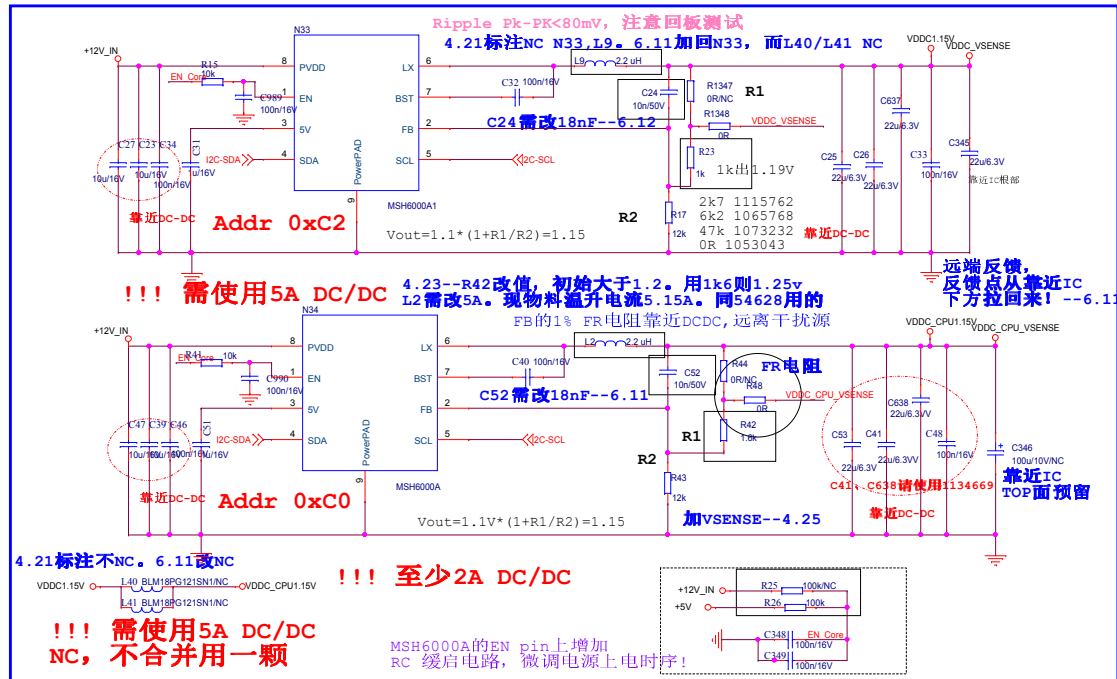
1 12VS <<>> 12VS
1 BL-ADJUST <<>> BL-ADJUST
1 BL-ON/OFF <<>> BL-ON/OFF

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Size	A3	Document Number	<Doc>
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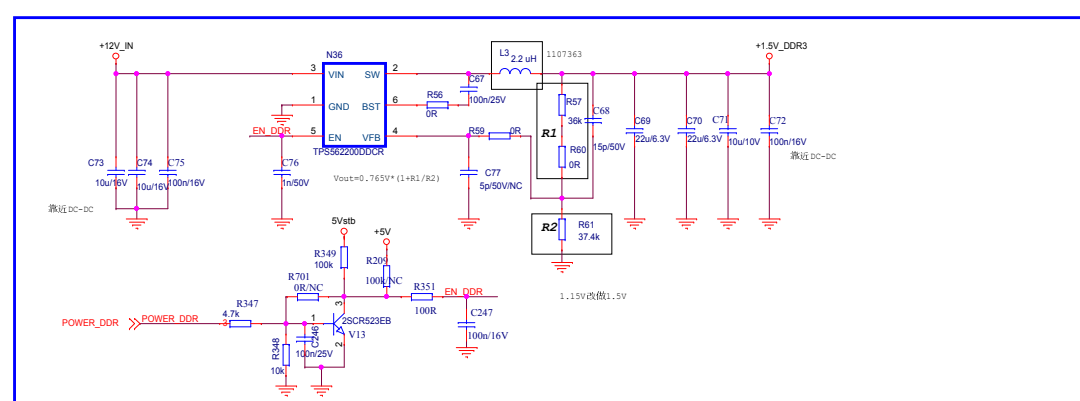
Power Input



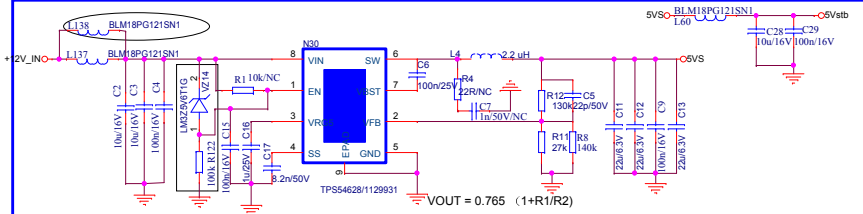
Core Power



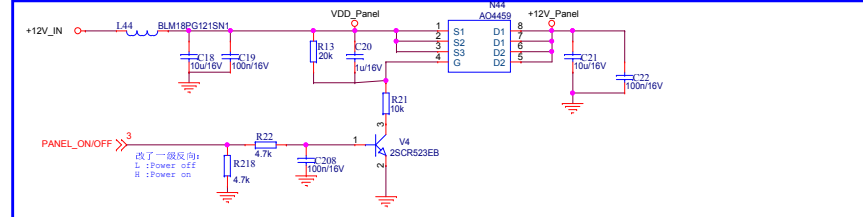
DDR_POWER



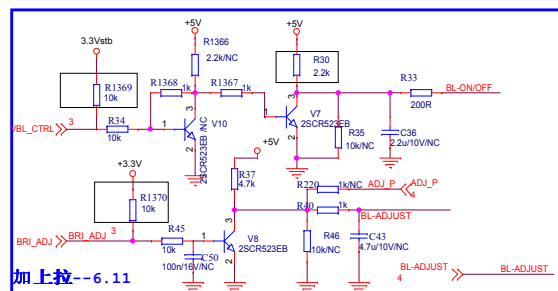
+5V FOR SYS 主5V按照5.15V设计, 考虑USB应用



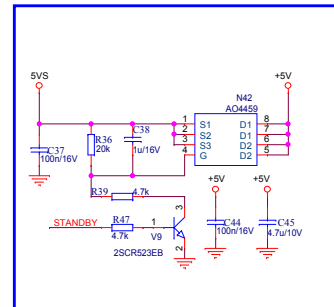
Power for Panel



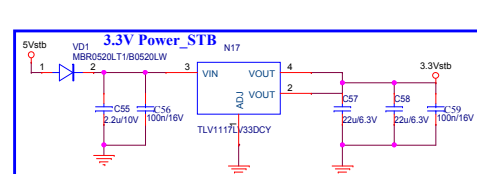
背光开关和调光



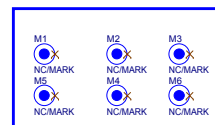
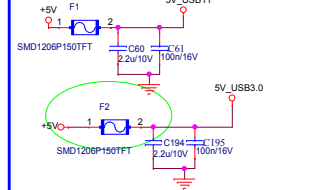
+5V POWER



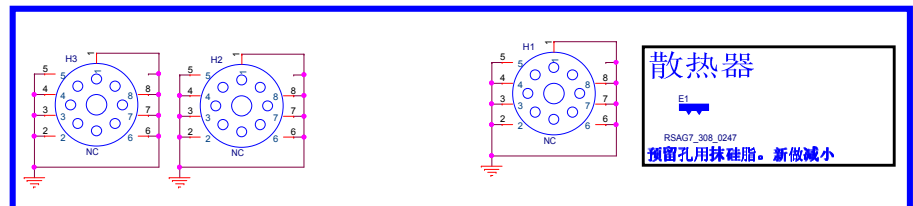
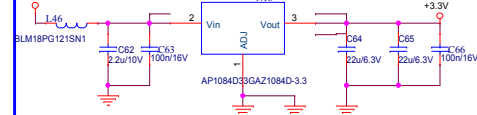
LDO



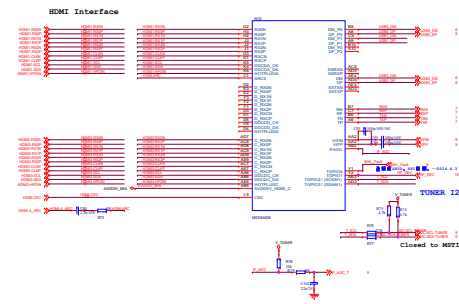
USB POWER



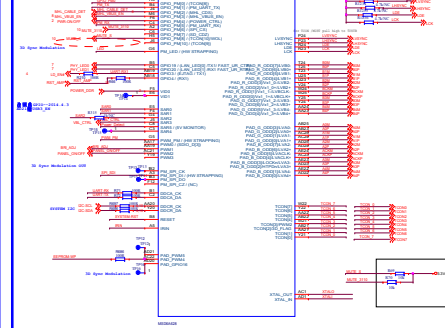
+5V 3.3V Power_Normal



HDMI



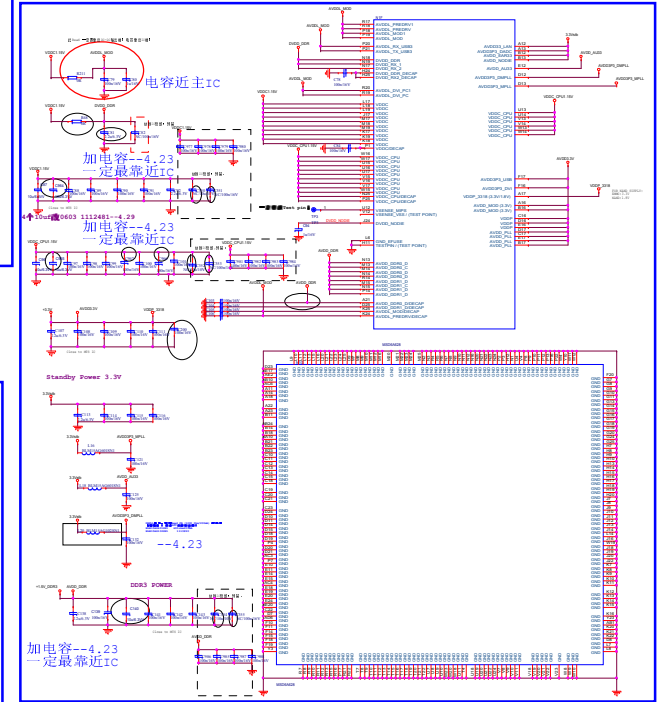
GPIO Pull Up/Down



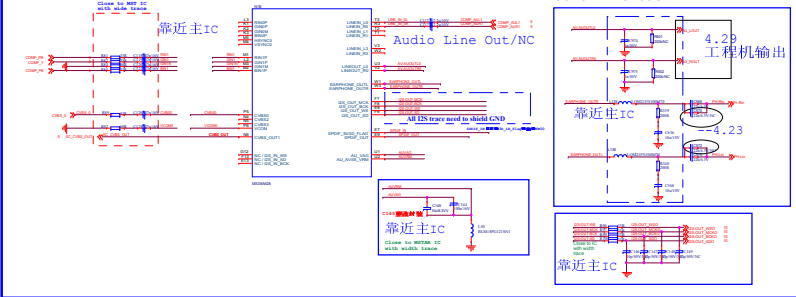
PCMCIA & TS



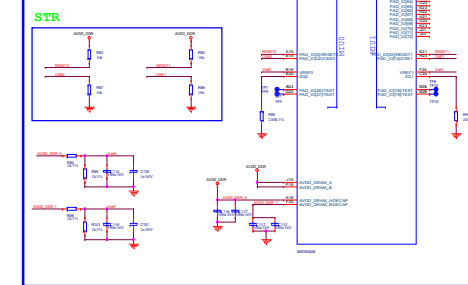
Power interface



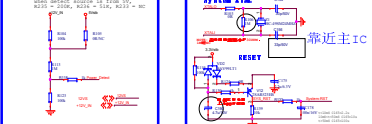
SOURCE INPUT



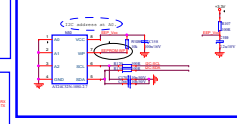
MIU INTERFACE



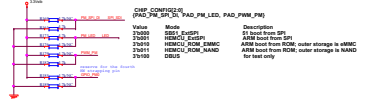
RESET&Crystal



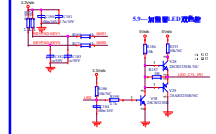
EEPROM



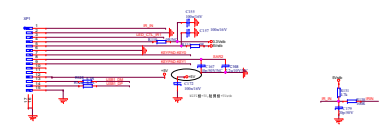
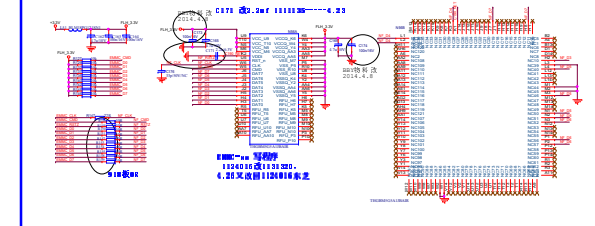
Mode Selection



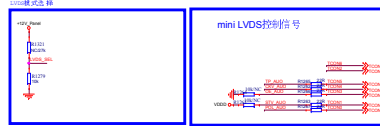
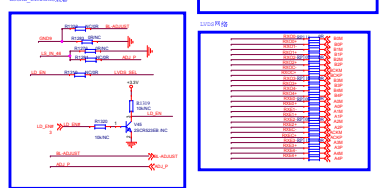
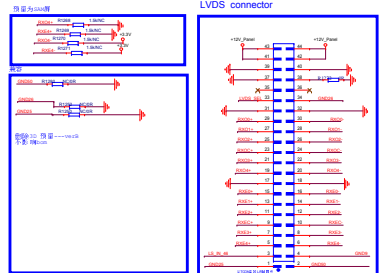
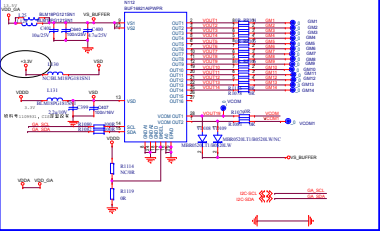
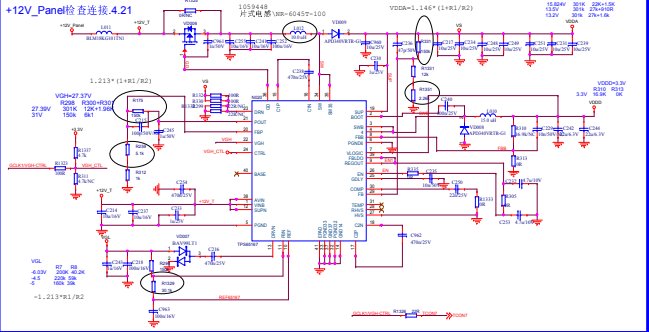
LEDKEY



KEY & IR & RF IR

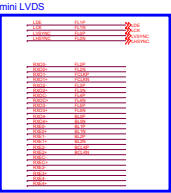
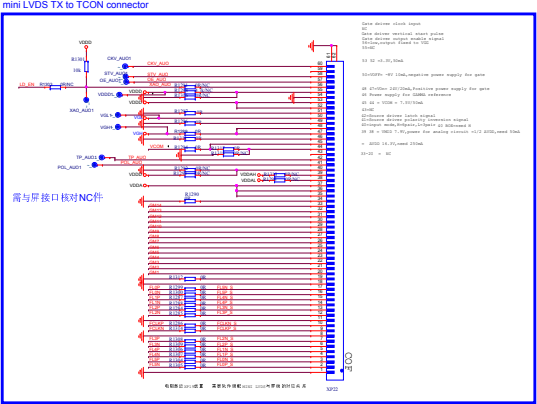
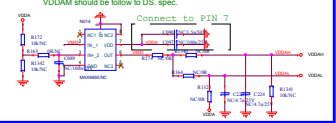
eMMC

TCON POWER



COM产生电路/NC G1625 4.21

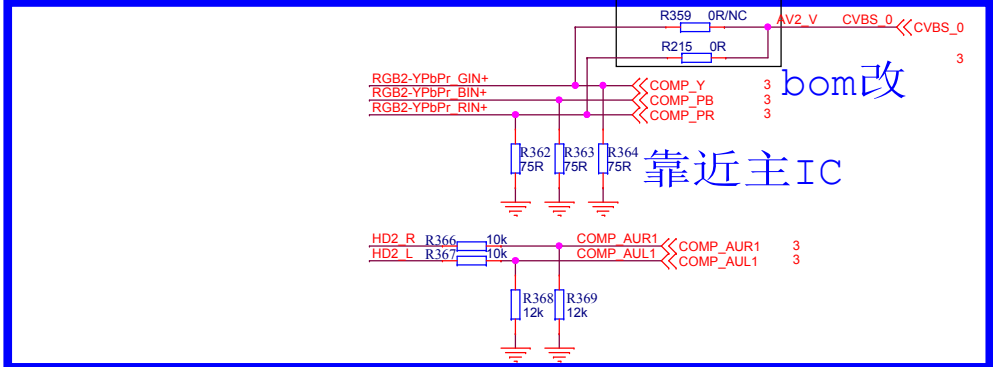
保留 VDDAH / VDDAL POWER CIRCUIT



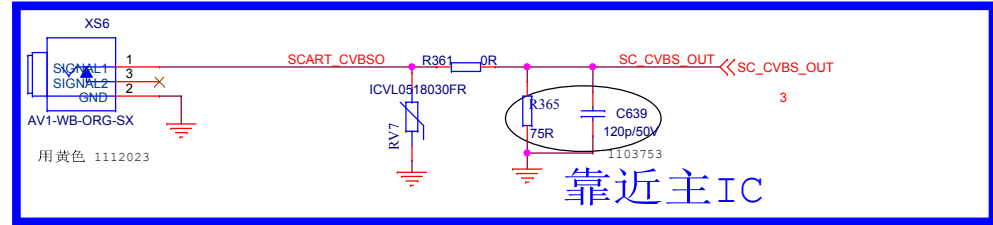
AV_YPbPr INPUT/工程机功放AUout



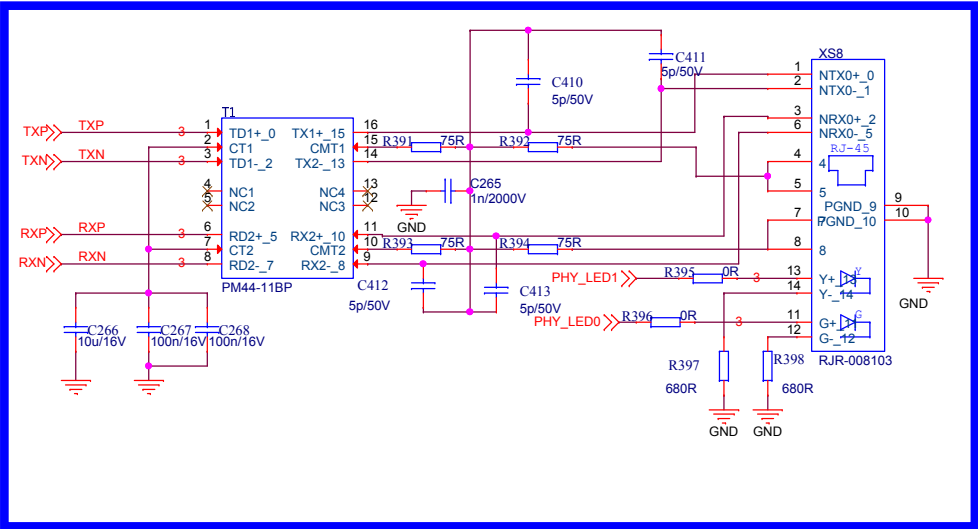
AV_YPbPr L/R INPUT



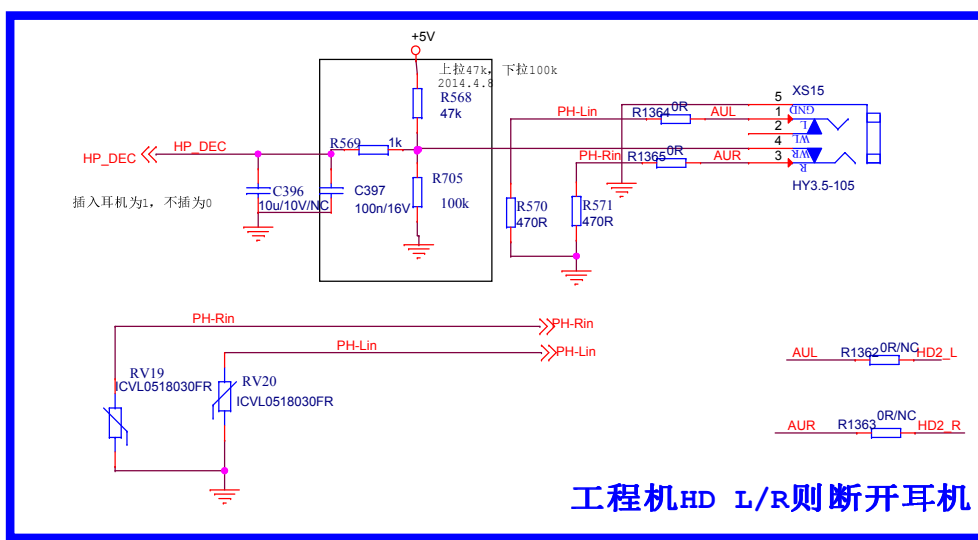
AV OUTPUT



网口

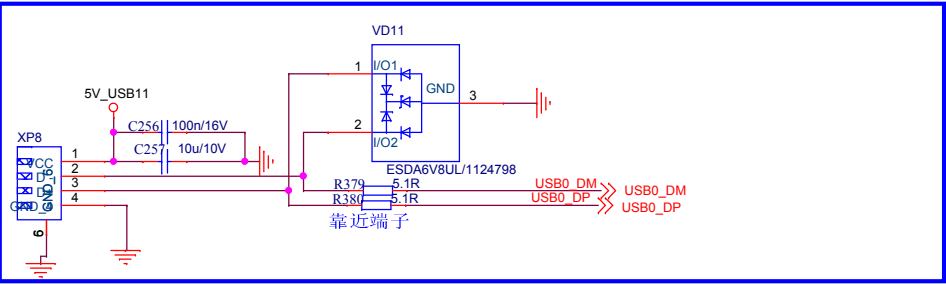


耳机输出/音频输出/工程机HD音频入



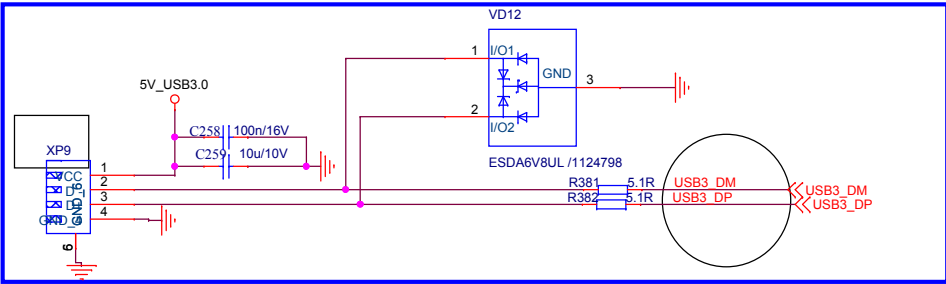
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USB1



USB2

5.1欧姆电阻靠近USB端子，较小EOS。



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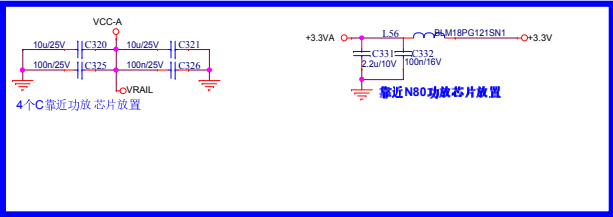
The schematic diagram illustrates the HDMI interface circuit. It features a 200RNC resistor connected between the HDMI1_CEC pin of the ESDC0402 and the HDMI-CEC pin of the HDMI-SDA. The HDMI1_RX1N, HDMI1_RX1P, HDMI1_CLKN, and HDMI1_CLKP pins of the ESDC0402 are connected to the HDMI3_RX2N, HDMI3_RX2P, HDMI3_RX0N, and HDMI3_RX0P pins of the ESDC0402, respectively. The circuit is powered by a 3.4V supply.

Figure 1: Pin connection diagram of the DCT1001UB1. The diagram shows the connection of the DCT1001UB1 chip to an HDMI-A ARC source. The chip's pins are numbered 1 to 20. The connections are as follows: Pin 1 (GND) to GND; Pin 2 (GND) to GND; Pin 3 (HDMI-A ARC) to HDMI-A ARC; Pin 4 (DATA1-) to DATA1-; Pin 5 (DATA1+) to DATA1+; Pin 6 (DATA0-) to DATA0-; Pin 7 (DATA0+) to DATA0+; Pin 8 (DATA0-) to DATA0-; Pin 9 (DATA0+) to DATA0+; Pin 10 (DATA0-) to DATA0-; Pin 11 (DATA0+) to DATA0+; Pin 12 (DATA0-) to DATA0-; Pin 13 (DATA0+) to DATA0+; Pin 14 (DATA0-) to DATA0-; Pin 15 (DATA0+) to DATA0+; Pin 16 (DATA0-) to DATA0-; Pin 17 (DATA0+) to DATA0+; Pin 18 (DATA0-) to DATA0-; Pin 19 (DATA0+) to DATA0+; Pin 20 (DATA0-) to DATA0-.

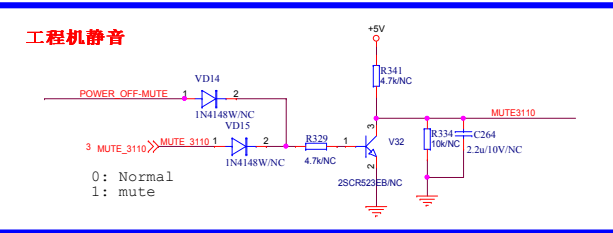
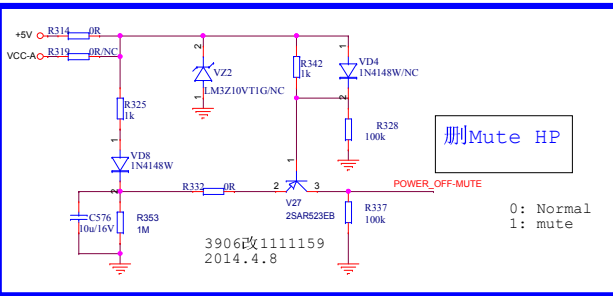
The schematic diagram illustrates the MHL to HDMI converter circuit. It features two main ICs: the TPS2065C2 and the TPS2065C2-2. The MHL cable is connected to the MHL_CD_SENSE, MHL_Cable_DET, and MHL_Fault pins. The HDMI port is connected to the HDMI3-5V, VBUS, and MHL_VBUS_EN pins. The circuit is powered by a 5V_MHL supply and an AVDD5V_MHL supply. The output is connected to the HDMI3-5V, VBUS, and MHL_VBUS_EN pins. The circuit is labeled "5.16- 改用R441" and "bom减件".

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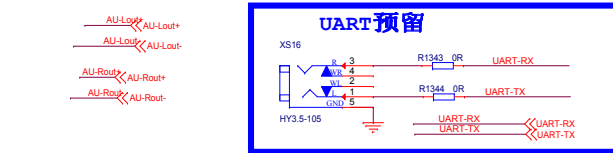
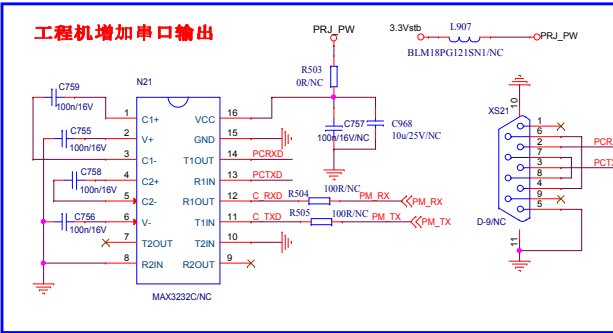
电源



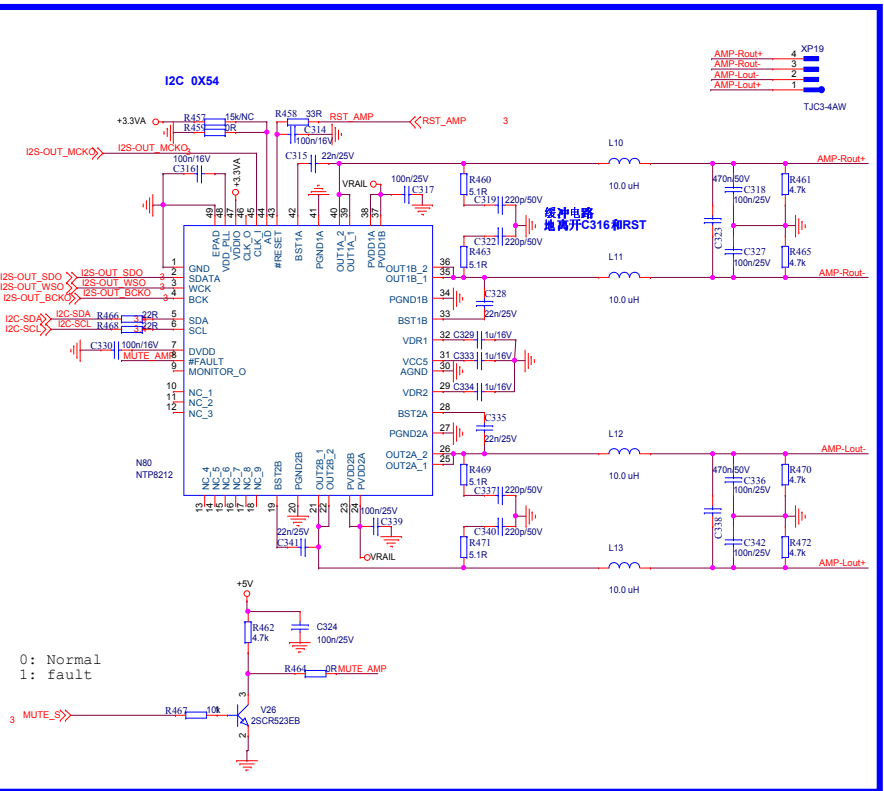
Mute



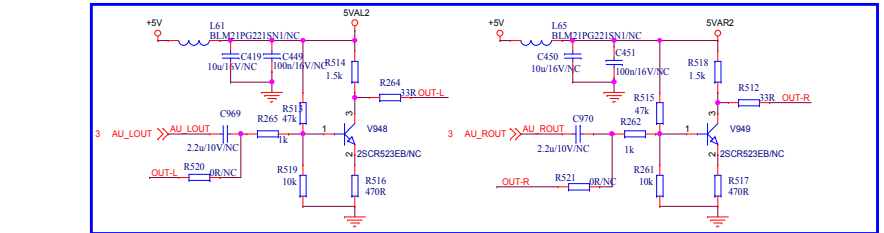
工程机串口-4.29



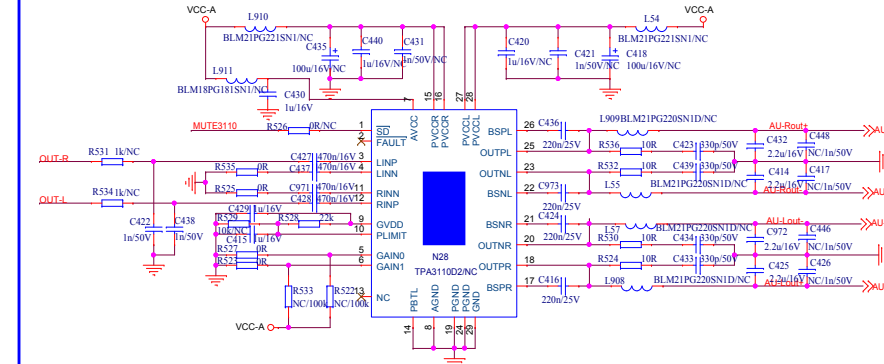
功放NT8212



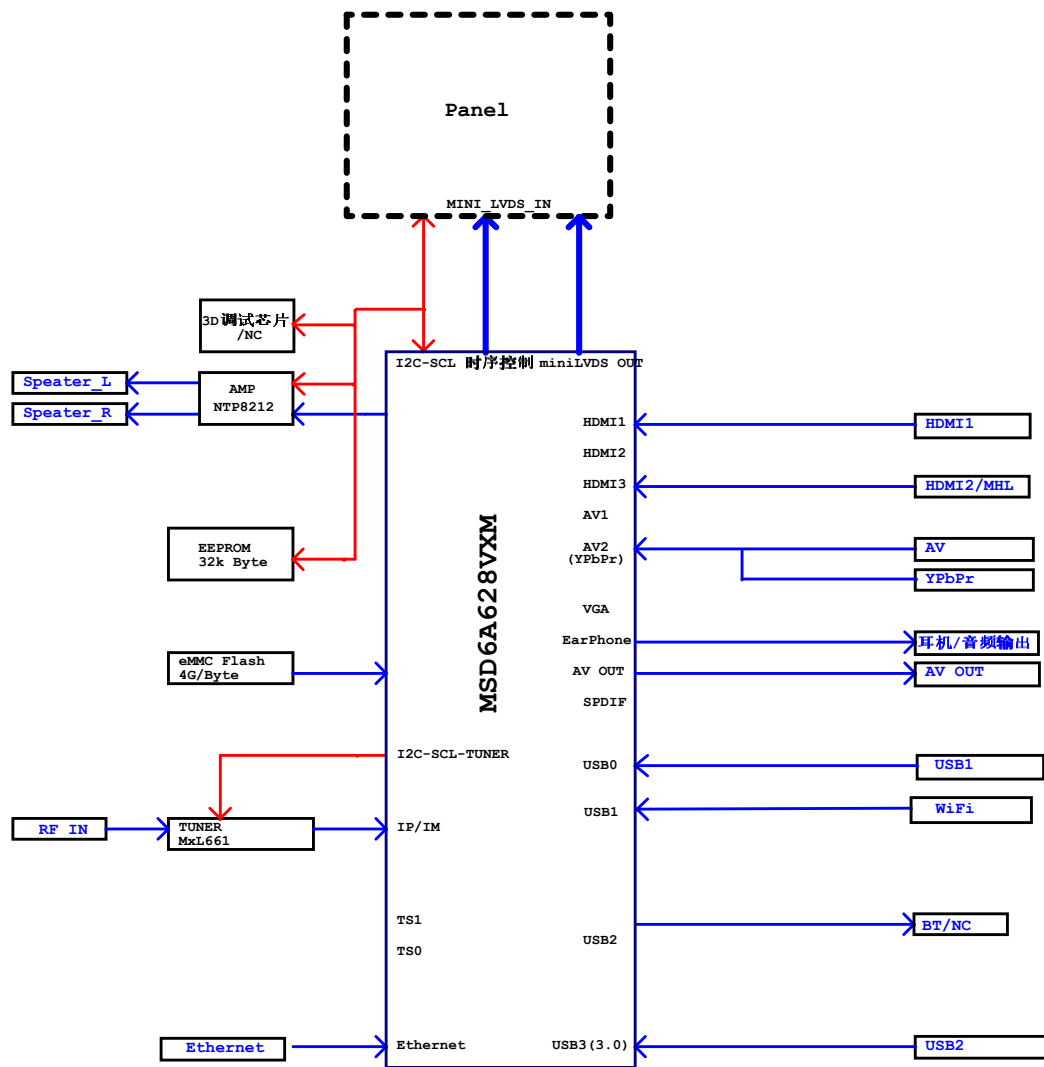
工程机声音预放大



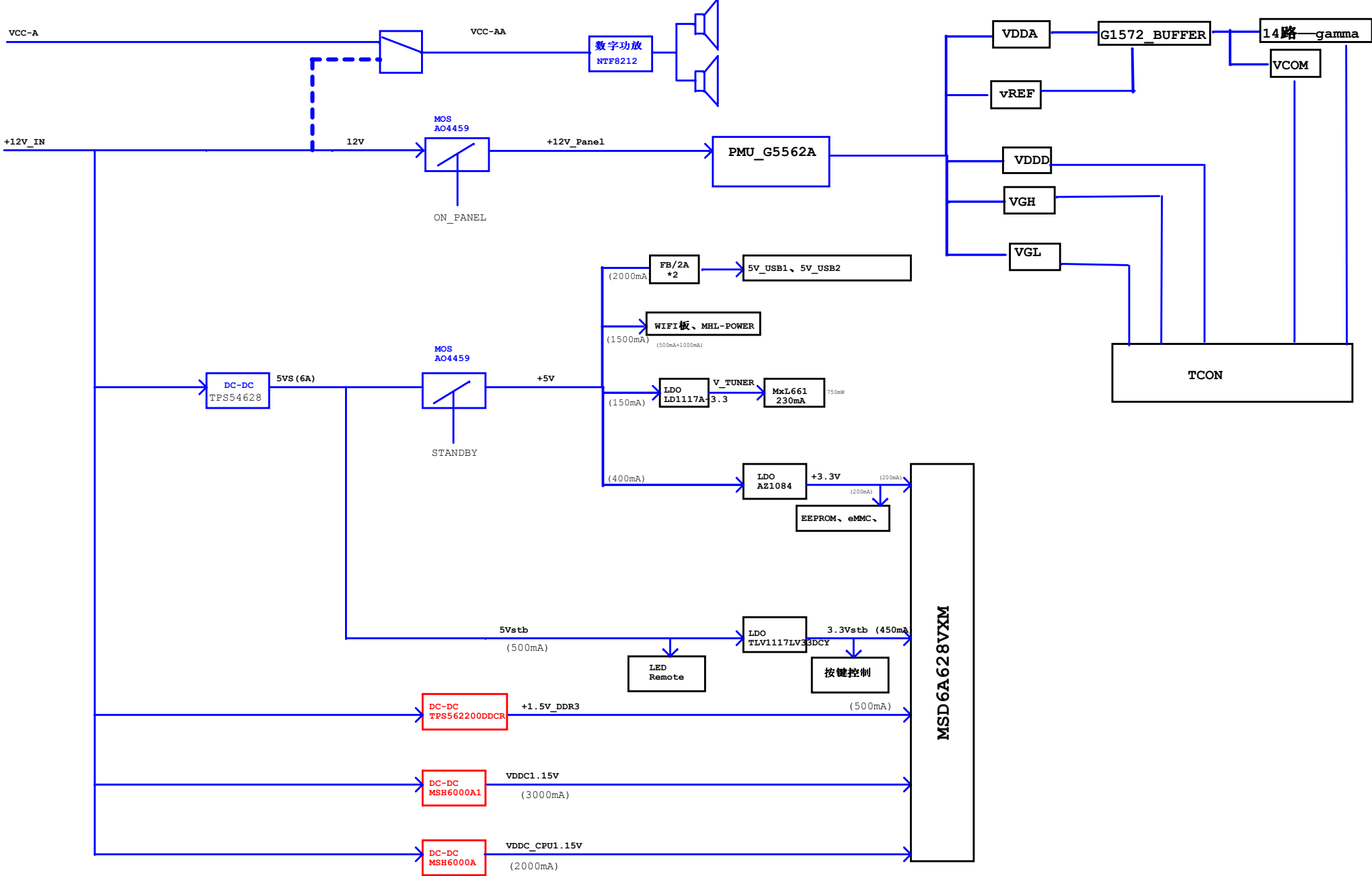
工程机功放



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MSD6A628		
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APR要求：VDDC电压不低于1.15V，也不高于1.236V。

VDD10	VDD11	VDDC_CPU1
B	B	1.28V
B	L	1.28V
L	B	1.28V
L	L	1.28V
B	B	1.15V
B	L	1.15V
L	B	1.15V
L	L	1.15V