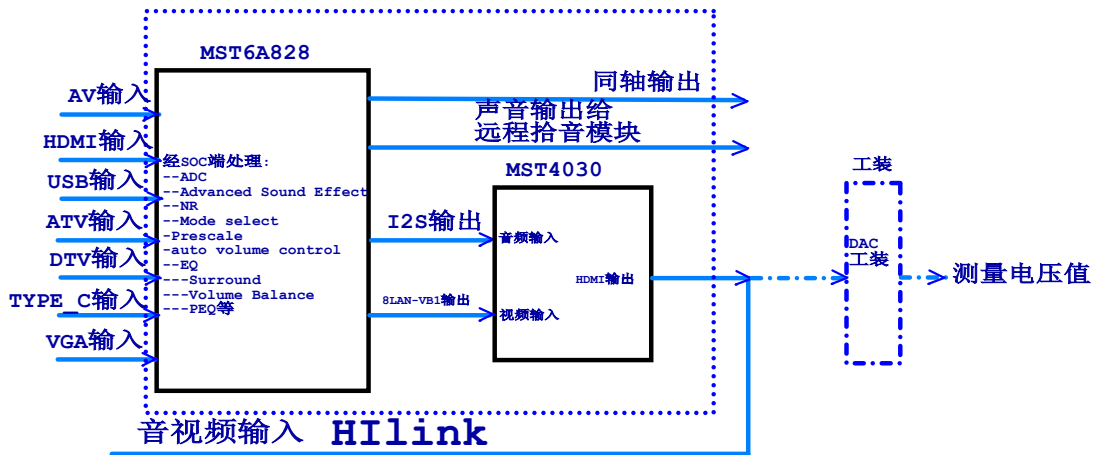
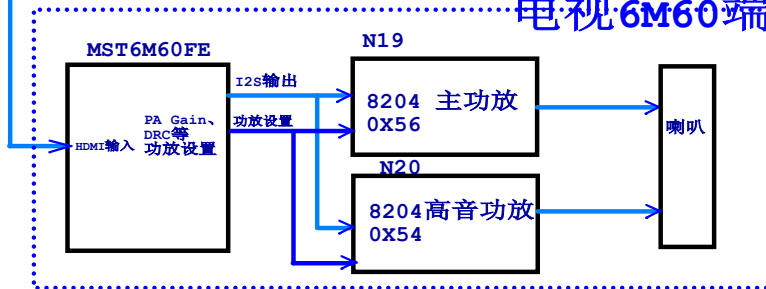


盒子端

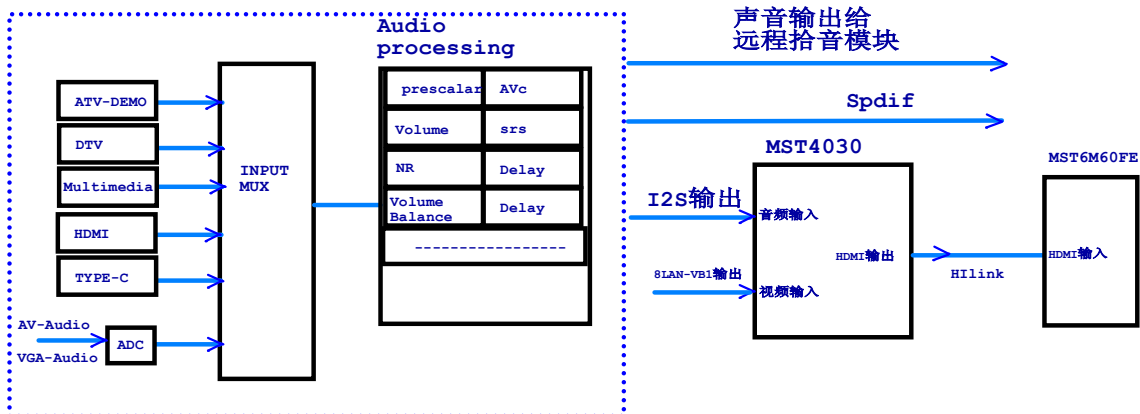


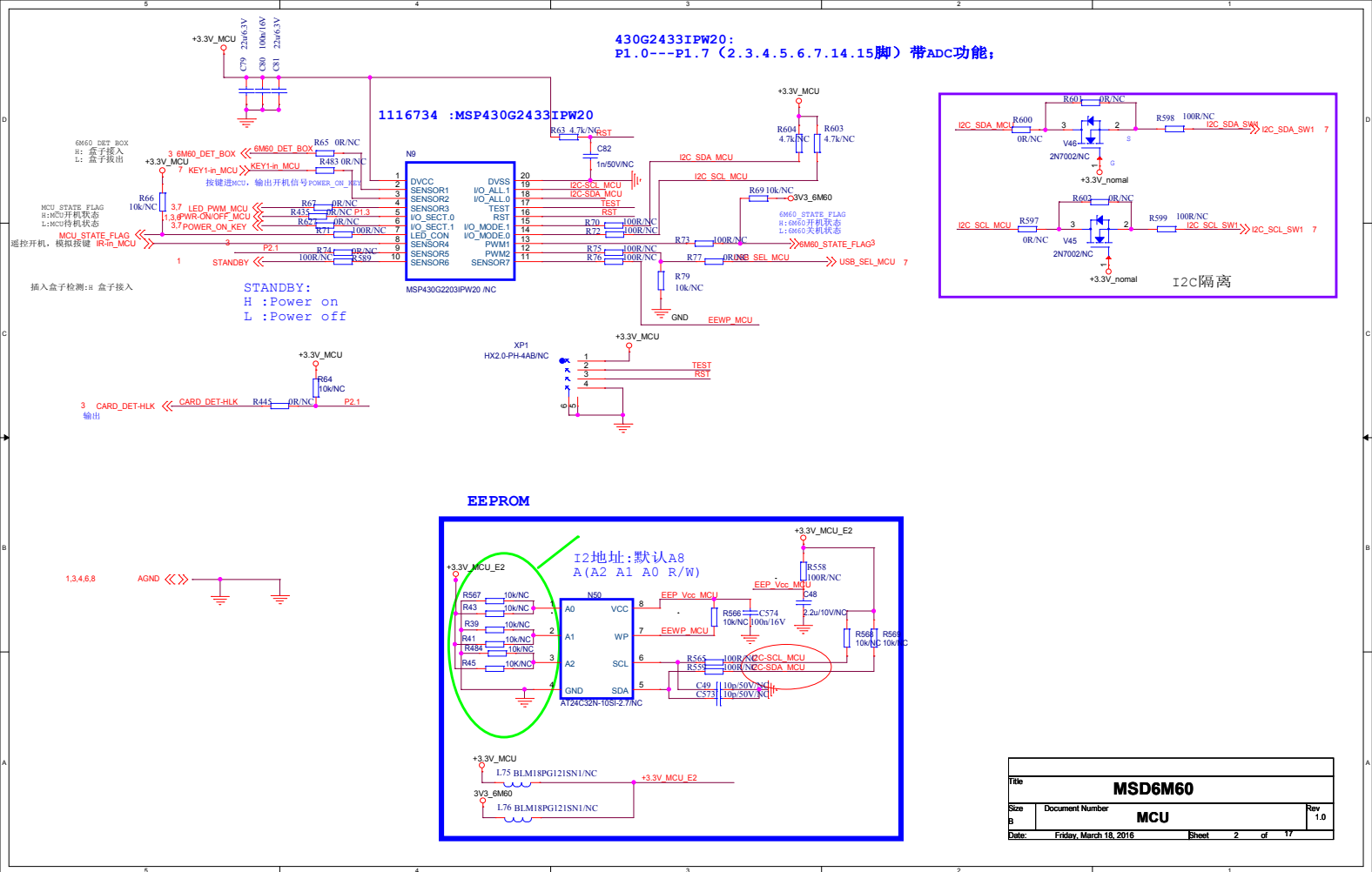
音视频输入 HILink

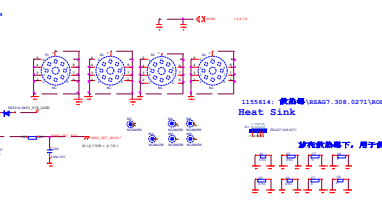
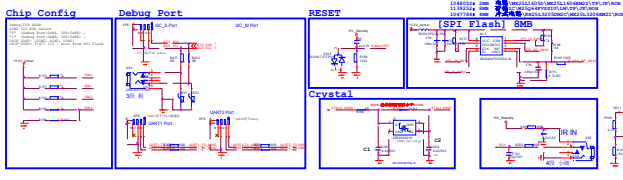
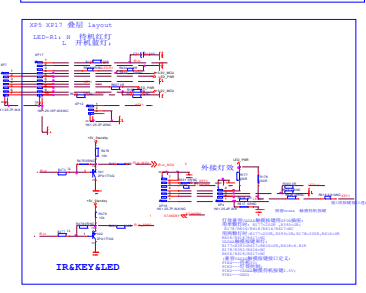
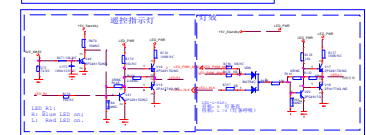
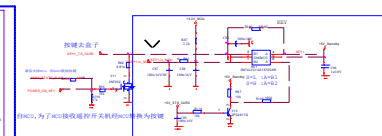
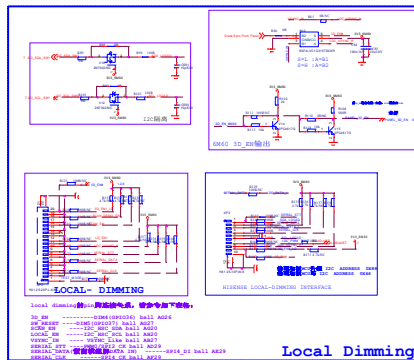
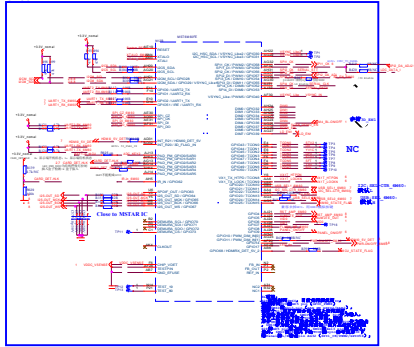
电视6M60端

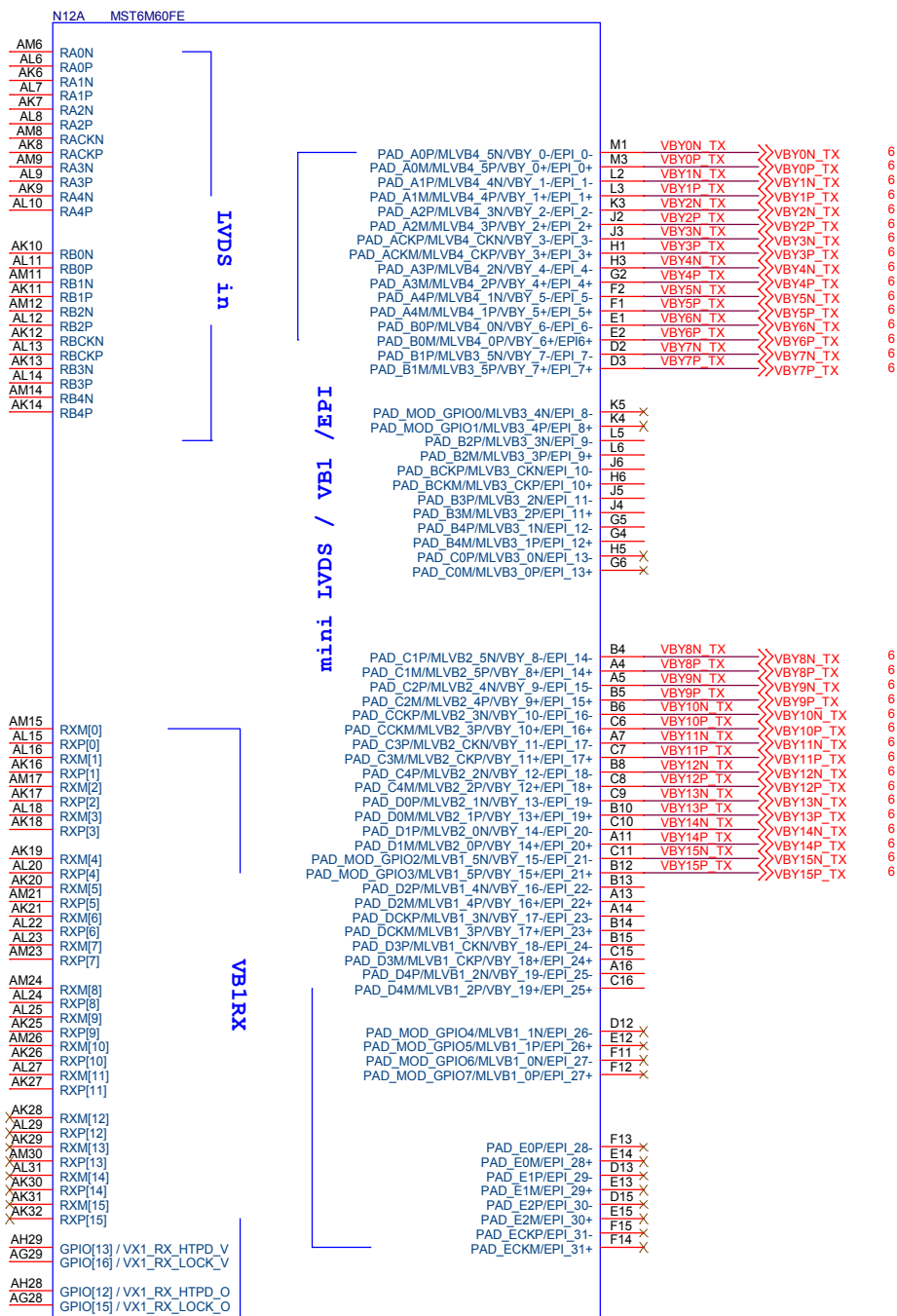


MST6A828







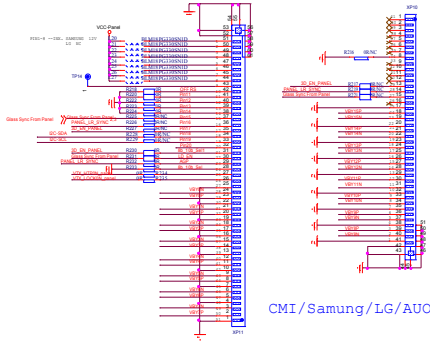


VB1 Connector

电容靠近主IC放置

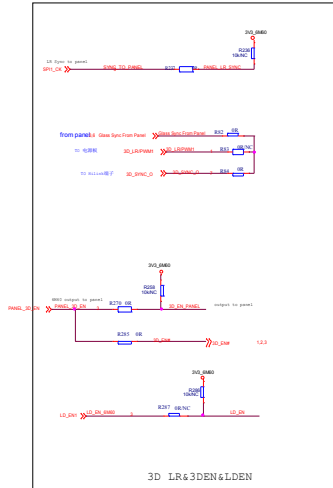
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VB1P1_3	3	100nF	100nF
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VB1P1_5	5	100nF	100nF
VB1P1_6	6	100nF	100nF
VB1P1_7	7	100nF	100nF
VB1P1_8	8	100nF	100nF
VB1P1_9	9	100nF	100nF
VB1P1_10	10	100nF	100nF
VB1P1_11	11	100nF	100nF
VB1P1_12	12	100nF	100nF
VB1P1_13	13	100nF	100nF
VB1P1_14	14	100nF	100nF
VB1P1_15	15	100nF	100nF
VB1P1_16	16	100nF	100nF
VB1P1_17	17	100nF	100nF
VB1P1_18	18	100nF	100nF
VB1P1_19	19	100nF	100nF
VB1P1_20	20	100nF	100nF
VB1P1_21	21	100nF	100nF
VB1P1_22	22	100nF	100nF
VB1P1_23	23	100nF	100nF
VB1P1_24	24	100nF	100nF
VB1P1_25	25	100nF	100nF
VB1P1_26	26	100nF	100nF
VB1P1_27	27	100nF	100nF
VB1P1_28	28	100nF	100nF
VB1P1_29	29	100nF	100nF
VB1P1_30	30	100nF	100nF
VB1P1_31	31	100nF	100nF
VB1P1_32	32	100nF	100nF
VB1P1_33	33	100nF	100nF
VB1P1_34	34	100nF	100nF
VB1P1_35	35	100nF	100nF
VB1P1_36	36	100nF	100nF
VB1P1_37	37	100nF	100nF
VB1P1_38	38	100nF	100nF
VB1P1_39	39	100nF	100nF
VB1P1_40	40	100nF	100nF
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VB1P1_47	47	100nF	100nF
VB1P1_48	48	100nF	100nF
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VB1P1_62	62	100nF	100nF
VB1P1_63	63	100nF	100nF
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VB1P1_66	66	100nF	100nF
VB1P1_67	67	100nF	100nF
VB1P1_68	68	100nF	100nF
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VB1P1_94	94	100nF	100nF
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VB1P1_96	96	100nF	100nF
VB1P1_97	97	100nF	100nF
VB1P1_98	98	100nF	100nF
VB1P1_99	99	100nF	100nF
VB1P1_100	100	100nF	100nF

8K 65寸屏:
L20/L21/L22/L23/L24/L25/L26/L27
R230/R231/R232/R233 需配
R234/R235/R236 上件



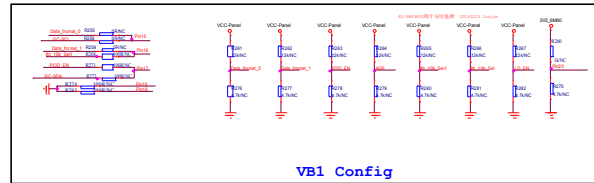
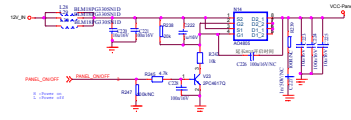
CMI/Samsung/LG/AUO

Pin	Signal	Level	Notes
1	VDD	1.2V	
2	VDD	1.2V	
3	VDD	1.2V	
4	VDD	1.2V	
5	VDD	1.2V	
6	VDD	1.2V	
7	VDD	1.2V	
8	VDD	1.2V	
9	VDD	1.2V	
10	VDD	1.2V	
11	VDD	1.2V	
12	VDD	1.2V	
13	VDD	1.2V	
14	VDD	1.2V	
15	VDD	1.2V	
16	VDD	1.2V	
17	VDD	1.2V	
18	VDD	1.2V	
19	VDD	1.2V	
20	VDD	1.2V	
21	VDD	1.2V	
22	VDD	1.2V	
23	VDD	1.2V	
24	VDD	1.2V	
25	VDD	1.2V	
26	VDD	1.2V	
27	VDD	1.2V	
28	VDD	1.2V	
29	VDD	1.2V	
30	VDD	1.2V	
31	VDD	1.2V	
32	VDD	1.2V	
33	VDD	1.2V	
34	VDD	1.2V	
35	VDD	1.2V	
36	VDD	1.2V	
37	VDD	1.2V	
38	VDD	1.2V	
39	VDD	1.2V	
40	VDD	1.2V	
41	VDD	1.2V	
42	VDD	1.2V	
43	VDD	1.2V	
44	VDD	1.2V	
45	VDD	1.2V	
46	VDD	1.2V	
47	VDD	1.2V	
48	VDD	1.2V	
49	VDD	1.2V	
50	VDD	1.2V	
51	VDD	1.2V	
52	VDD	1.2V	
53	VDD	1.2V	
54	VDD	1.2V	
55	VDD	1.2V	
56	VDD	1.2V	
57	VDD	1.2V	
58	VDD	1.2V	
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73	VDD	1.2V	
74	VDD	1.2V	
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88	VDD	1.2V	
89	VDD	1.2V	
90	VDD	1.2V	
91	VDD	1.2V	
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93	VDD	1.2V	
94	VDD	1.2V	
95	VDD	1.2V	
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97	VDD	1.2V	
98	VDD	1.2V	
99	VDD	1.2V	
100	VDD	1.2V	



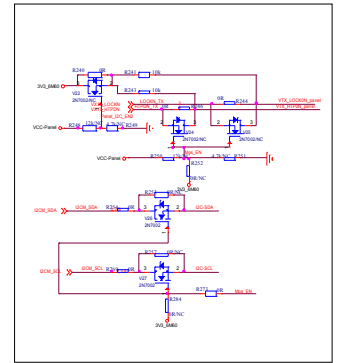
3D LR&3DEN&LDEN

Panel power

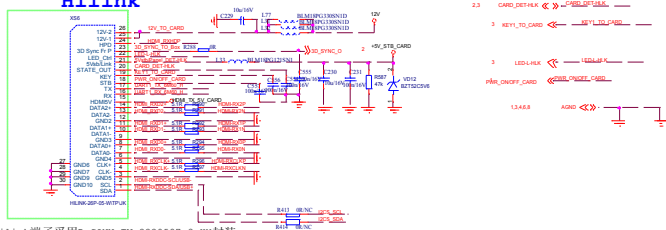


VB1 Config

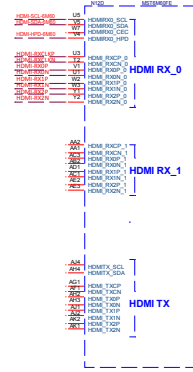
1.2V



Hilink



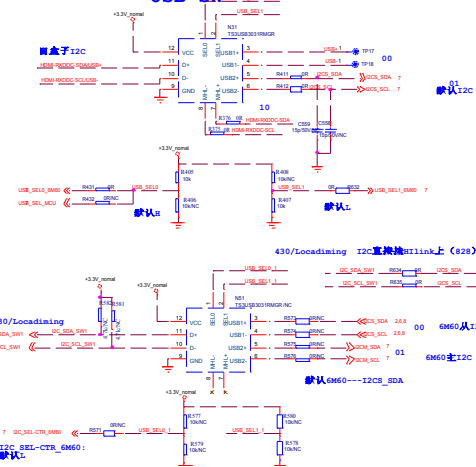
6M60



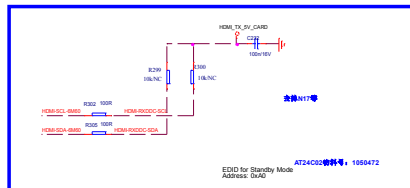
FUNCTION TABLE

SEL1	SEL0	SWITCH STATUS
Low	Low	DnD to USB1+HDMI1
Low	High	DnD to USB2+HDMI2
High	Low	DnD to MHL+HDMI1
High	High	USB and MHL switches in High-Z

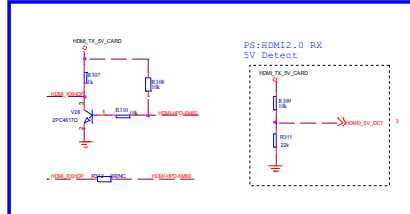
USB-SW



HDMI 2.0 EDID



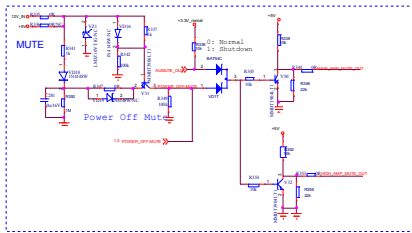
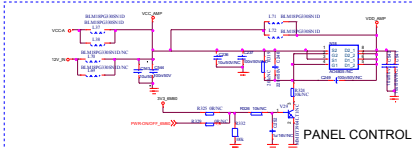
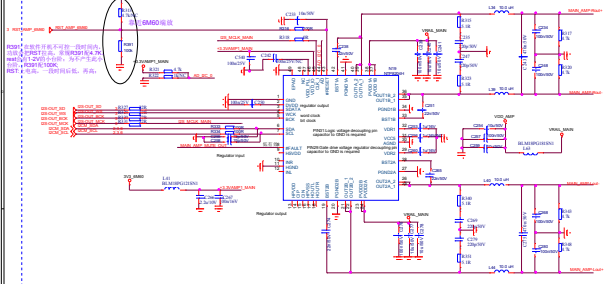
HDMI 2.0 RX0



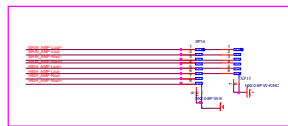
HILINK IN

MSD6M60	
Doc No.	MSD6M60
Rev.	1.0
Date	2023.12.15

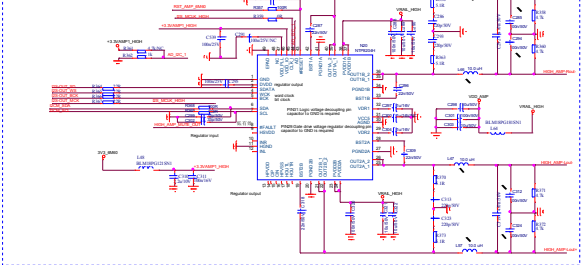
AD H== I2C ADDRESS 0X56



1.5V

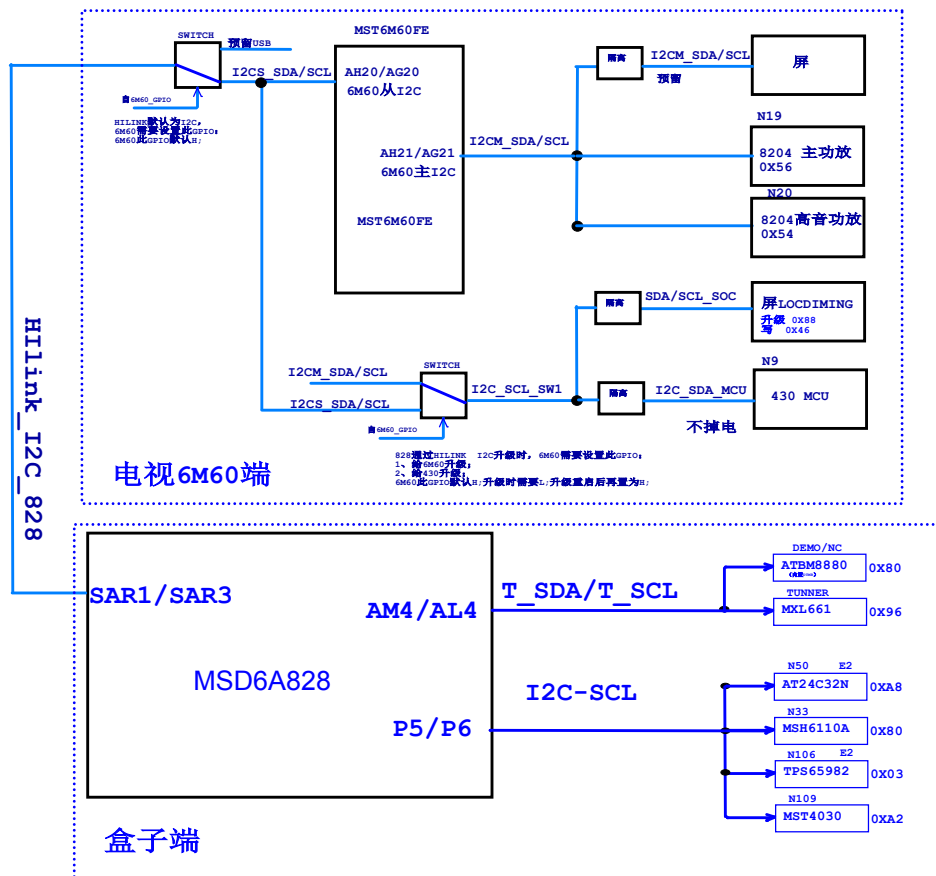
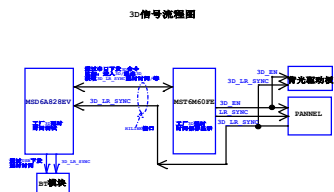
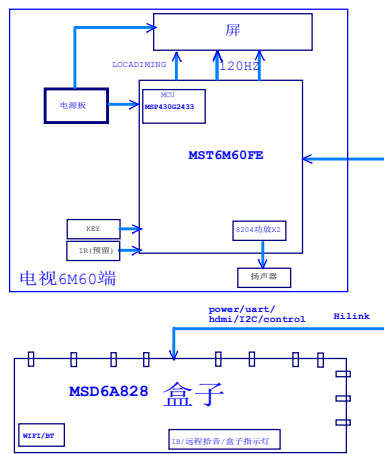


AD L== I2C ADDRESS 0X54



MSD6M60	
Part Number	AMP
Rev	1.0

分离电视框图



Title		MSD6M60	
Size	Docuement Number	分离电批拆卸图	

