AN5277

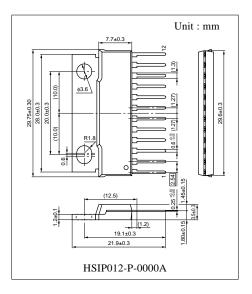
Dual Channel SEPP Power Amplifier

Overview

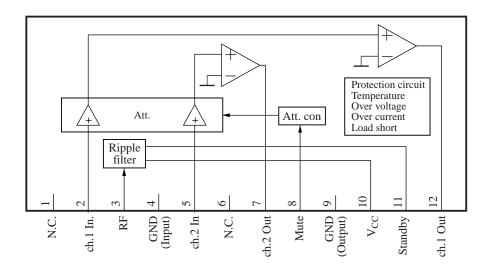
The AN5277 is a monolithic integrated circuit designed for 10.0 W (26 V, 8 Ω)output audio power amplifier. It is a dual channel SEPP IC suitable for stereo operation in TV application.

Features

- Few external components :
 - No Boucherot cells(output C, R)
 - No Bootstrap Capacitors
 - No Negative Feedback Capacitors
- Built-in muting circuit
- Built-in standby circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Compatible with AN5275, AN5276
- Operating voltage range 10 ~ 32 V(26 V typ.)



- Applications
- TV
- Block Diagram



Pin Descriptions

Pin No.	Description	Pin No.	Description
1	N.C.	7	ch.2 Output
2	ch.1 Input	8	Mute
3	Ripple Filter	9	Output GND
4	Input GND	10	V _{CC}
5	ch.2 Input	11	Standby
6	N.C.	12	ch.1 Output

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Supply voltage	V _{CC}	35.0	V
Supply current	I _{CC}	4.0	А
Power dissipation *2	P _D	37.5	W
Operating ambient temperature *1	T _{opr}	-25 to +75	°C
Storage temperature *1	T _{stg}	-55 to +150	°C

Note) *1: $T_a = 25$ °C except operating ambient temperature and storage temperature.

*2: At $T_a = 70 \ ^{\circ}C$.

Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	10.0 to 32.0	V

Electrical Caracteristics at V_{CC} = 26 V, $R_L = 8 \Omega$, f = 1 kHz, $T_a = 25 \degree C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent current	I _{CQ}	$V_{IN} = 0 mV$	—	40	80	mA
Output end noise voltage *1	V _{NO}	No input, $R_G = 10 \text{ k}$		0.22	04	mV
Voltage gain	G _V	$V_{IN} = 57 \text{ mV}$	32	34	36	dB
Total harmonic distortion *1	THD	$V_{IN} = 57 \text{ mV}$		0.2	0.4	%
Maximum output power	Po	V _{CC} = 26 V,THD = 10 %	8.0	10.0		W
Ripple rejection ratio *1	RR	$V_R = 1 \text{ Vrms} f_R = 120 \text{ Hz}, R_G = 10 \text{ k}\Omega,$	45	55		dB
Channel balance	СВ	$V_{IN} = 57 \text{ mV}$	-1.0	0	1.0	dB
Muting ratio	MR	$V_{IN} = 57 \text{ mV}$	70	80		dB
Muting control voltage	V _{MUTE}	$V_{IN} = 57 \text{ mV}, \text{MR} \ge 70 \text{ dB}$	3.0			V
Standby control voltage 'on'	V _{STDON}	No input, I _{CC} ≤0.1 mA	—		5.0	V
Standby control voltage 'off'	V _{STDOFF}	No input, I _{CC} ≥20 mA	8.5			V
Channel crosstalk	СТ	$V_{IN} = 57 \text{ mV}, R_G = 10 \text{ k}\Omega$	50	60		dB

Note) *1 : For this measurement, use the 20 Hz to 20 kHz(12 dB/OCT) filter.

Panasonic

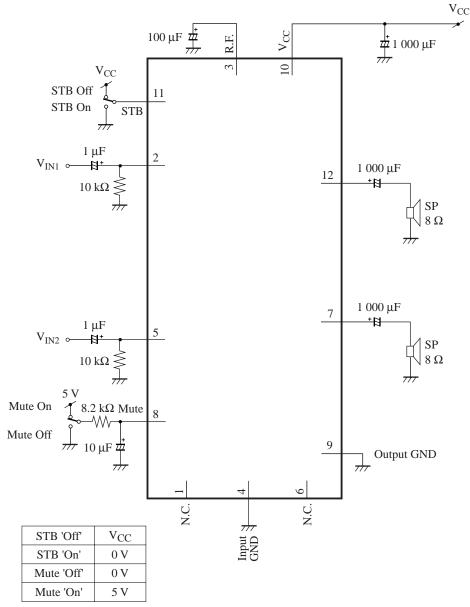
■ Terminal Equivalent Circuit

Pin No.	Equivalent Circuit	Description	DC Voltage	
1		Not connected		
2	$\frac{\operatorname{Pin2}}{5} \underbrace{\begin{array}{c} 200 \ \Omega \\ \end{array}}_{400 \ \Omega} \underbrace{\begin{array}{c} 400 \ \Omega \\ \end{array}}_{30 \ k\Omega} \underbrace{\begin{array}{c} 30 \ k\Omega \\ \end{array}}_{4}$	ch.1 Input : This is the amplifier input pins.	0 V	
3	(10) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (3) (4)	Ripple Filter : This is the pin to connect the positive terminal of a ripple filter capacitor.	V _{CC} – 1.5 V _{BE}	
4	_	Input GND : Input ground pin.	0 V	
5	Refer to Pin2	ch.2 Input : This is the amplifier input pins.	_	
6	_	Not connected		
7	Pre-amp. $O(0) \Omega$ $O(0) \Omega$ O(0)	ch.2 Output : ch.2 output pin	V _{cc} /2	

Pin Equivalent Circuit (continued)

Pin No.	Equivalent Circuit	Description	DC Voltage
8	(10) $3 k\Omega$ $3 k\Omega$ $3 k\Omega$ $3 k\Omega$ (3) (4)	Mute : Mute input pin. Mute 'On' = 5 V Mute 'Off' = 0 V	
9		Output GND : ch.1 & ch.2 output ground.	0 V
10		V _{CC} : This is the power supply pin.	typ. : 26 V
11	$\begin{array}{c} 10 \\ \hline 10 \\ \hline 1 \\ \hline 5 k\Omega \\ \hline 5 k\Omega \\ \hline 5 k\Omega \\ \hline 3 k\Omega \\ \hline 4 \\ \hline \end{array}$	Standby : This is the standby control pin.	
12	Pre-amp. 000Ω $30 k\Omega$ 9	ch.1 Output : ch.1 output pin	V _{CC} /2

Application Circuit Example



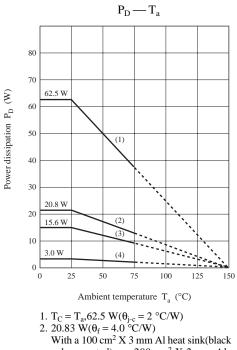
■ Usage Notes

normal operation.

- 1. External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2. Fin of the IC can be connected to GND.
- 3. Please prevent output to $V_{C\!C}$ short and output toGND short.
- 4. Load short protection will only prevent the IC from damaging if operating V_{CC} <30 V
- The temperature protection circuit will operate at Tj around 150 °C. However, if temperature decrease, the protection circuit will automatically be deactivated and resume

Technical Information

• P_D — T_a Curves of HSIP012-P-0000A



With a 100 cm² X 3 mm Al heat sink(black colour coated)or a 200 cm² X 2 mm Al heat sink(not lacquered) 15 62 w(0 = 6.0 °C/W)

3. 15.63 W(θ_r = 6.0 °C/W) With a 100 cm² X 2 mm Al heat sink(not lacquered)

4. 3.0 W at $T_a = 25 \text{ °C}(\theta_{j-a} = 42 \text{ °C/W})$ Without heat sink

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