SSC9522S



General Description

The SSC9522S is a controller IC (SMZ* method) for half-bridge resonant type power supply, incorporating a floating drive circuit for High-side MOSFET drive.

*SMZ = Soft-switched Multi-resonant Zero Current switch, all switching periods work with soft switching operation. The IC is suitable for high performance power supply system with small size, high efficiency and low noise, because for various power supply specifications, more effective and easier design works are achievable with effective functions as the Automatic Dead Time Adjustment, the Uncontrollable Operation Detection and so on.

Features and Benefits

- Built-in floating drive circuit for High-side MOSFET
- Soft Start Function, reducing of power MOSFET stress and preventing Uncontrollable Operation, at startup
- Uncontrollable Operation Detection Function on pulse-by-pulse basis, improving the ability of transformer output wattage because the frequency range is available up to the resonant frequency, f₀, and reducing power MOSFET stress
- Automatic Dead Time Adjustment Function, not being necessary to make the dead time adjustment for each power supply specification
- Line Undervoltage Protection Function (Brown-In / Brown-Out Function) Prevention of excessive input current and overheat at low input voltage
- Protection Functions External Latch Function ----- Latch shutdown by external signal input

Overcurrent Protection (OCP)----- Pulse-by-pulse Overvoltage Protection (OVP) ---- Latch shutdown Overload Protection (OLP) ------ Latch shutdown Thermal Shutdown (TSD)------ Latch shutdown

Typical Application Circuit







Not to scale

35V(MAX)

28.3kHz(TYP)

300kHz(TYP)

2.45µs(TYP)

Electrical Characteristics

- Maximum rating of V_{CC} :
- Minimum frequency, F_(MIN):
- Maximum frequency, $F_{(MAX)}$:
 - Maximum dead time, $t_{d(MAX)}$:
- Minimum dead time , $t_{d(MIN)}$: 0.50 μ s(TYP)

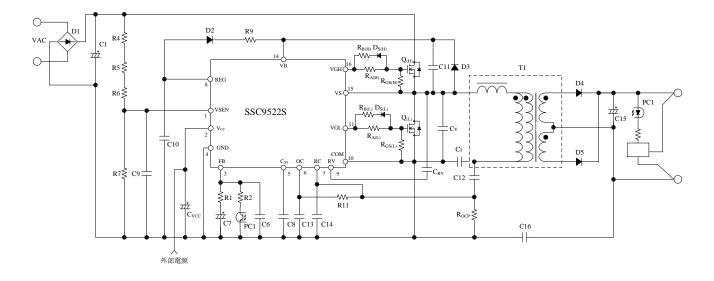
Applications

Switching power supplies for electronic devices such as:

- LCD and PDP TV
- Sarver

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- Multi Function Printer
- Industrial Equipment
- Communications Equipment



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Characteristic	Pins	Symbol	Ratings	Units	Notes
VSEN pin voltage	1-4	V _{SEN}	-0.3 toV _{REG}	V	
V _{CC} pin voltage	2-4	V _{CC}	-0.3 to $+35$	V	
FB pin voltage	3-4	V _{FB}	-0.3 to $+10$	V	
C _{SS} pin voltage	5-4	V _{Css}	-0.3 to $+12$	V	
RC pin voltage	7-4	V _{RC}	-6 to +6	V	
DV -:	0.4	I _{RV}	-2 to +2	mA	DC
RV pin current	9-4		-100 to +100	mA	Pulse 40ns
OC pin voltage	6-4	V _{OC}	-6 to +6	V	
VGL pin voltage	11-4	V _{GL}	-0.3 to V _{REG} +0.3	V	
REG pin source current	8-4	I _{REG}	-20.0	mA	
Voltage between VB and VS pin	14-15	V _B -V _S	-0.3 to $+15.0$	V	
VS pin voltage	15-4	Vs	-1 to $+600$	V	
VGH pin voltage	16-4	V _{GH}	$V_{\rm S}{-}0.3$ to $V_{\rm B}{+}0.3$	V	
Operating ambient temperature	_	T _{OP}	-20 to +85	°C	
Storage temperature	_	T _{stg}	-40 to +125	°C	
Junction temperature	_	Tj	+150	°C	

Absolute Maximum Ratings * Valid at Ta = 25°C, unless otherwise specified

*The polarity value for current specifies a sink as "+", and a source as "-", referencing the IC.

Note: Surge voltage withstand (Human body model) of No.14 to No.16 pin is guaranteed 1000V. Other pins are guaranteed 2000V.

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Denomator	Dim	$20 \text{ V}, \text{ Ta} = 25^{\circ}$	Ratings				Nutri	
Parameter	Pin	Symbol		MIN	TYP	MAX	Unit	Notes
Start/Circuit current		•						
Operation start voltage	2-4	V _{CC}	C(ON)	10.2	11.8	13.0	V	V _{CC(OFF)}
Operation stop voltage	2-4	V _{CC}		8.8	9.8	10.9	V	<v<sub>CC(ON</v<sub>
Circuit current in operation	2-4	I _{CC}	(ON)	_	_	20.0	mA	
Circuit current in non-operation	2-4	I _{CC(}	OFF)		_	1.2	mA	V _{CC} =9V
Circuit current in latch-operation	2-4	I _{CC}				1.2	mA	V _{CC} =11V
OLP latch/External Latch	•							•
FB pin source current	3-4	I _F	B	-30.5	-25.5	-20.5	μΑ	
FB pin threshold voltage	3-4		FB	6.55	7.05	7.55	V	
C_{ss} pin threshold voltage(1)	5-4	V _C	ss(1)	7.0	7.8	8.6	V	
Latch circuit release V_{CC} voltage	2—4	V _{CC(}		6.7	8.2	9.5	V	V _{CC(La.OF} <v<sub>CC(OF</v<sub>
Oscillator		1						(
Minimum frequency	11-10 16-15	F _{(N}	1IN)	26.2	28.3	31.2	kHz	
Maximum frequency	11-10 16-15	F _{(M}	IAX)	265	300	335	kHz	
Maximum dead-time	11-10 16-15	t _{d(M}	IAX)	1.90	2.45	3.00	μs	
Minimum dead-time	11-10 16-15	t _{d(N}	4IN)	0.25	0.50	0.75	μs	
Control								
Burst mode start FB pin source current	3-4	I _{CON}	NT(1)	-2.9	-2.5	-2.1	mA	
Oscillation stop FB pin source current	3-4	I _{CON}	NT(2)	-3.7	-3.1	-2.5	mA	
Soft start								
C _{ss} pin charge current	5-4	I _{Cs}	s(C)	-0.21	-0.18	-0.15	mA	
C _{ss} pin reset current	5-4	I _{Cs}		1.0	1.8	2.4	mA	V _{CC} =9V
Overvoltage protection / Thermal pro	tection							•
OVP operating V_{CC} voltage	2-4	Vo	OVP	28.0	31.0	34.0	V	
Thermal shutdown operating temperature		Tj		150	_	_	°C	
Detection of current resonant / Overc	urrent pro	tection						
				0.055	0.155	0.255	V	
Uncontrollability detection voltage	7—4	V_{RC}		-0.255	-0.155	-0.055	V	
DO alla desate 11 de la CUL	7-4	V _{RC(S)}		2.15	2.35	2.55	V	
RC pin threshold voltage (Hi speed)				-2.55	-2.35	-2.15	V	
OC pin threshold voltage(Low)	6-4	V _{OC(L)}		1.42	1.52	1.62	V	
OC pin threshold voltage (High)	6-4		C(H)	1.69	1.83	1.97	V	
OC pin threshold voltage (Hi speed)	6-4	1	C(S)	2.15	2.35	2.55	V	
			(L)	1.0	1.8	2.4	mA	
C _{ss} pin sink current	5—4	I _{Css}	(H)	12.0	20.0	28.0	mA	
-		(S)		11.0	18.3	25.0	mA	

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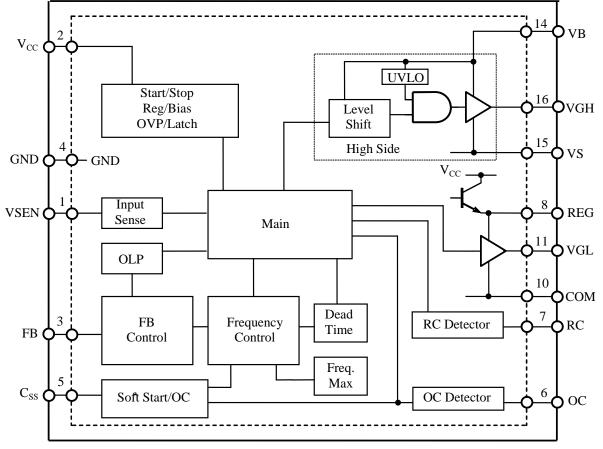
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		: 20 V, Ta = 2		Ratings			
Parameter	Pin	Symbol	MIN	TYP	MAX	Unit	Notes
Detection of voltage resonant							
RV pin voltage detect Resonance voltage(1)	9—4	V _{RV(1)}	3.8	4.9	5.4	V	
RV pin voltage detect Resonance voltage(2)	9-4	V _{RV(2)}	1.20	1.77	2.30	V	
Stand by							
Burst oscillation frequency	5-4	f _{Css}	70	105	130	Hz	
ON/OFF							
C _{SS} pin threshold voltage (2)	5-4	V _{Css(2)}	0.50	0.59	0.68	V	
Input voltage detect function							
VSEN pin threshold voltage (ON)	1-4	V _{SEN(ON)}	1.32	1.42	1.52	V	
VSEN pin threshold voltage (OFF)	1-4	V _{SEN(OFF)}	1.08	1.16	1.24	V	
Supply of driver circuit	•						•
REG pin output voltage	8-4	V _{REG}	9.9	10.5	11.1	V	
High-side driver						I	1
High-side drive operation start voltage	14-15	V _{BUV(ON)}	6.3	7.3	8.3	V	
High-side drive operation stop voltage	14-15	V _{BUV(OFF)}	5.5	6.4	7.2	V	
Drive circuit							
VGL,VGH pin source current 1	11—10 16—15	IGL _{SOURCE1} IGH _{SOURCE1}		-515	_	mA	$V_{REG}=10.5$ $V_{B}=10.5$ $V_{GL}=0$ $V_{GH}=0$
VGL,VGH pin sink current 1	11—10 16—15	IGL _{SINK1} IGH _{SINK1}		685		mA	$V_{REG}=10.5$ $V_{B}=10.5$ $V_{GL}=10.5$ $V_{GH}=10.5$
VGL,VGH pin source current 2	11-10 16-15	IGL _{SOURCE2} IGH _{SOURCE2}	-120	-85	-50	mA	$V_{REG}=12V$ $V_{B}=12V$ $V_{GL}=10.5V$ $V_{GH}=10.5V$
VGL,VGH pin sink current 2	11-10 16-15	IGL _{SINK2} IGH _{SINK2}	70	113	160	mA	$V_{REG}=12V$ $V_{B}=12V$ $V_{GL}=1.5V$ $V_{GH}=1.5V$
Thermal characteristics	T	1		T	1	r	<u></u>
Junction to ambient thermal resistance		θ_{j-a}	—	—	95	°C/W	

* The polarity value for current specifies a sink as "+", and a source as "-", referencing the IC.

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Functional Block Diagram



Pin List Table

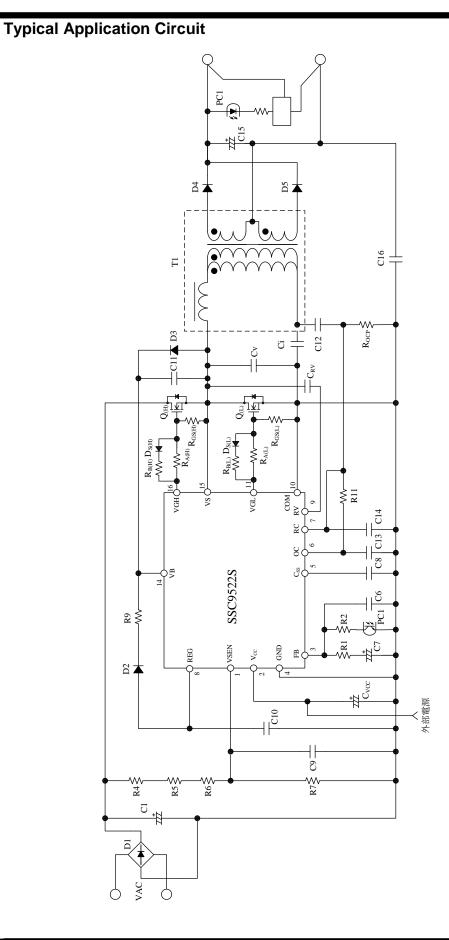
			Terminal	Symbol	Functions
	\bigcirc		1	VSEN	AC line input voltage detection
1	VSEN (NC		2	V _{CC}	Power supply input for control circuit and Overvoltage protection signal input
2	V _{CC} (NC		3	FB	Constant voltage control signal input and Overload protection signal input
3	FB VG	16	4	GND	Ground for control part
4	GND V		5	C _{SS}	Soft start capacitor connection and external latch shutdown signal input
5	C _{SS} V	3 14	6	OC	Overcurrent protection signal input
6	OC (NC) 13	7	RC	Resonant current signal input
			8	REG	Power supply output for gate drive
7	RC (NC) 12	9	RV	Resonant voltage signal input
8	REG VG	_ 11	10	COM	Ground for power part
9	RV COI	/ 10	11	VGL	Low-side gate drive output
9	RV COI		12	(NC)	(No connection)
			13	(NC)	(No connection)
			14	VB	High-side gate drive voltage input
		15	VS	High-side drive floating ground	
			16	VGH	High-side gate drive output
			17	(NC)	(No connection)
			18	(NC)	(No connection)

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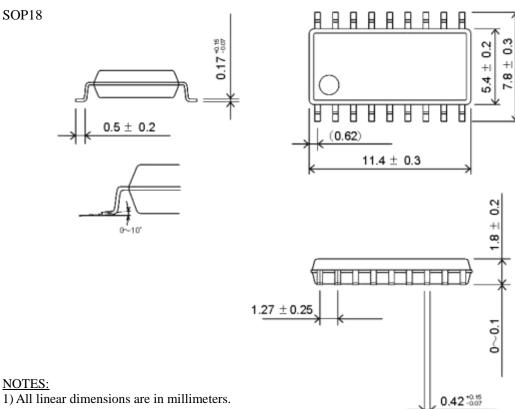






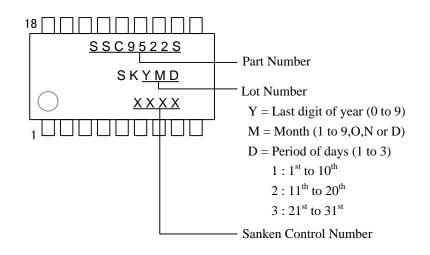
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2) Pb-free. Device composition compliant with the RoHS directive.

Marking Diagram





OOPERATING PRECAUTIONS

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5 to 35°C) and the standard relative humidity (around 40 to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
 - 260 ± 5 °C 10 ± 1 s (Flow, 2 times)
 - 380 ± 10 °C 3.5 ± 0.5 s (Soldering iron, 1 time)

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least $1M\Omega$ of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.



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In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.

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