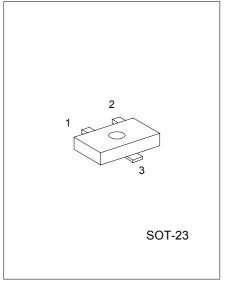
UTCDTC144E

NPN DIGITAL TRANSISTOR

NPN DIGITAL TRANSISTOR (BUILT-IN RESISTORS)

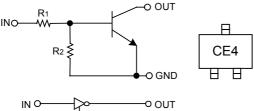
FEATURES

- *Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- *The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input They also have the advantage of almost completely eliminating parasitic effects.
- *Only the on / off conditions need to be set for operation, making device design easy.



EQUIVALENT CIRCUIT

MARKING



1: GND 2: IN 3: OUT

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

₩ GND

7.2002012 http://www.nath.net.com/nath.net/							
PARAMETER	SYMBOL	RATING	UNIT				
Supply Voltage	Vcc	50	V				
Input Voltage	VIN	-10~+12	V				
Output Current	lo	100	mA				
	IC(Max)	100	IIIA				
Power Dissipation	Pd	200	mW				
Junction Temperature	Tj	150	°C				
Storage Temperature	T _{stg}	-55~+150	°C				

ELECTRICAL CHARACTERISTICS(Ta=25°C,unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V _I (off)	Vcc=5V , Io=100μA			0.5	V
	V _I (on)	Vo=0.3V , Io=2mA	3			V
Output Voltage	Vo(on)	Io/I=10mA/0.5mA			0.3	V
Input Current	lı	Vi=5V			0.18	mA
Output Current	lo(off)	Vcc=50V,Vi=0V			0.5	μА
DC Current Gain	G	Vo=5V, Io=5mA	68			

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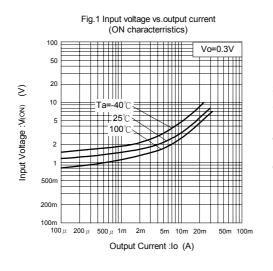
UTCDTC144E

NPN DIGITAL TRANSISTOR

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Resistance	R1		32.9	47	61.1	$\mathbf{k} \Omega$
Resistance Ratio	R2/R1		0.8	1	1.2	
Transition Frequency	f _T	VCE=10V,IE= -5mA f=100MHz *		250		MHz

^{*}Transition frequency of the device.

TYPICAL CHARACTERISTIC CURVES



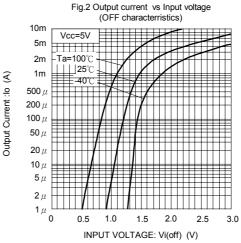
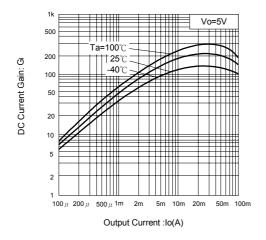
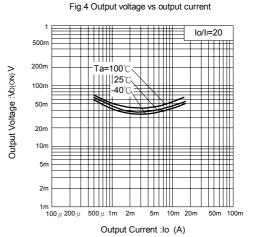


Fig.3 DC current gain vs.output current





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