

P-Channel 20-V (D-S) MOSFET

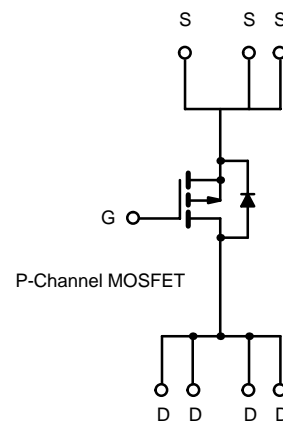
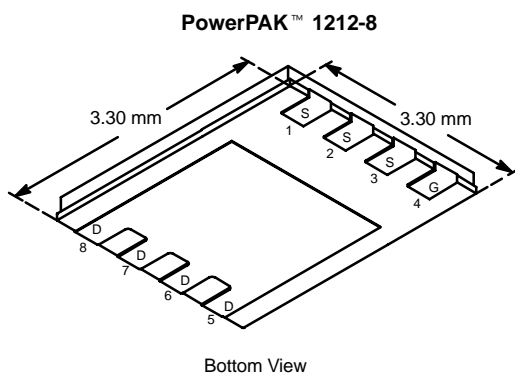
PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-20	0.1 @ $V_{GS} = -4.5$ V	-4.5
	0.135 @ $V_{GS} = -2.5$ V	-3.8

FEATURES

- TrenchFET® Power MOSFETS: 2.5-V Rated
- New PowerPAK™ Package
 - Low Thermal Resistance, R_{thJC}
 - Low 1.07-mm Profile

APPLICATIONS

- Load Switching
- PA Switching



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	V_{DS}	-20		V	
Gate-Source Voltage	V_{GS}	± 8			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	-4.5	-2.9	A
		$T_A = 85^\circ\text{C}$	-3.2	-2.1	
Pulsed Drain Current	I_{DM}	-20			
continuous Source Current (Diode Conduction) ^a	I_S	-3.0	-1.3		
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	3.5	1.5	W
		$T_A = 85^\circ\text{C}$	1.9	0.8	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 10$ sec	28	35	$^\circ\text{C/W}$
		Steady State	65	81	
Maximum Junction-to-Case	R_{thJC}	4.5	5.6		

Notes

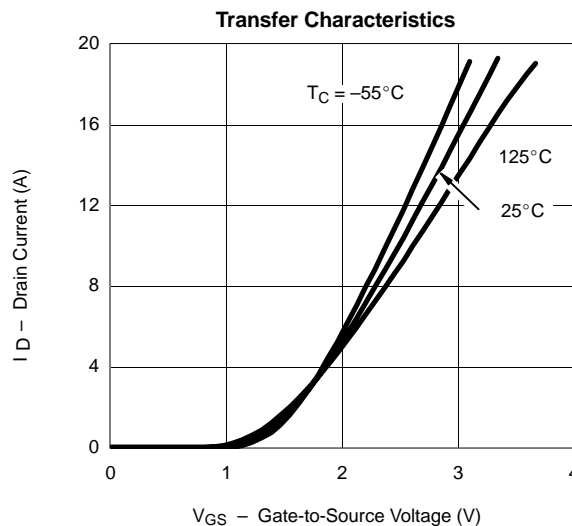
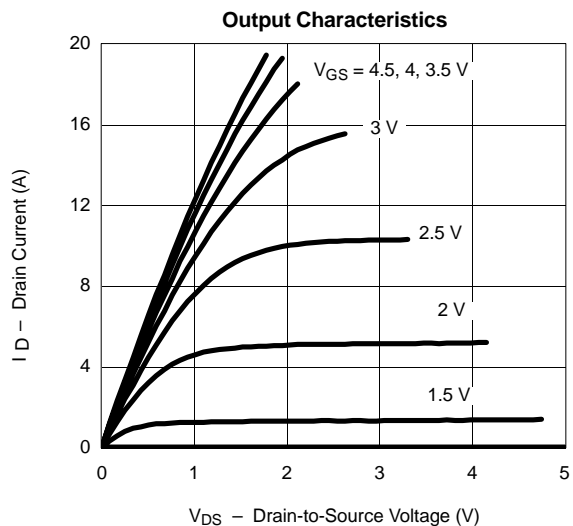
a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.45			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±4.5 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -20 V, V _{GS} = 0 V, T _J = 70 °C			-5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -4.5 V	-10			A
		V _{DS} = -5 V, V _{GS} = -2.5 V	-4			
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -3.3 A		0.078	0.1	Ω
		V _{GS} = -2.5 V, I _D = -2.9 A		0.110	0.135	
Forward Transconductance ^a	g _{fs}	V _{DS} = -10 V, I _D = -3.3 A		8.8		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.6 A, V _{GS} = 0 V		0.8	-1.2	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -4.5 A		8.6	14	nC
Gate-Source Charge	Q _{gs}			1.5		
Gate-Drain Charge	Q _{gd}			3.1		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -10 V, R _L = 10 Ω I _D ≅ -1.6 A, V _{GEN} = -4.5 V, R _G = 6 Ω		27	50	ns
Rise Time	t _r			17	30	
Turn-Off Delay Time	t _{d(off)}			52	80	
Fall Time	t _f			45	70	
Source-Drain Reverse Recovery Time	t _{rr}		I _F = -1.6 A, di/dt = 100 A/μs		50	

Notes

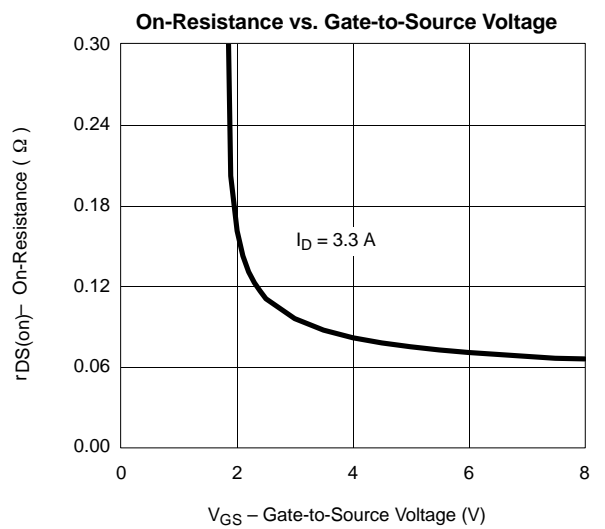
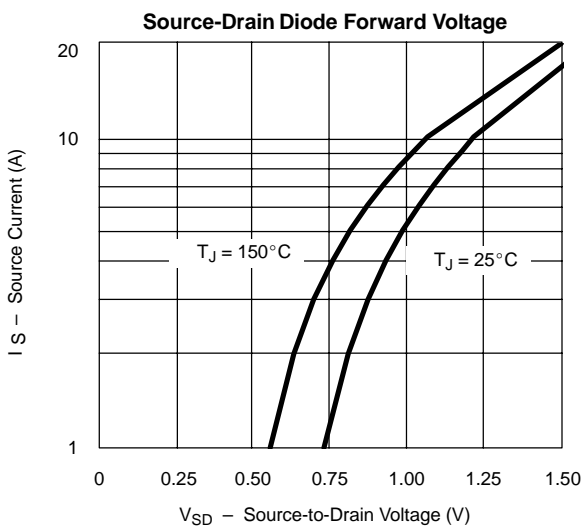
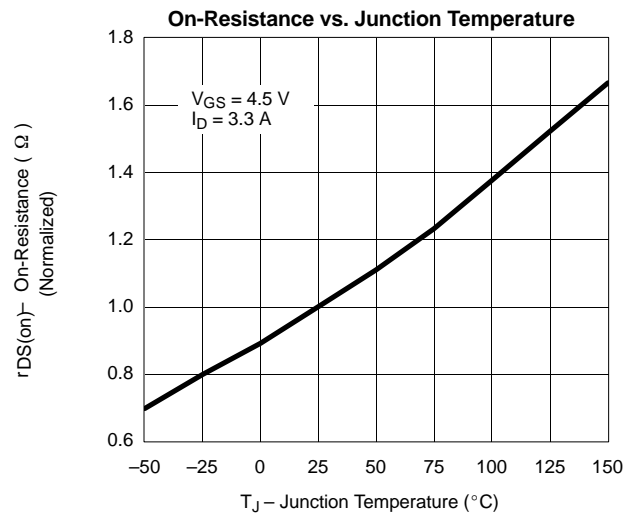
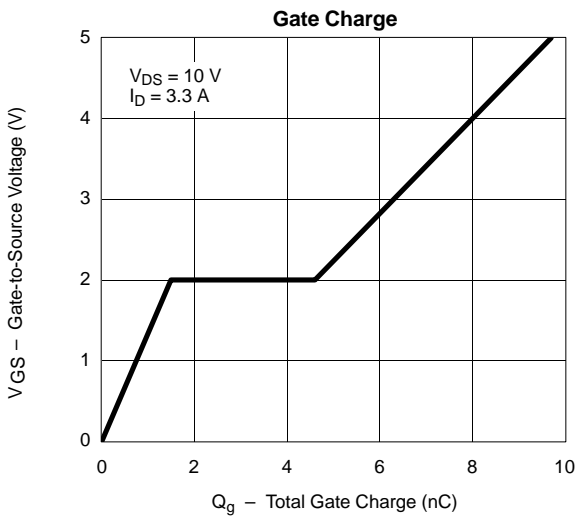
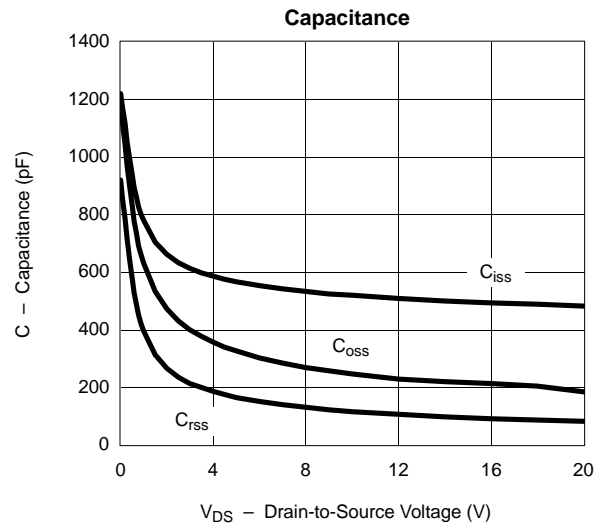
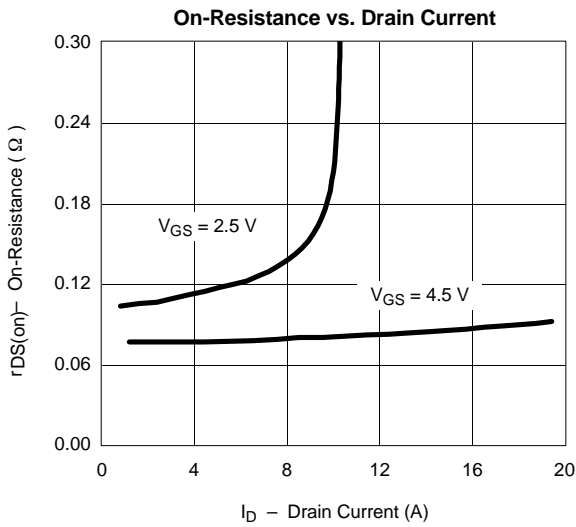
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

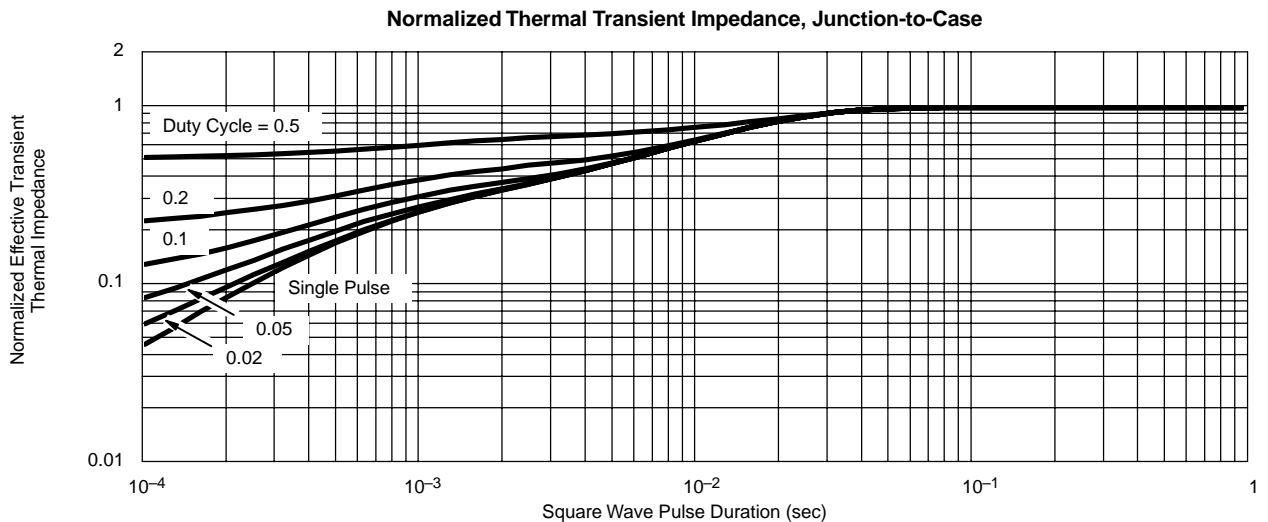
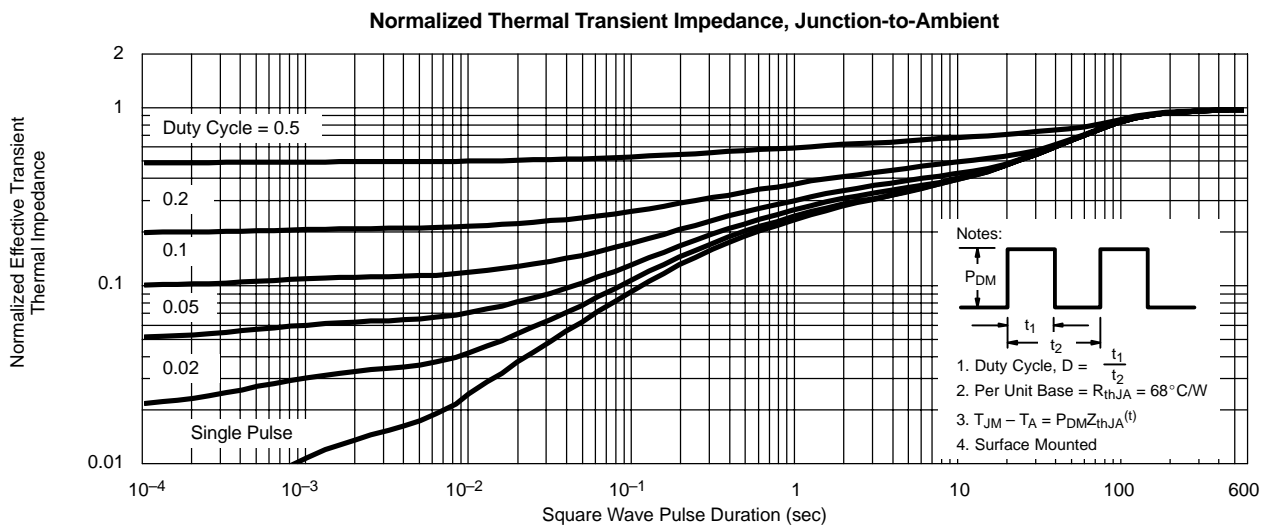
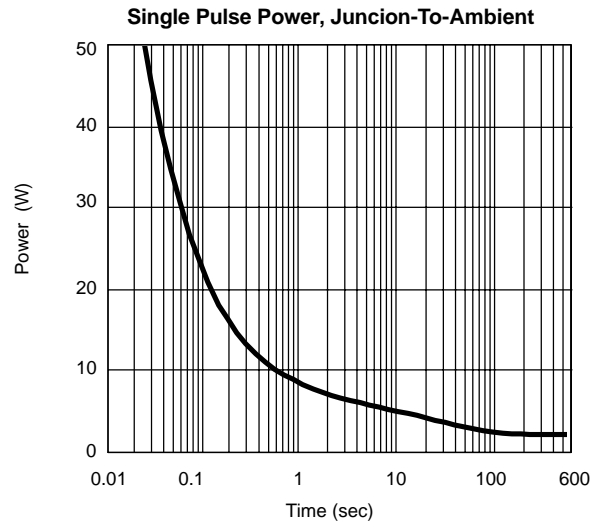
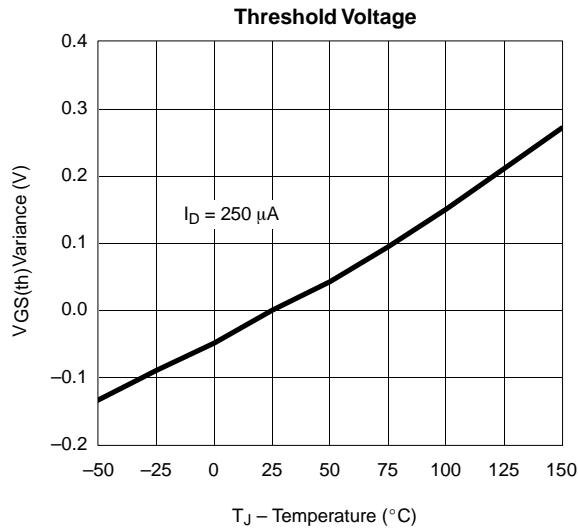




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