# General purpose (dual digital transistors) EMB11/UMB11N/IMB11A

## Features

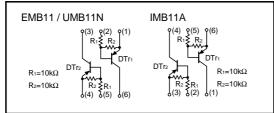
- 1) Two DTA114E chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

#### Structure

Epitaxial planar type PNP silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

#### Equivalent circuit



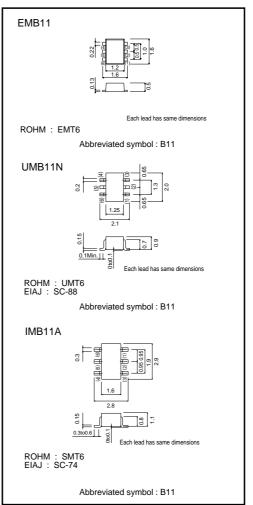
#### ●Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	-50	V	
Input voltage		Vin	-40	V	
		VIN	10		
Output current		lo	-50	mA	
		IC (Max.)	-100		
Power dissipation	EMB11, UMB11N	Pd	150 (TOTAL)	mW_*1 *2	
	IMB11A	Fu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

\*1 120mW per element must not be exceeded.

\*2 200mW per element must not be exceeded.

#### •External dimensions (Unit : mm)



# Transistors

•Electrical characteristics	(Ta = 25°C)
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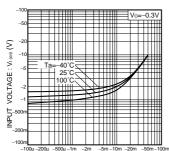
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltogo	VI (off)	-	_	-0.5	V	Vcc= -5V, Io= -100µA	
Input voltage	VI (on)	-3.0	_	-	v	Vo= -0.3V, Io= -10mA	
Output voltage	VO (on)	-	-0.1	-0.3	V	lo/lı= -10mA/ -0.5mA	
Input current	h	-	_	-0.88	mA	VI=-5V	
Output current	IO (off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V	
DC current gain	Gi	30	-	-	-	Vo= -5V, Io= -5mA	
Transition frequency	f⊤	-	250	-	MHz	Vce= -10V, Ie=5mA, f=100MHz *	
Input resistance	R1	7	10	13	kΩ	_	
Resistance ratio	R2/R1	0.8	1	1.2	-	_	

\* Transition frequency of the device

#### Packaging specifications

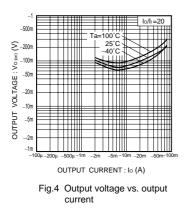
	Package	Taping			
	Code	T2R	TN	T110	
Туре	Basic ordering unit (pieces)	8000	3000	3000	
EMB11		0	—	—	
UMB11N		—	0	_	
IMB11A		_		0	

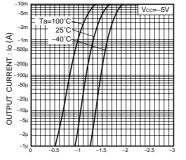
#### •Electrical characteristic curves



OUTPUT CURRENT : Io (A)

Fig.1 Input voltage vs. output current (ON characteristics)





INPUT VOLTAGE : VI (off) (V)

Fig.2 Output current vs. input voltage (OFF characteristics)

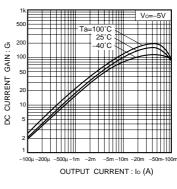


Fig.3 DC current gain vs. output current

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