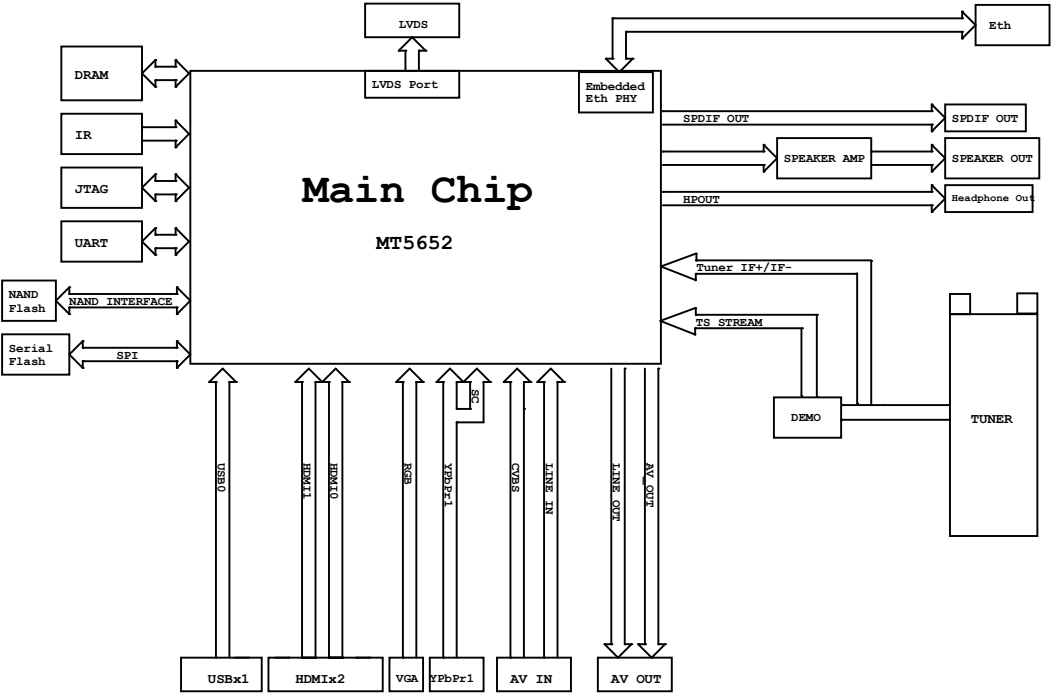
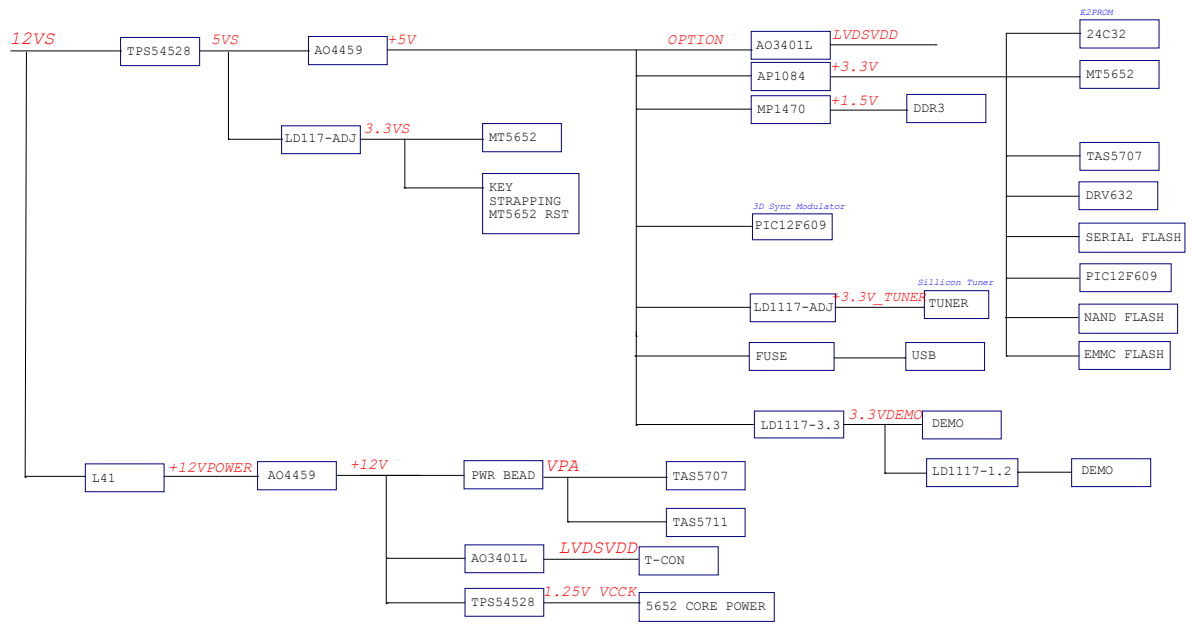


GPIO LIST

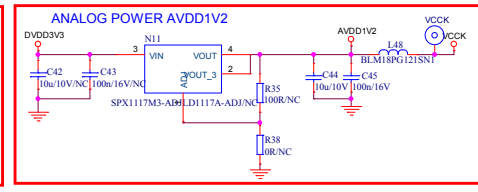
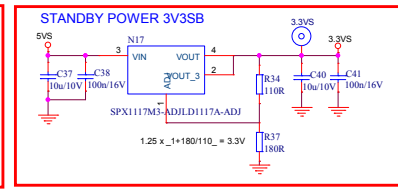
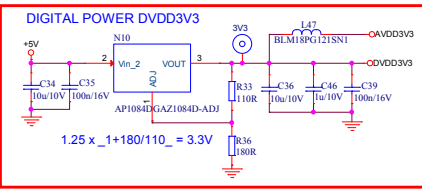
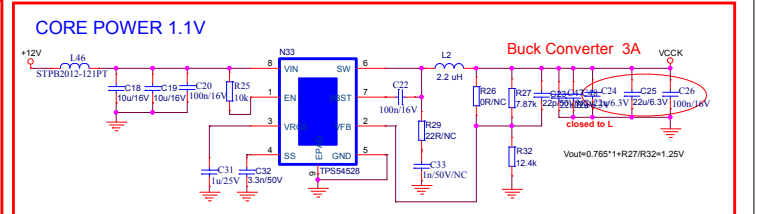
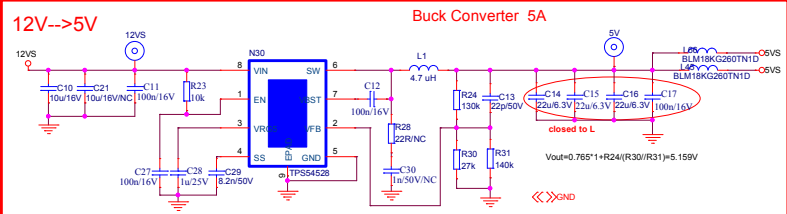
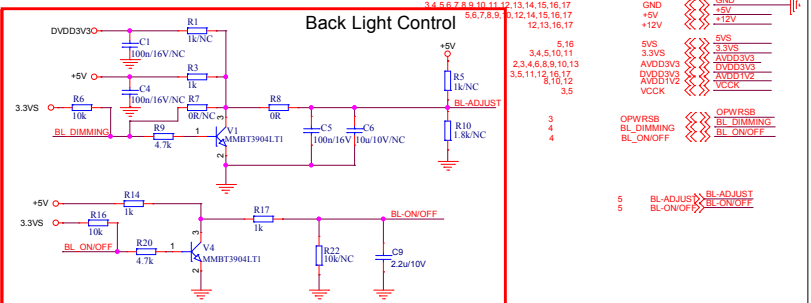
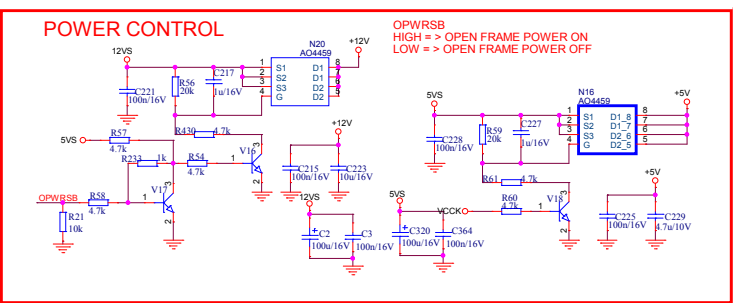
PIN NAME	GPIO Function	Function define
GPIO_0		LOCAL_DIM_EN
GPIO_1		TUNER_SW
GPIO_2		LVDS_PWR_EN
GPIO_3		EMMC_RST
GPIO_4		Demod_RST#
GPIO_5		not used
GPIO_6		3D_EN
GPIO_7		3D相关
GPIO_8		3D相关
GPIO_9		3D_OUT
GPIO_10		HPDET#
GPIO_11		SYS_EEPROM_WP
ADIN0		SCART_FS_SEL
ADIN1		通过10k接地，未使用
ADIN2		ADIN0
ADIN3		ADIN1
ADIN4		LED_DX
ADIN5		POWER_LED
OPCTRL0		FLASH写保护控制
OPCTRL1		BL_ON/OFF
OPCTRL2		AMP_MUTE
OPCTRL3		strap[2]
OPCTRL4		strap[3] RST_AMP
OPWM0		WIFI_EN
OPWM1		MEMC_ON/OFF
OPWM2		BL_DIMMING
OPWM3		MUTE_HP
OPWM4		3D_CTRL
OPWM5		LED2/PHYAD2
OPWM6		LED3/PHYAD3
PACLE		strap[0]
CIGPIO0		RESERVED LVDS预留
CIGPIO15		TS1_OUT_CLK
CIGPIO16		TS1_OUT_VLD
CIGPIO17		TS1_OUT_SYNC
CIGPIO18		TS1_OUT_D0
CIGPIO19		TS1_OUT_D1
CIGPIO20		TS1_OUT_D2
CIGPIO21		TS1_OUT_D3
CIGPIO22		TS1_OUT_D4
CIGPIO23		TS1_OUT_D5
CIGPIO24		TS1_OUT_D6
CIGPIO25		TS1_OUT_D7

Block Diagram

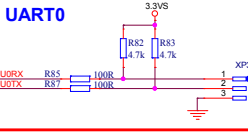
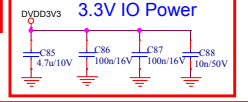
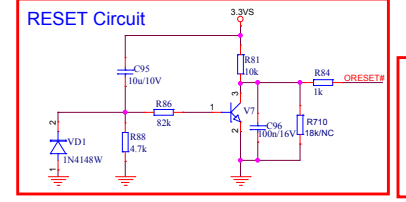
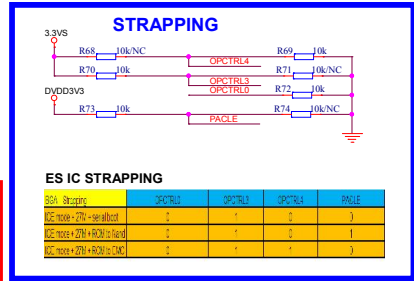
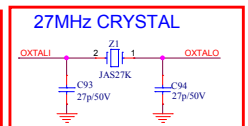
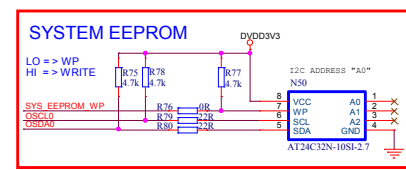
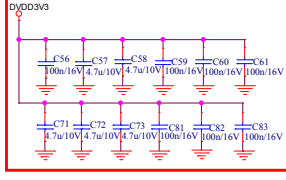
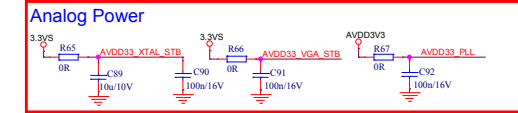
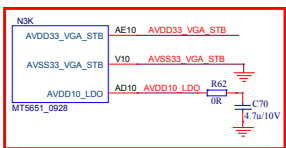
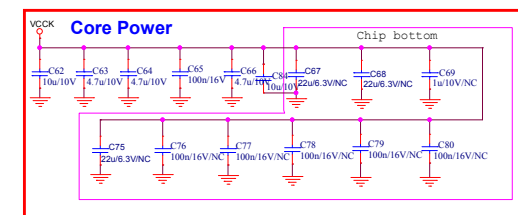
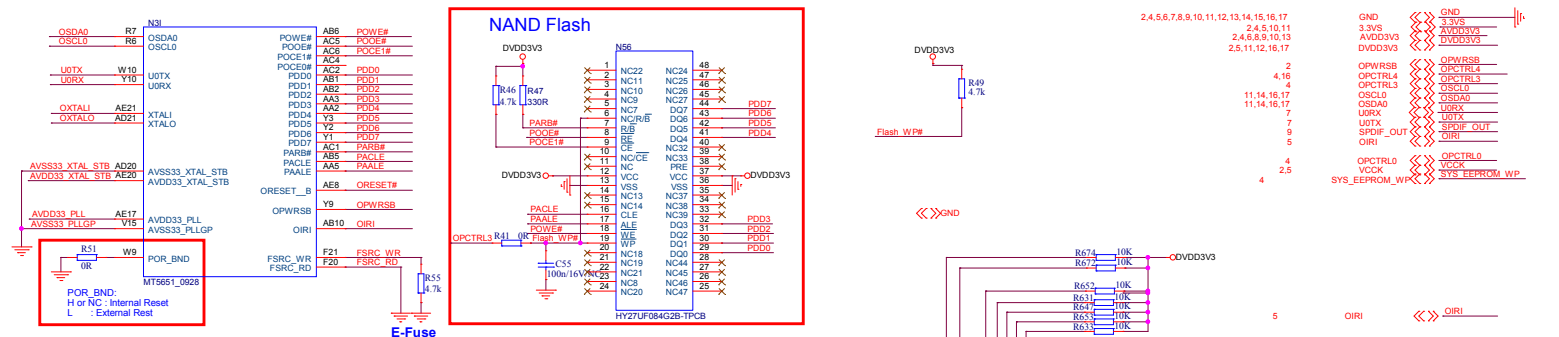




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Title: Power Tree		



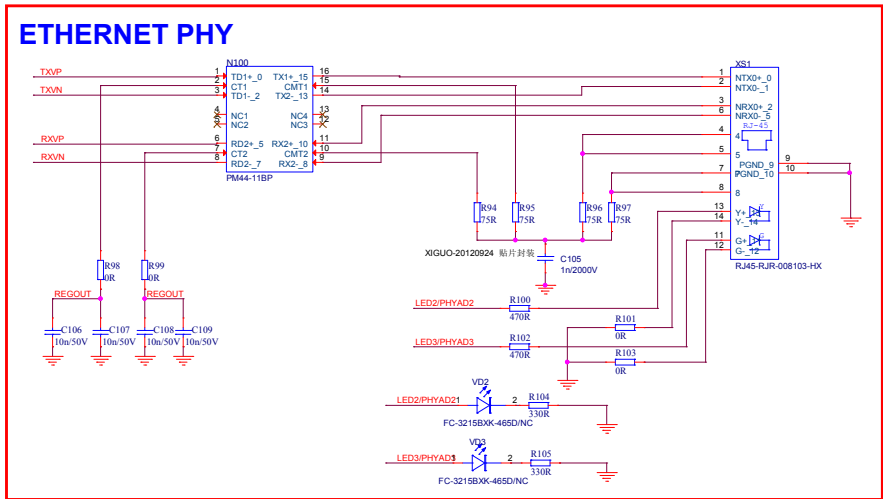
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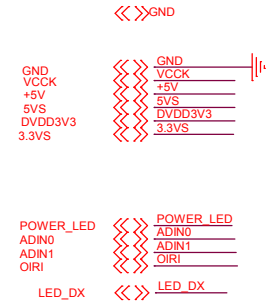
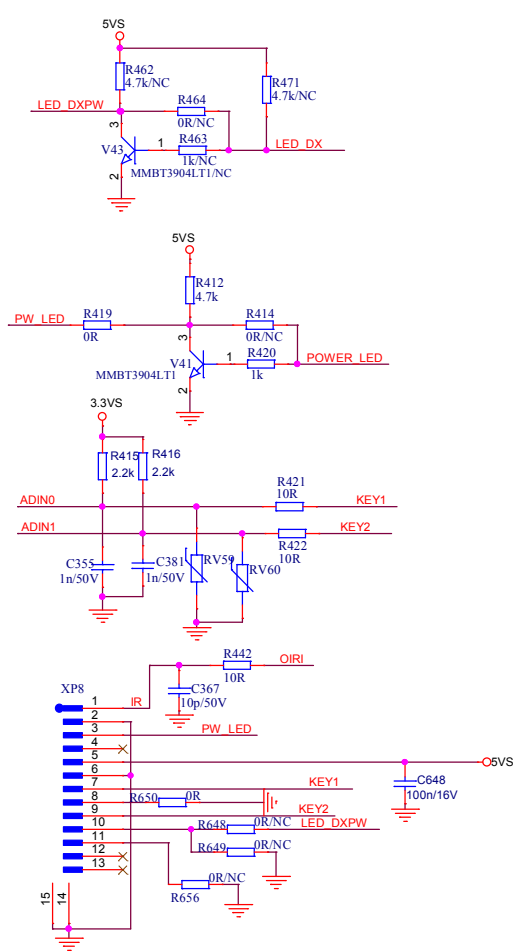
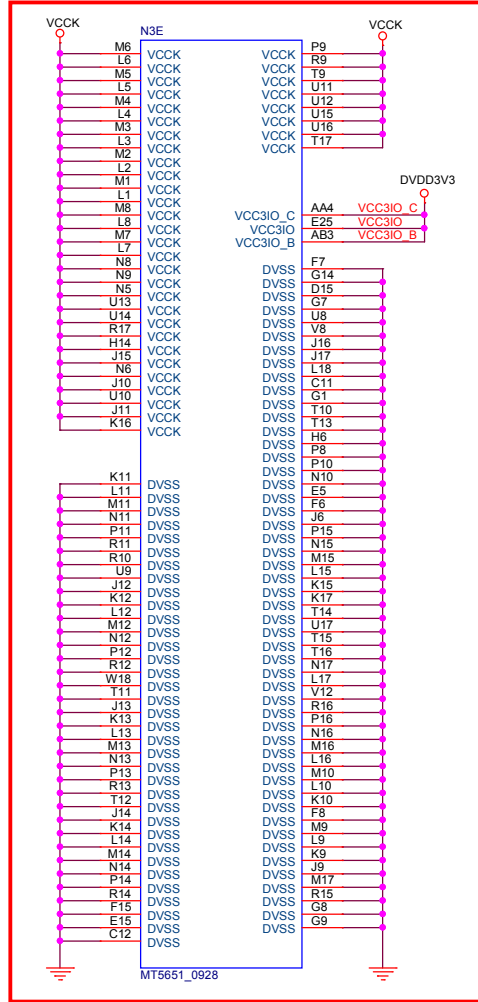
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2,4,5,6,7,8,9,10,11,12,13,14,15,16,17	AVDD3V3	AVDD3V3
2,4,5,6,7,8,9,10,11,12,13,14,15,16,17	DVDD3V3	DVDD3V3
2	OPWRSB	OPWRSB
4,16	OPCTRL4	OPCTRL4
4	OPCTRL3	OPCTRL3
11,14,16,17	OSCL0	OSCL0
7	U0TX	U0TX
7	SPDIF_OUT	SPDIF_OUT
5	OIRI	OIRI
4	OPCTRL0	OPCTRL0
2,5	V0CK	V0CK
	SYS_EEPROM_WP	SYS_EEPROM_WP

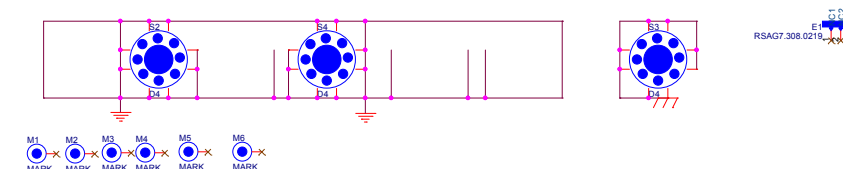
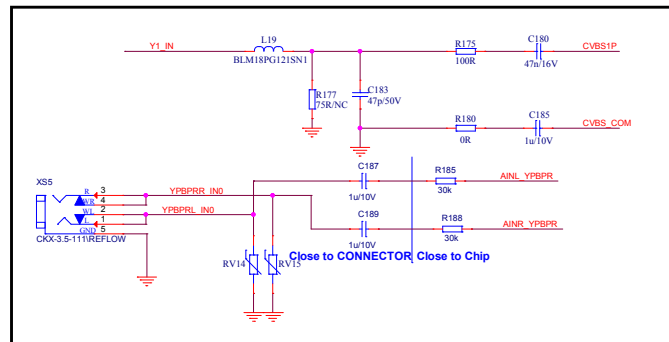
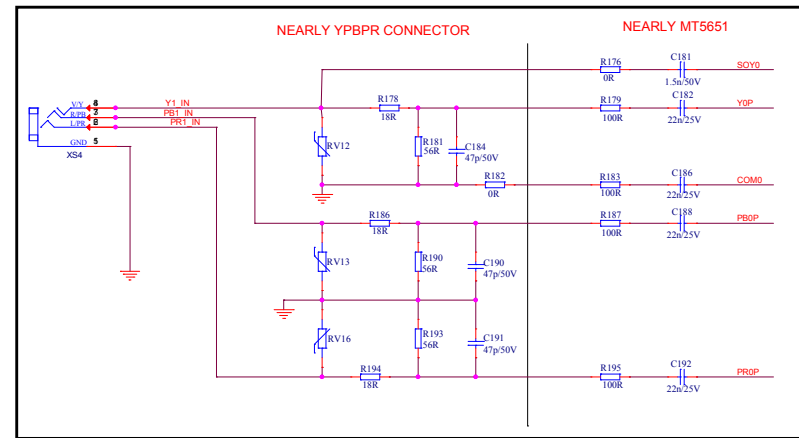
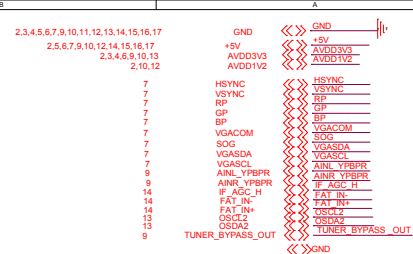
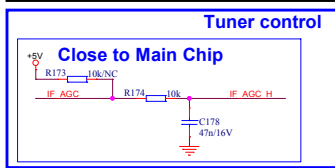
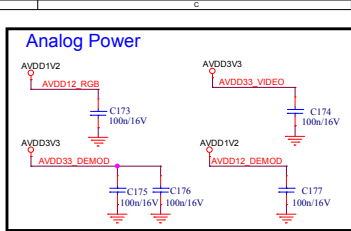
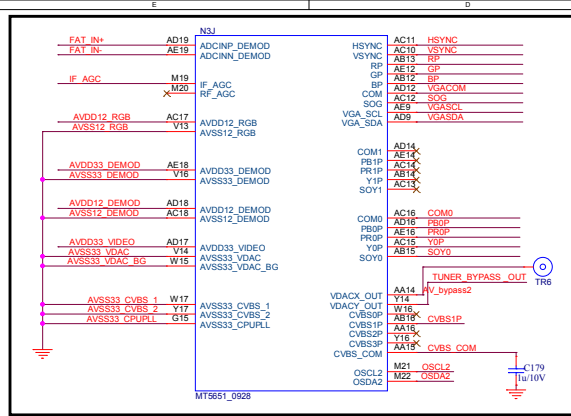
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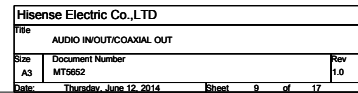
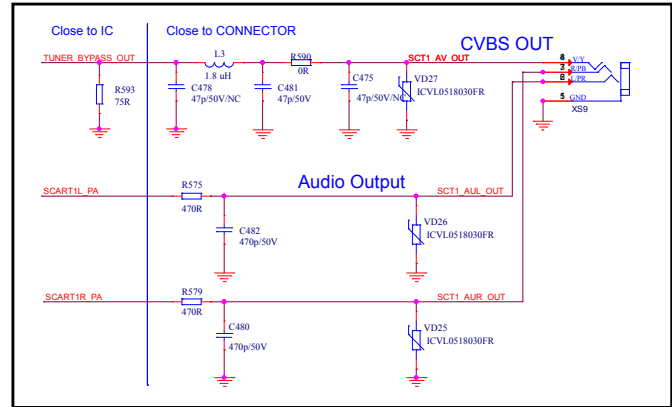
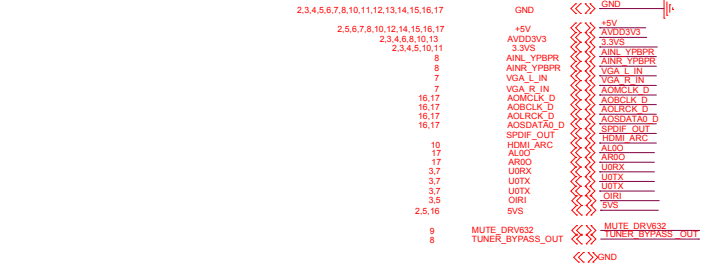


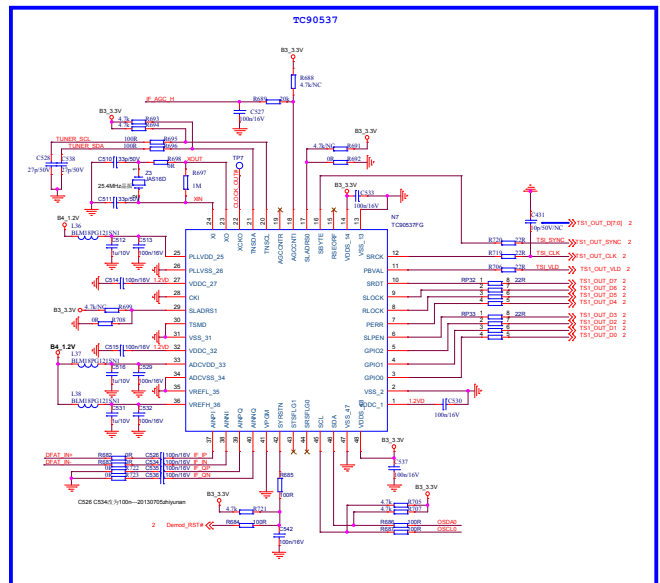
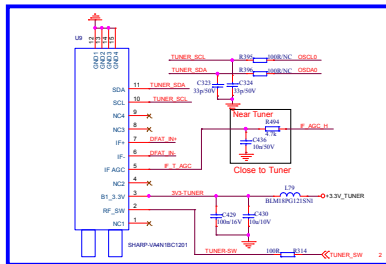
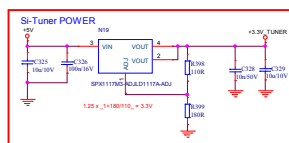
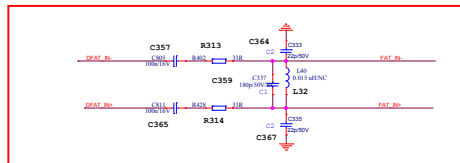
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2,3,5,11,12,16,17

GND
+5V
DVDD3V3

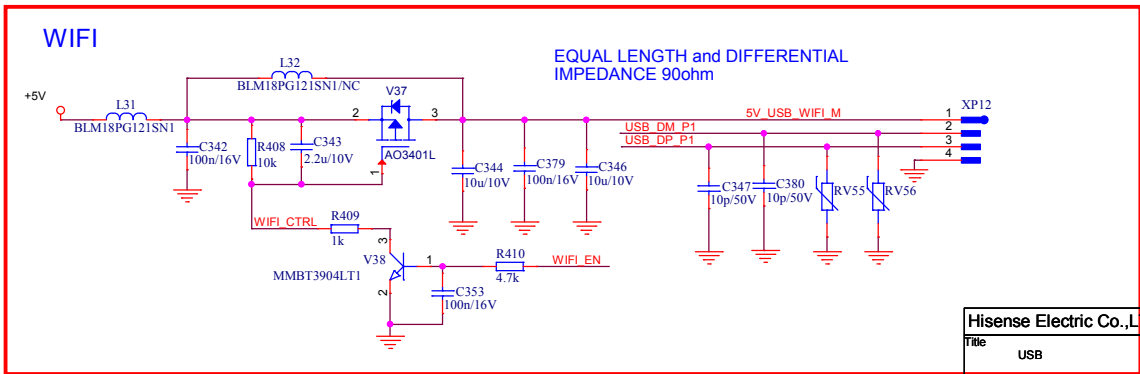
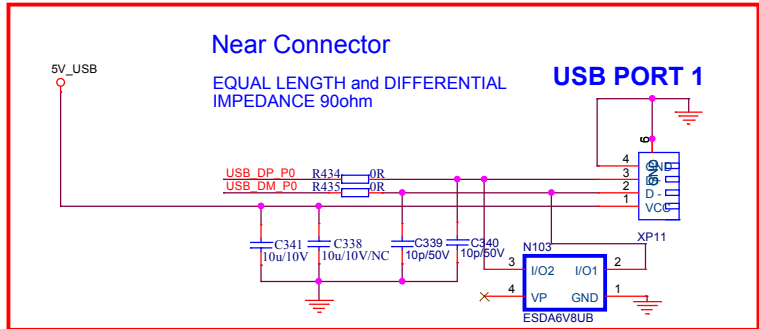
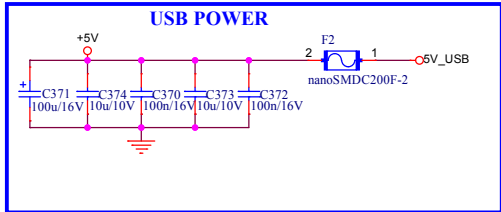
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<< USB_DP_P1
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<< WIFI_EN

<< GND

4
4
4
4
4

USB_DP_P0
USB_DM_P0
USB_DP_P1
USB_DM_P1
WIFI_EN



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