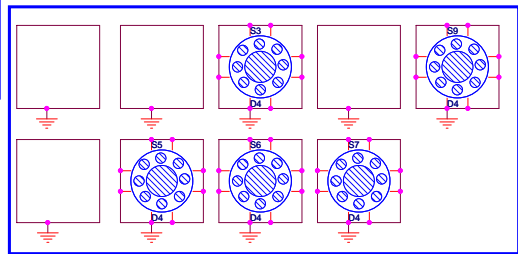
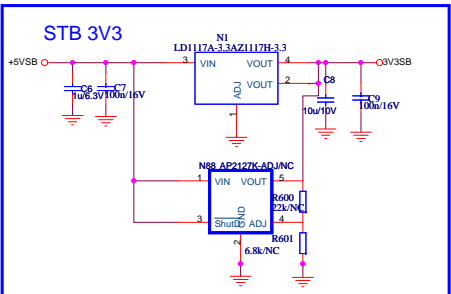
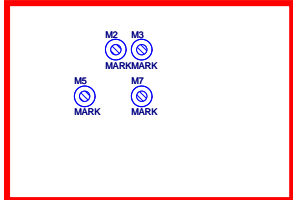
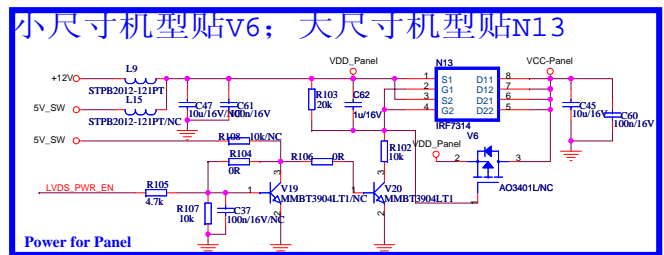
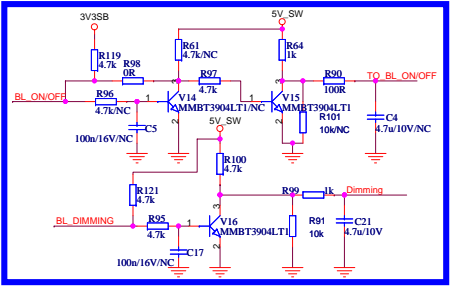
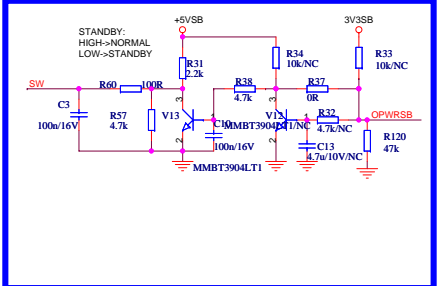
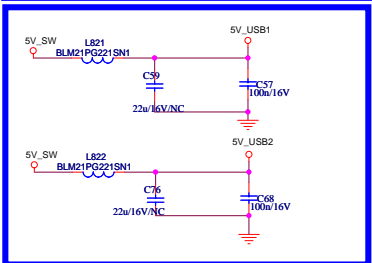
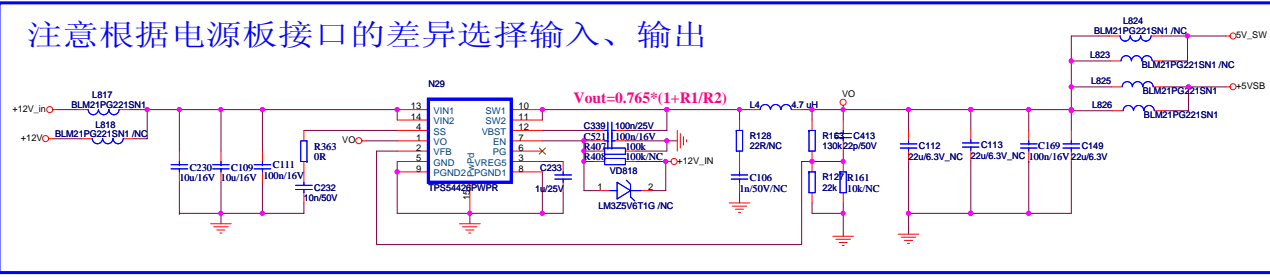
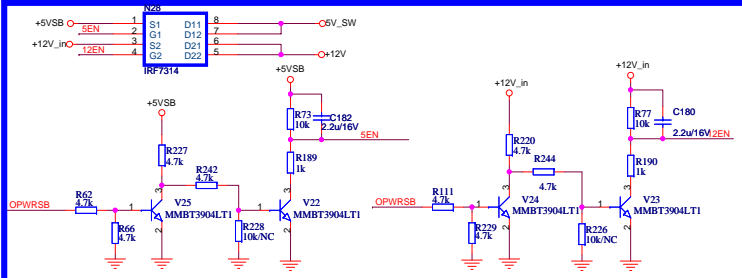
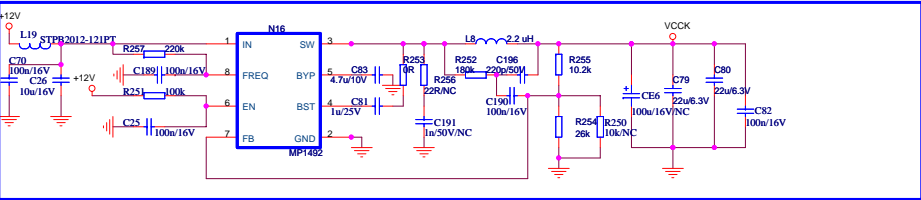
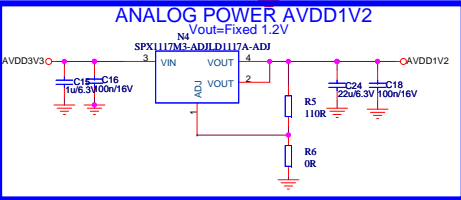
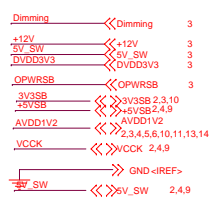
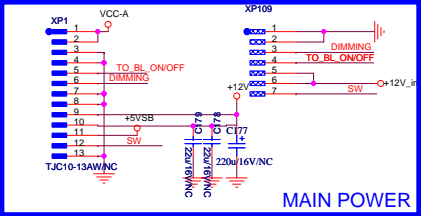
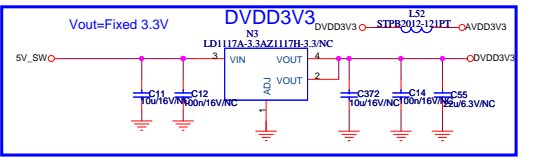
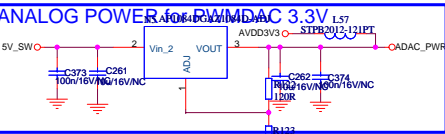


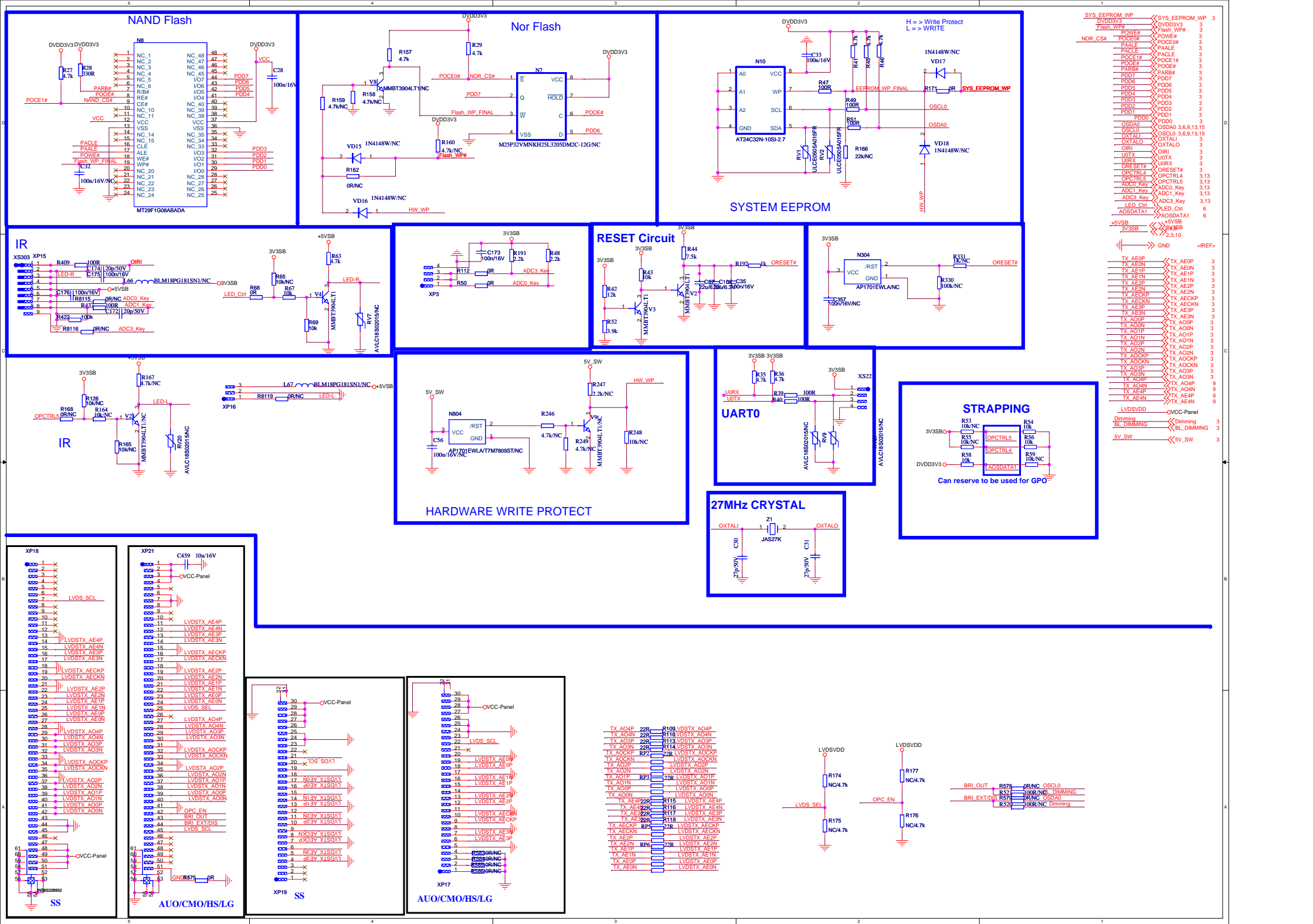
# MT5301C (128M\*8bit DDR3) VERSION V1.0

Dram	
DRAM Type	DDR3
Single DRAM Chip Size	2G bit(128M*8*2)
DRAM Chip Data Rate SPEC	1333MHz
Number of DRAM	200%
Flash	
First Serial Flash	64Mbit
NAND Flash	2Gbit
Tuner	
Tuner Type	DTMB
Tuner 30V PWM (O)	NA
RF_AGC (I)	NA
RF_AGC (O)	RF_AGC
IF_AGC (O)	IF_AGC
IF_AGC_SW	JTDO
AFT (I)	NA
External Demod/TS	
DEMODO_TSCLK	DEMODO_TSCLK
DEMODO_TSData	DEMODO_TSData
DEMODO_TSSYNC	DEMODO_TSSYNC
DEMODO_TSVAL	DEMODO_TSVAL
Demod_RST	Demod_RST
Panel CTRL	
LVDS Power Switch CTRL (O)	CL_INT
LVDS Status CTRL_0 (O)	NA
LVDS Status CTRL_1 (O)	NA
Back Light CTRL (O)	SPI_CLE
Dimming CTRL (O)	OPWM0
EEPROM WP	
SYS EEPROM WP (O)	GPIO14 (H=Write Protect)
SYS EEPROM Size	32Kbit
USB GPIO	
USB0 PWR Enable (O)	NA
USB0 Over Current (I)	NA
USB1 PWR Enable (O)	NA
USB1 Over Current (I)	NA
HDMI CTRL	
HDMI HPD1 (O)	HDMI_HPD (L= EEPROM available)
HDMI HPD2 (O)	OPCTRL4
HDMI HPD3 (O)	OPCTRL5
HDMI HPD4 (O)	NA
HDMI_5V_Detect 1 (I)	PWR5V (H= Plug-In)
HDMI_5V_Detect 2 (I)	ADIN2
HDMI_5V_Detect 3 (I)	ADIN3
HDMI_5V_Detect 4 (I)	NA
HDMI_SCL1	HDMI_SCL
HDMI_SCL2	OPCTRL0
HDMI_SCL3	OPCTRL2
HDMI_SDA1	HDMI_SDA
HDMI_SDA2	OPCTRL1
HDMI_SDA3	OPCTRL3
Audio Input Source	
AIN0	Exernal MUX input chip
Exernal MUX CTRL0	JTMS
Exernal MUX CTRL1	JTCK
Exernal MUX CTRL2	AOSDATA1
Audio DAC Out	
Audio DAC0 (PWM0)	AV Audio Out
Audio DAC1 (PWM1)	Headphone out

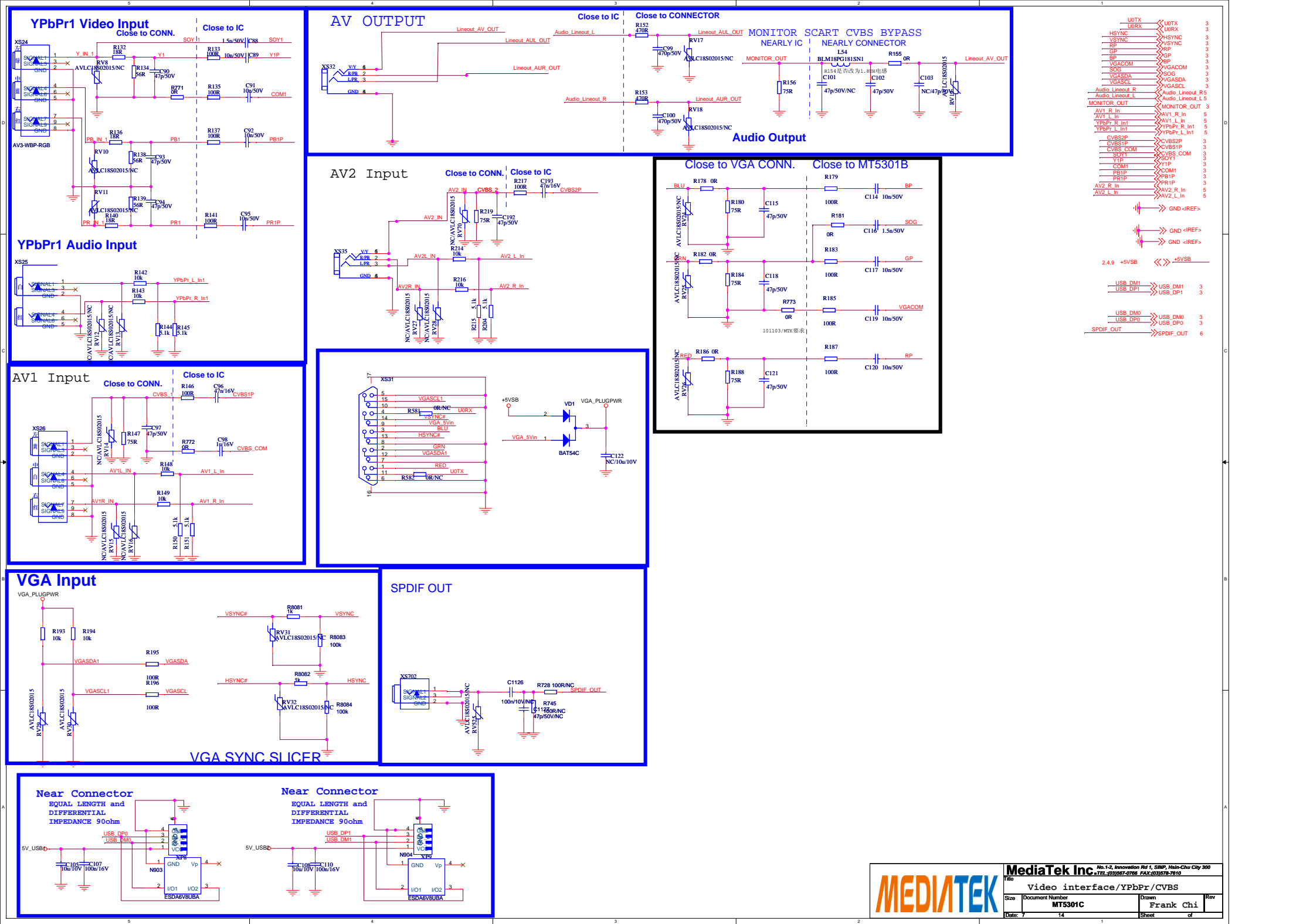
I2S Out	
I2S MCLK(O)	AOMCLK
I2S BCLK(O)	AOBCK
I2S LRCK(O)	AOLRCK
I2S DATA0(O)	AODATA0 (Speaker Out)
Audio CTRL	
All_MUTE_CTRL (O)	ADIN4 (H = UN-MUTE)
Head Phone DET (I)	SPI_DATA(L = Plug-In)
SPDIF IN (I)	NA
Speaker Amp CTRL	
Speaker AMP Type	AD82581B ( Digital SP AMP )
Speaker Amp Mute (O)	JTRST (L =MUTE)
AV Input Source	
CVBS0	CVBS0P
CVBS1	SC0
CVBS2	SY1
SY0	CVBS1P
SC0	SY0
SY1	NA
SC1	NA
YPbPr Input Source	
SOY0	SOY0
Y0P	Y0P
PB0P	PB0P
PR0P	PR0P
SOY1	SOY1
Y1P	Y1P
PB1P	PB1P
PR1P	PR1P
Video Out	
SCT Monitor CVBS Out	VDAC_OUT1
SCT TV CVBS Out	NA
Other	
OPWRSB (O)	OPWRSB (L=PWR On)
VCXO CTRL (O)	NA
Ethernet Port (MII/RMII)	
TXEN	TXEN
TXD0	TXD0
TXD1	TXD1
TXD2	NA
TXD3	NA
TXCLK	TXCLK
COL	COL
RXDV	RXDV
RXD0	RXD0
RXD1	RXD1
RXD2	NA
RXD3	NA
RXCLK	RXCLK
PHY_REF_CLK	PHY_REF_CLK
MDC	MDC
MDIO	MDIO
PHY RESET#	SPI_CLK











DDR3 X8 #2

