

er Board

er Board

5Vstb

R12 1K

R13 10K

R15 NC

R16 4.7K

ON-PBACK

ON-PBACK

1

V4 3904

3

2

BL-ON/OFF

R18 1K

BL-ADJ

V6 3904

3

2

R21 4.7K

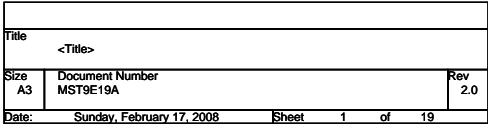
ADJ-PWM3

ADJ-PWM3

1

C10 2.2uF

Ground



$$0.81 \times (1 + R_{up}/R_{down}) = 1.8V \text{---} R_{up} = 10K, R_{down} = 8.2K$$

Vcc3.3for MST9E19A analog

Vcc1.8 for MST9E19A

+3.3AVDD for AVDD_HDMI

+3.3AVDD for AVDD_AU

+3.3AVDD for AVDDA

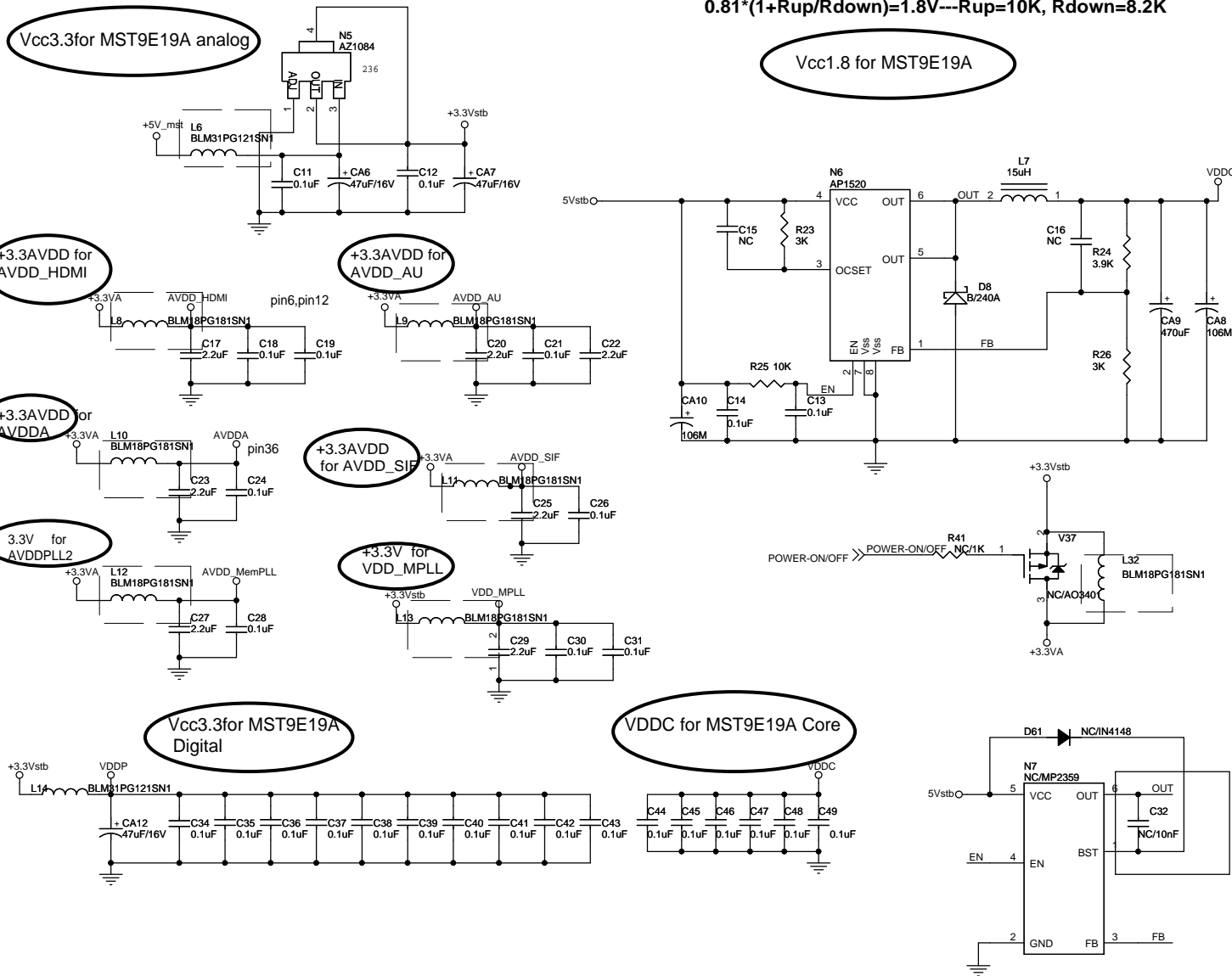
+3.3AVDD for AVDD_SIF

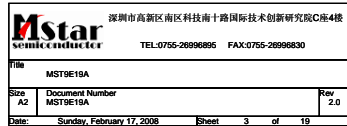
3.3V for AVDDPLL2

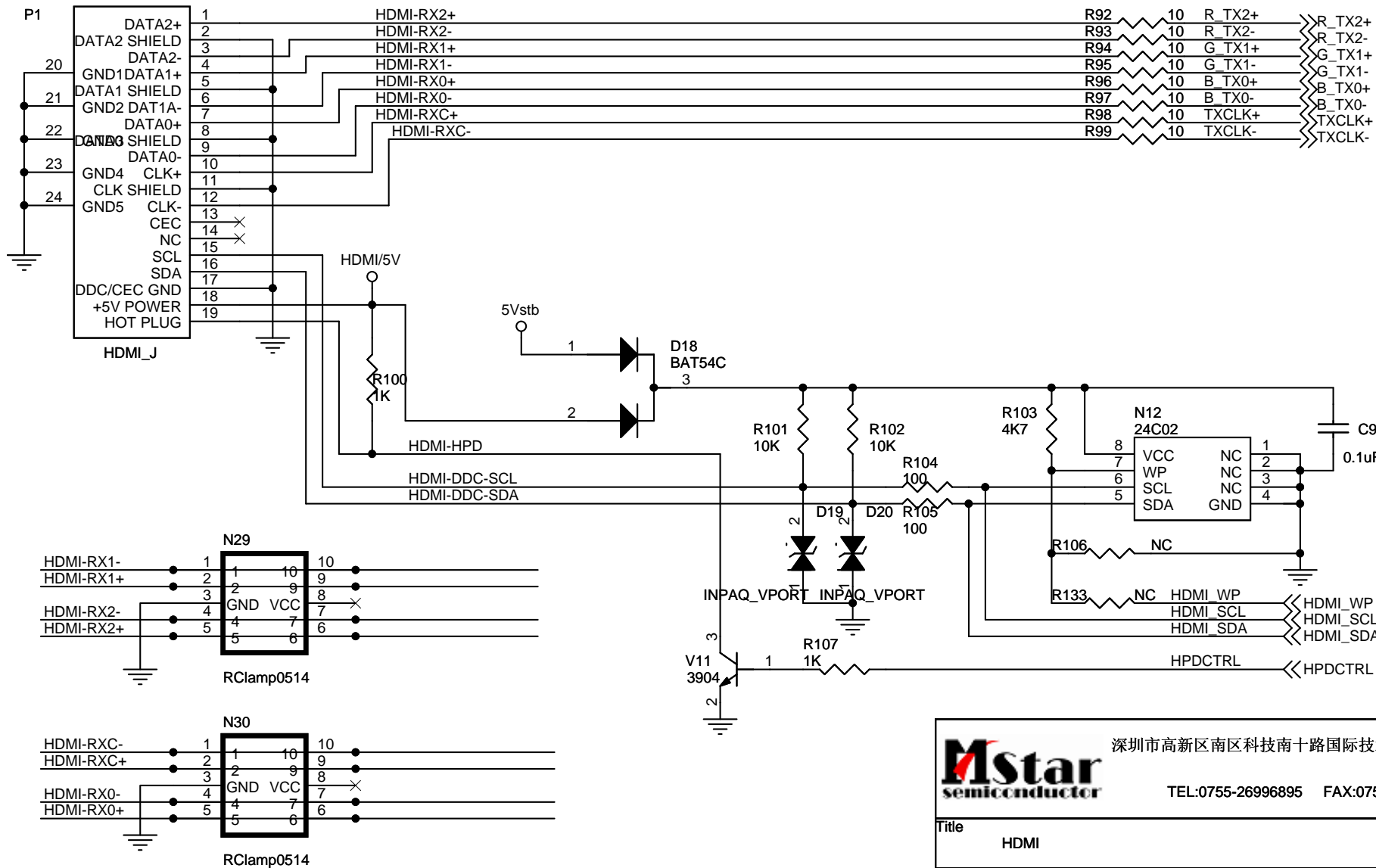
+3.3V for VDD_MPLL

Vcc3.3for MST9E19A Digital

VDDC for MST9E19A Core



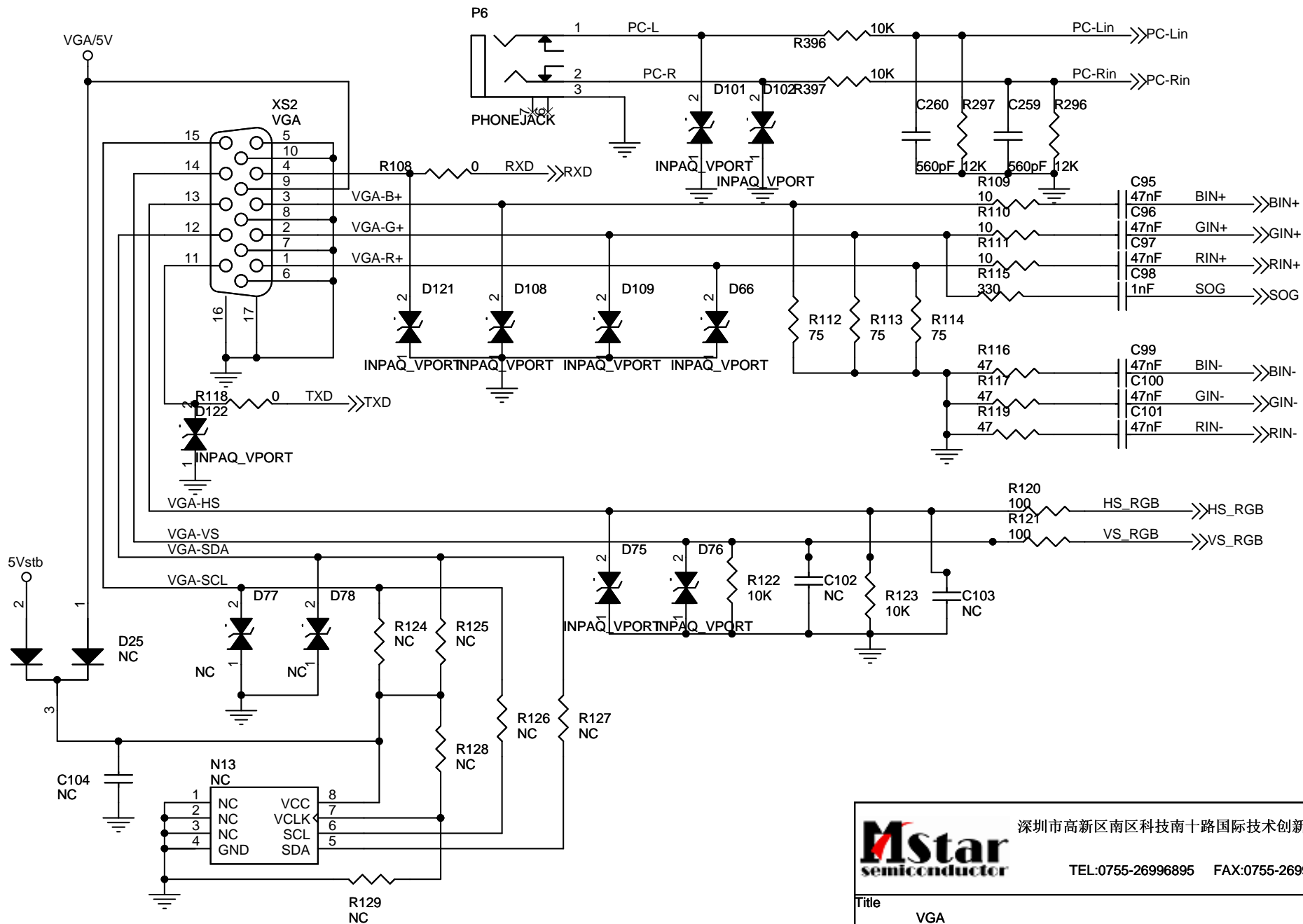




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Title		
HDMI		
Size	Document Number	Rev
A	MST9E19A	2.0
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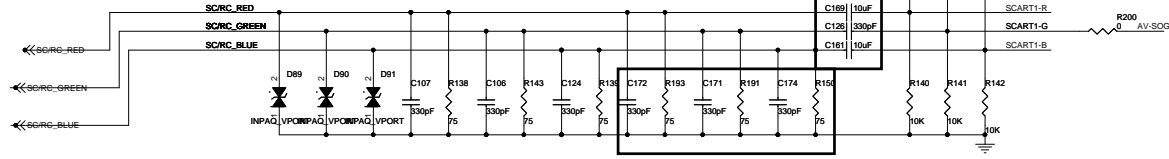
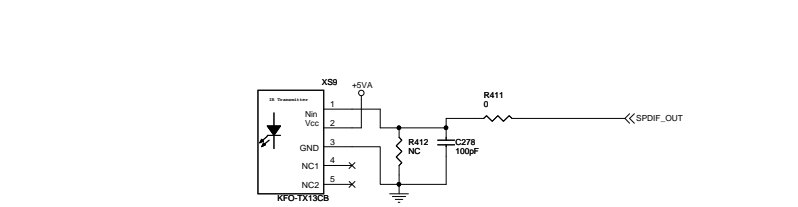
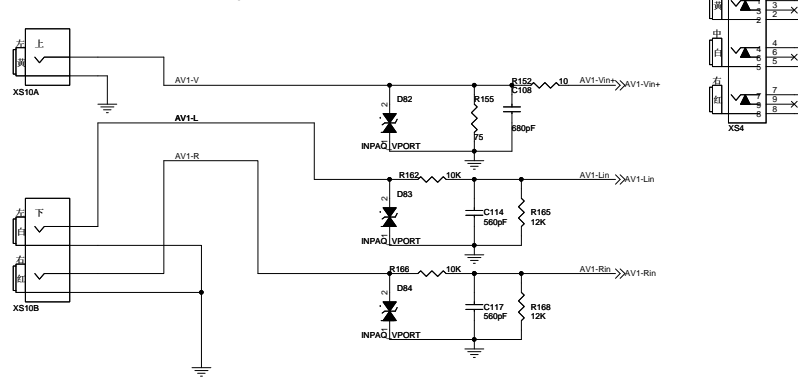
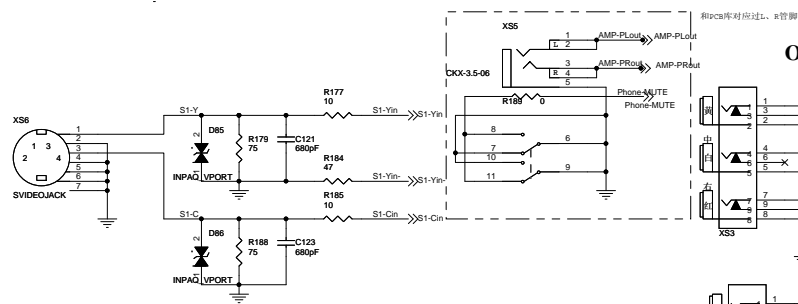
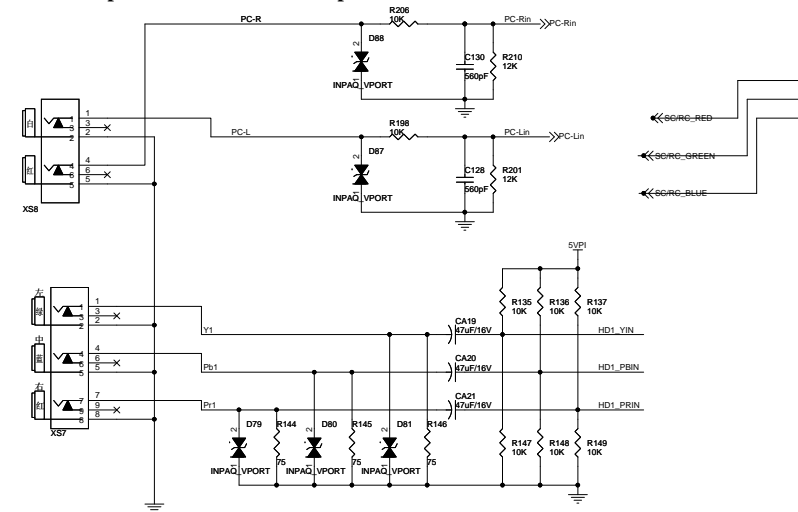


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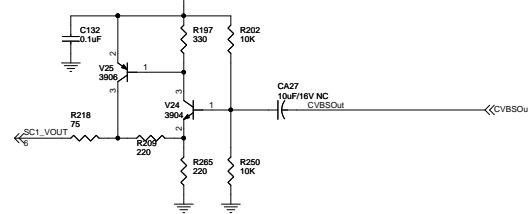
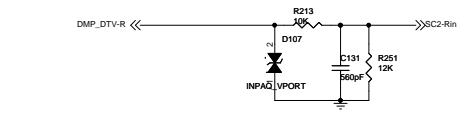
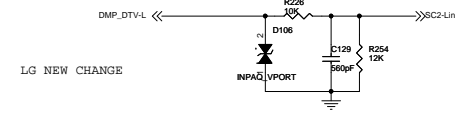
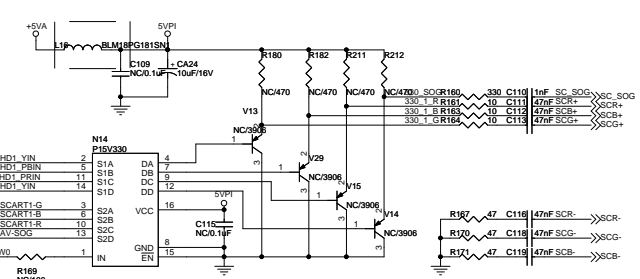
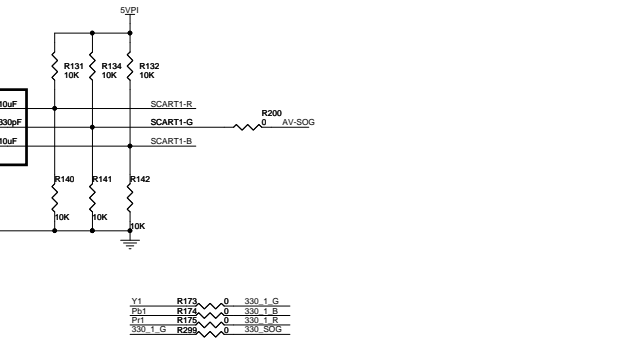
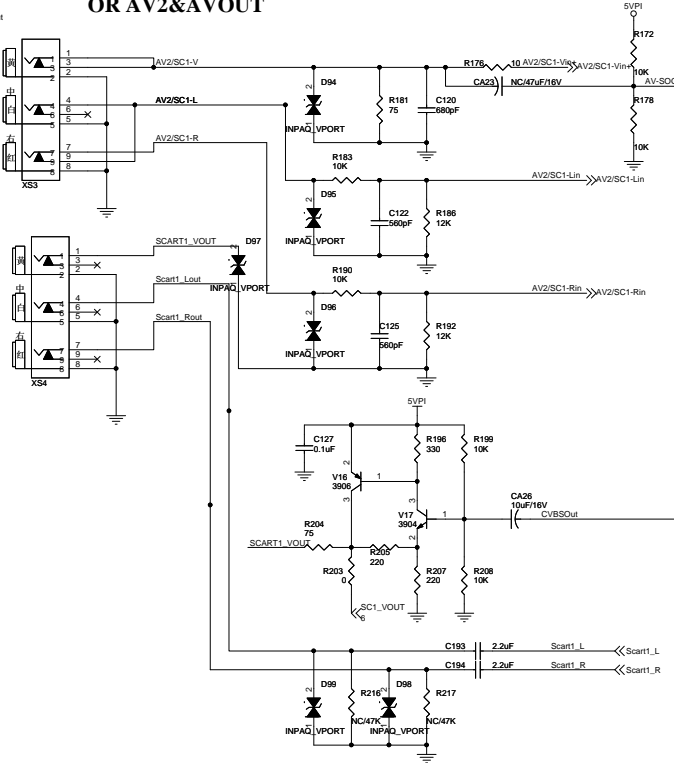
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Title VGA		
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PC Audio Input



OR AV2&AVOUT

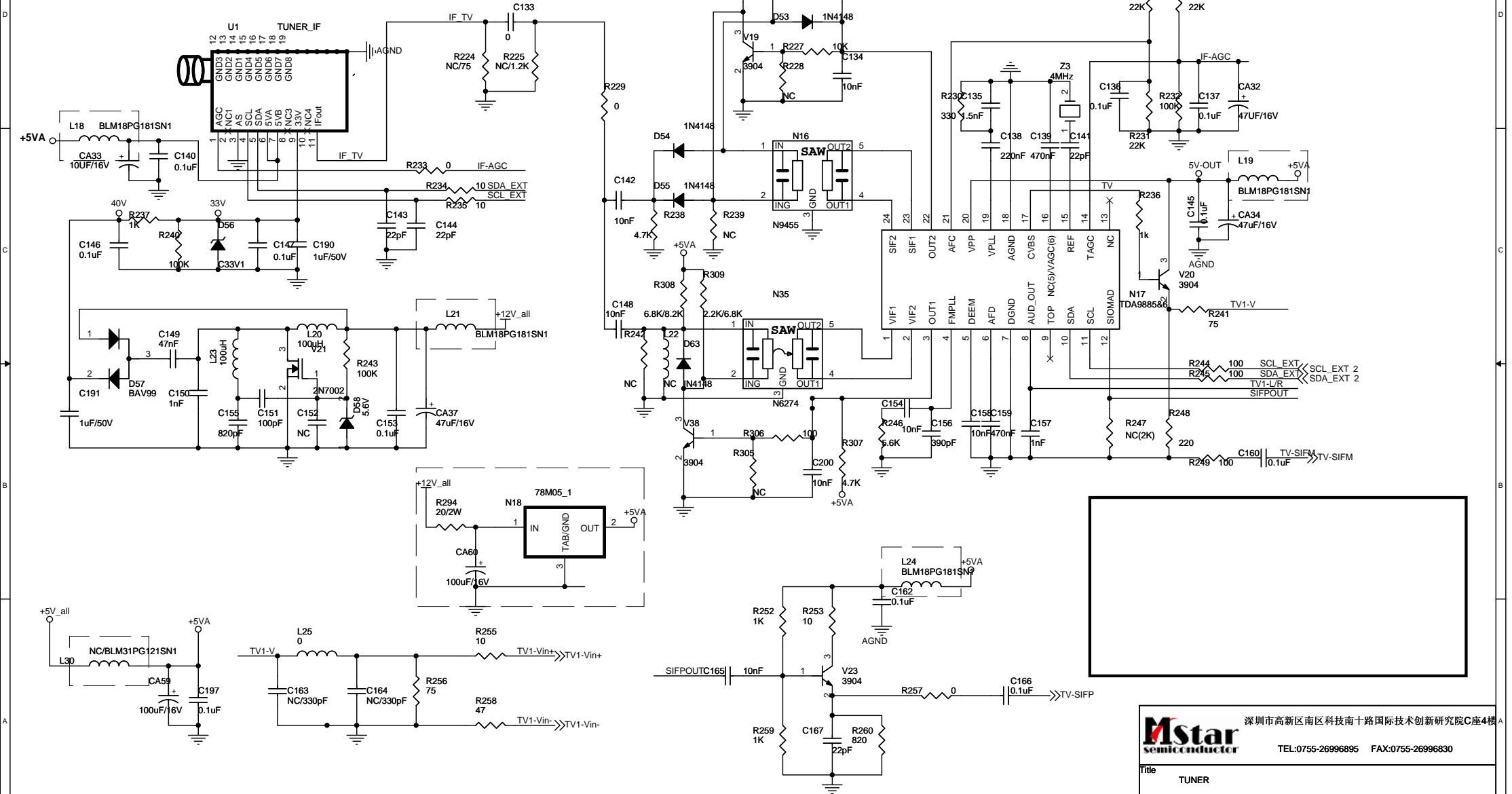


U23 PIN2 connected to PIN1 B/G,DK,I,L
U23 PIN2 connected to GND L'

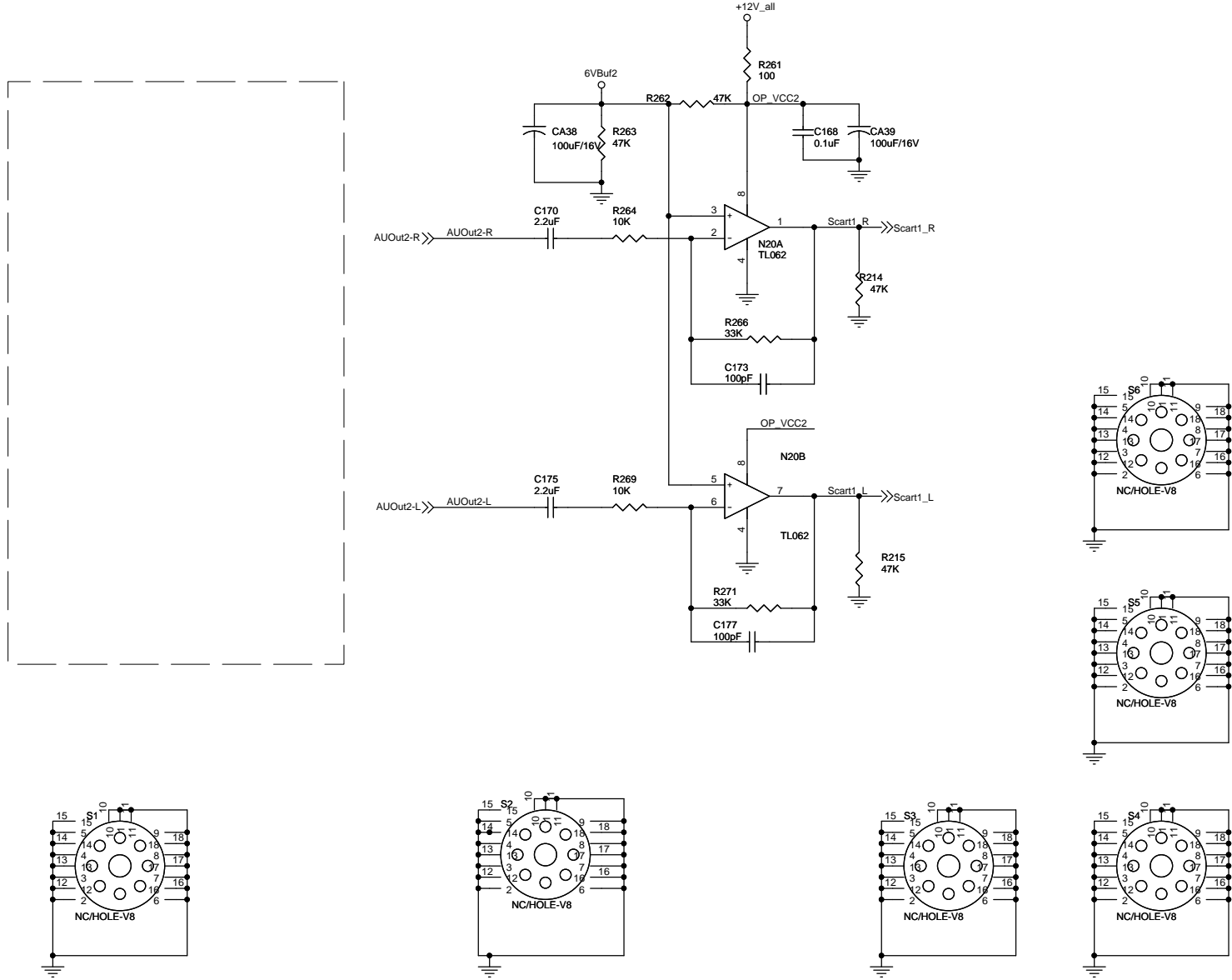
I2C ADDRESS(R263 NC):

READ is 0X87

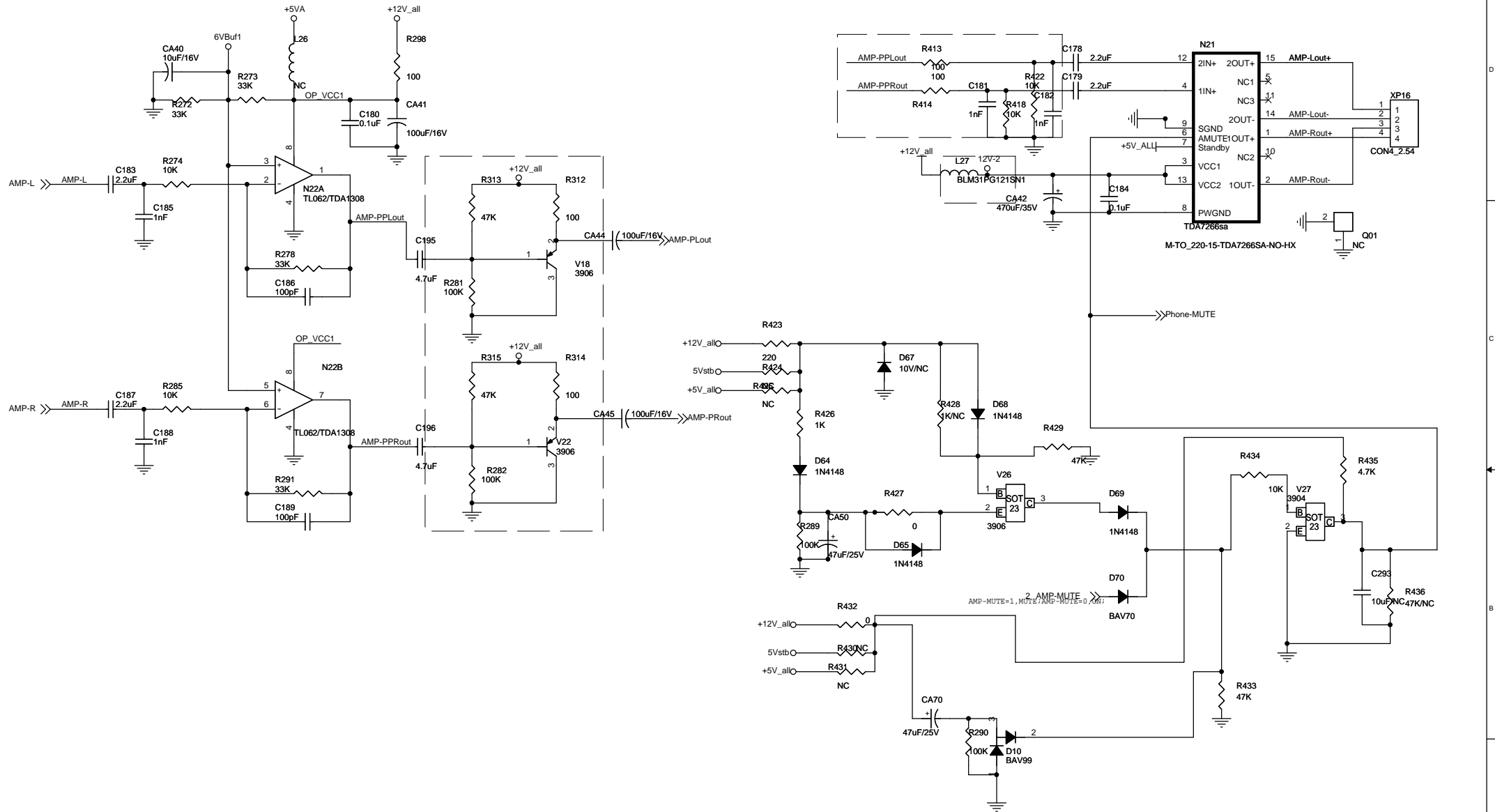
WRITE is 0X86



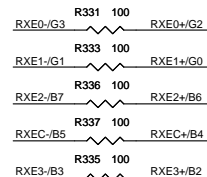
Audio OP(AUOUTL3/AUOUTR3; AUOUTL2/AUOUTR2)



FOR TDA1308/PT2308 L26 FB,R298 NC;
FOR TL062 L26 NC,R298 0;

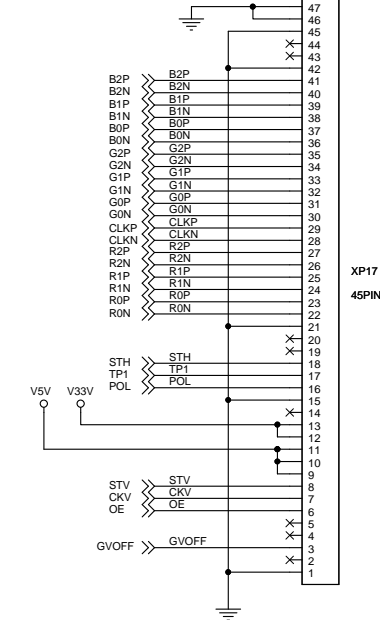
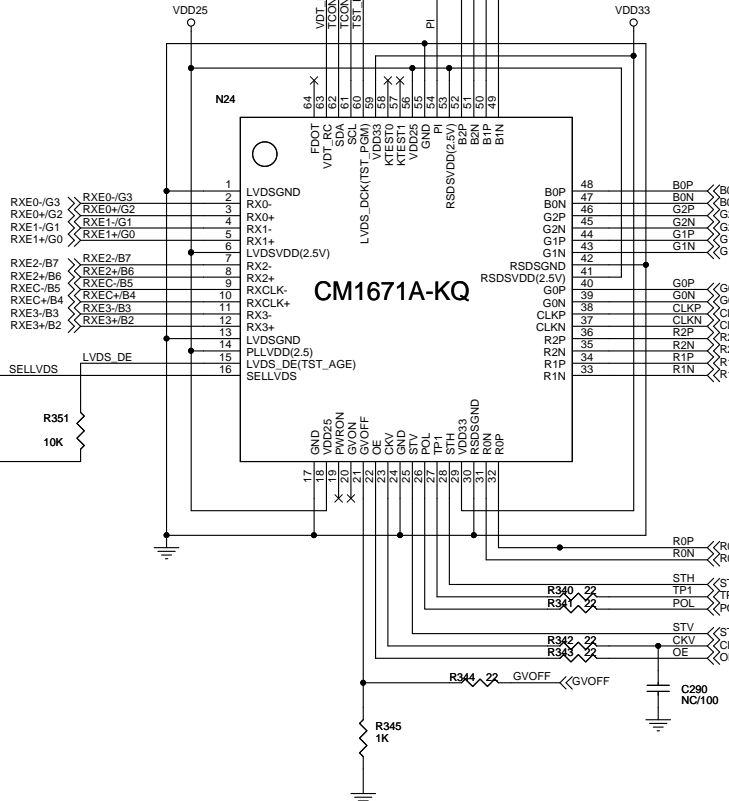
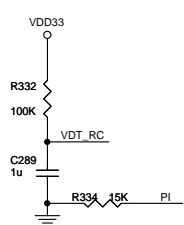


[Terminator near T-CON]

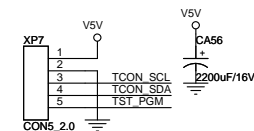


[ASIC Control]

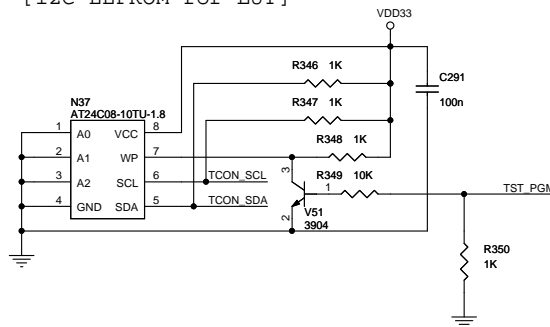
[T-CON]



Debug port



[I2C EEPROM for LUT]



[Filter near T-CON]

