

Vcc3.3for MST9E19A analog

$$0.81 \times (1 + R_{up}/R_{down}) = 1.8V \text{---} R_{up} = 10K, R_{down} = 8.2K$$

Vcc1.8 for MST9E19A

+3.3AVDD for AVDD_HDMI

+3.3AVDD for AVDD_AU

+3.3AVDD for AVDDA

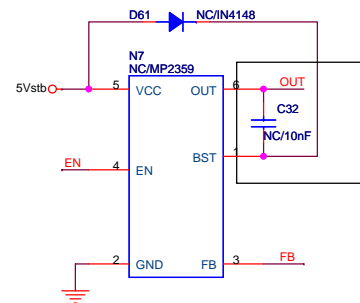
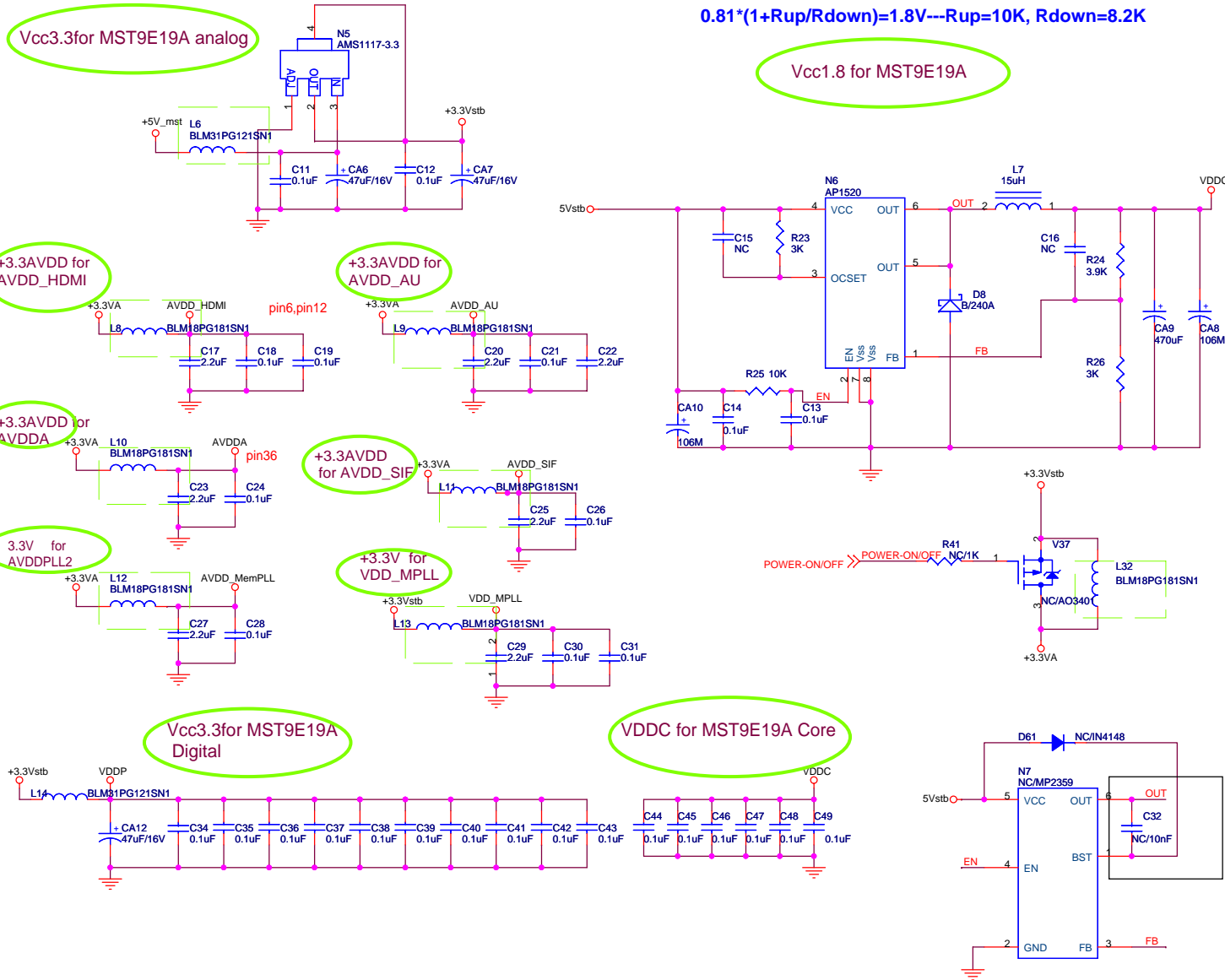
+3.3AVDD for AVDD_SIF

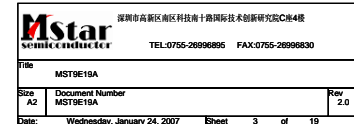
3.3V for AVDDPLL2

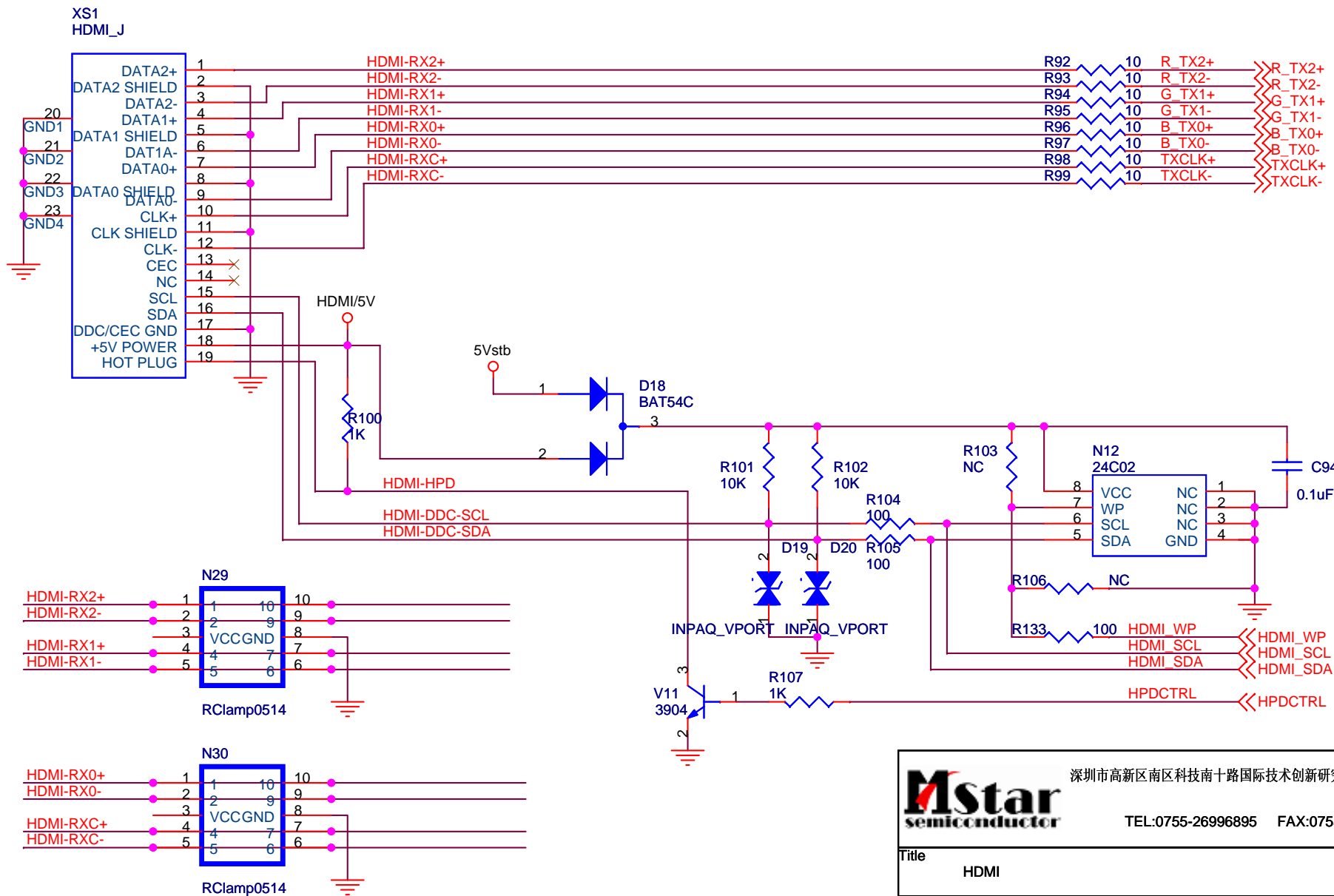
+3.3V for VDD_MPLL

Vcc3.3for MST9E19A Digital

VDDC for MST9E19A Core



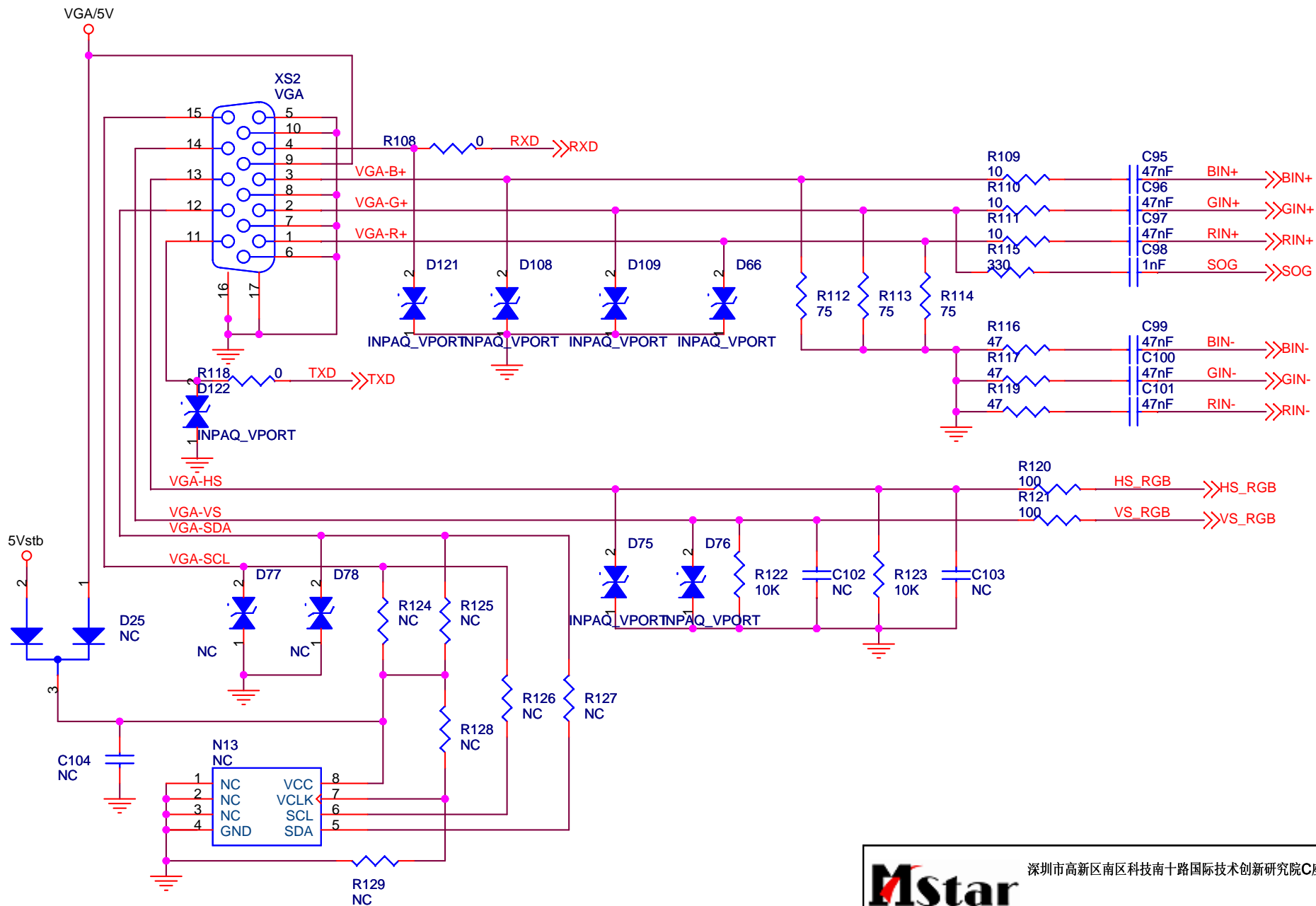




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VGA

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Wednesday, January 24, 2007

Sheet

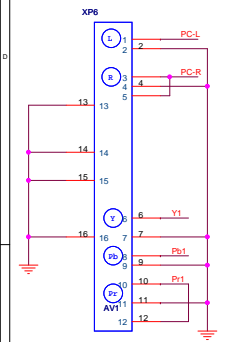
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of

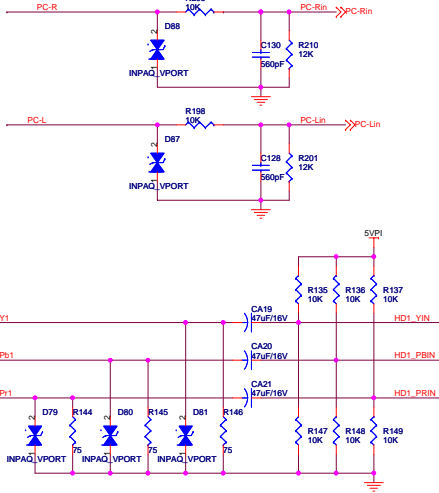
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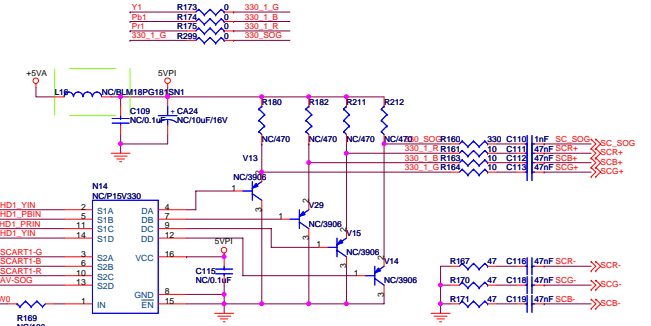
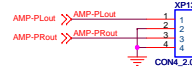
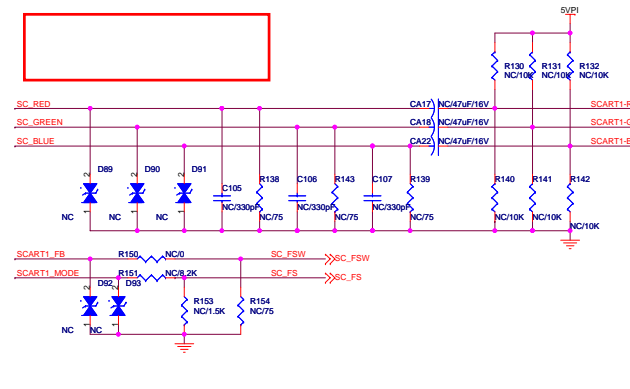
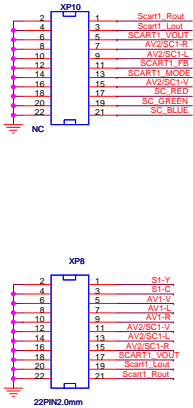
HDTV Input



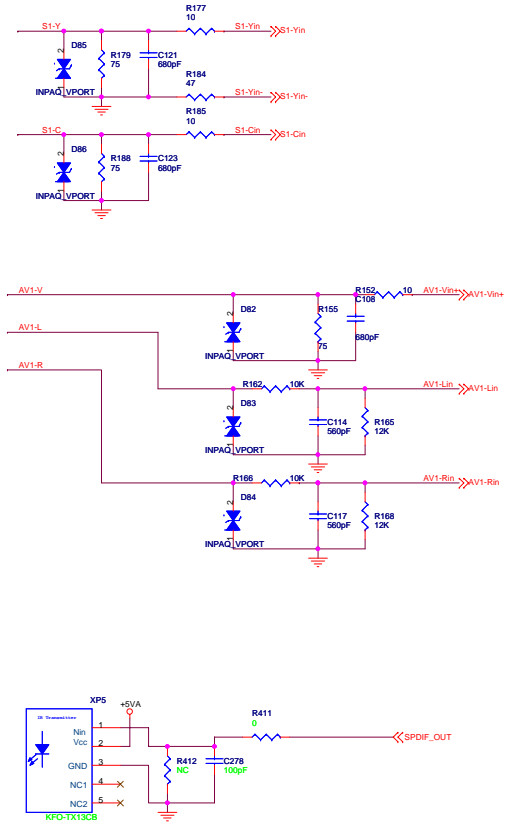
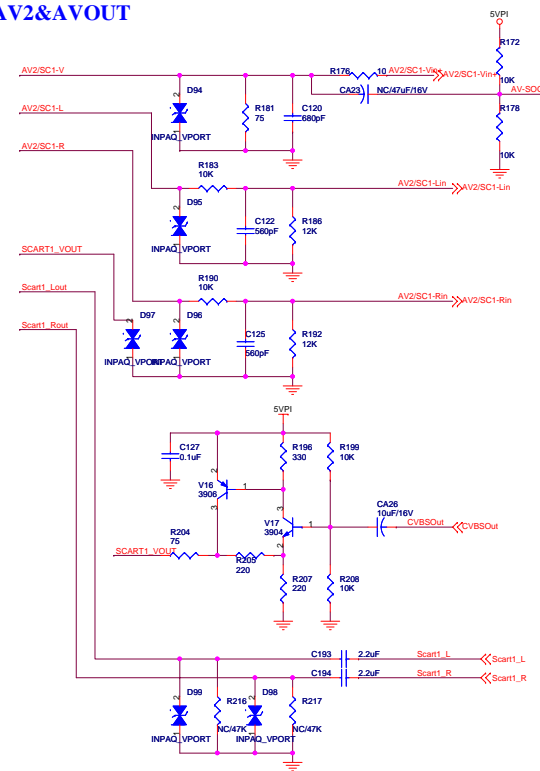
PC Audio Input



SCART1 Input

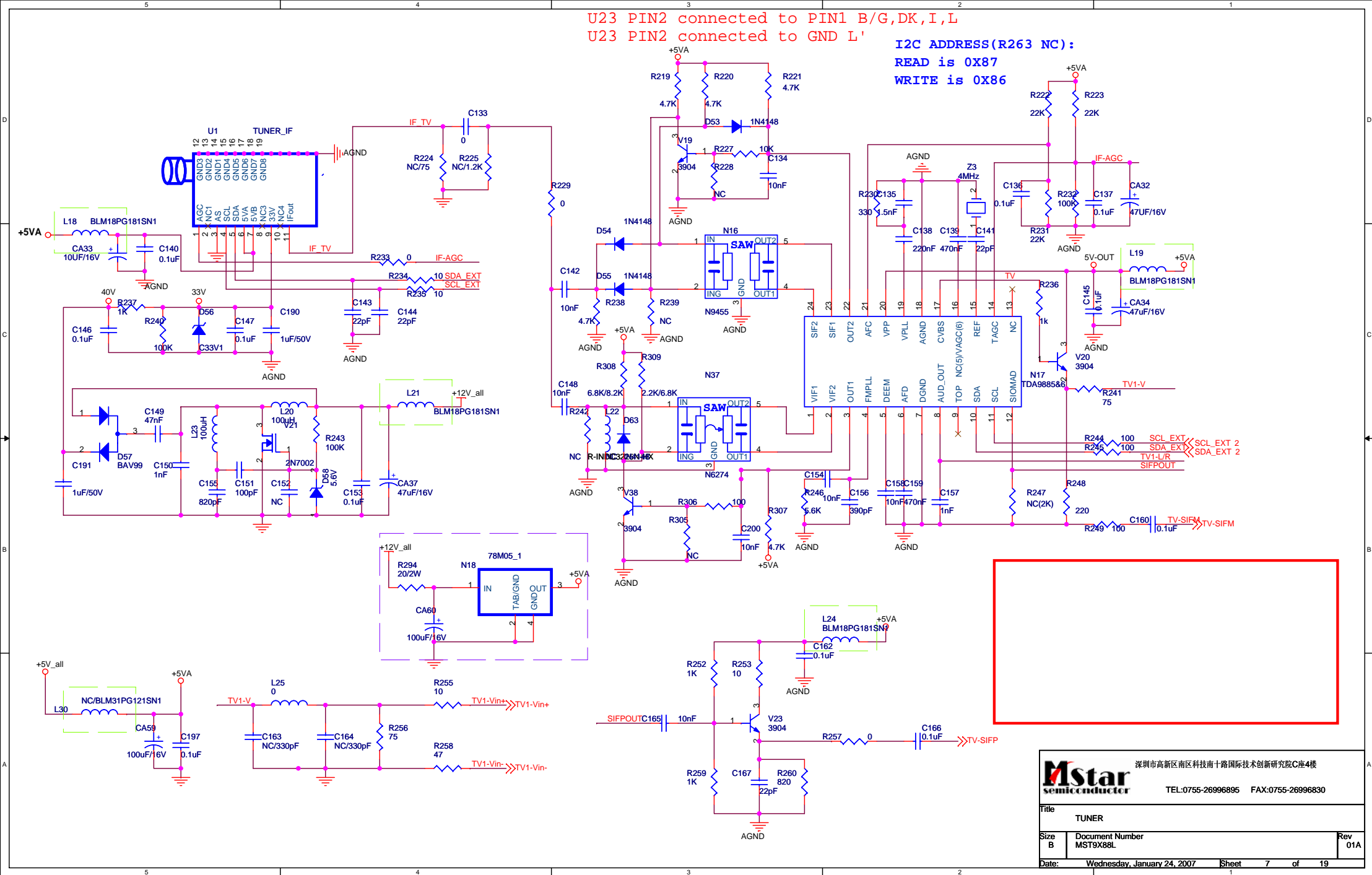


OR AV2&AVOUT

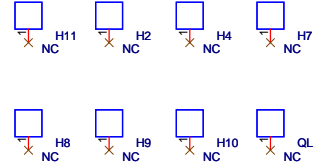
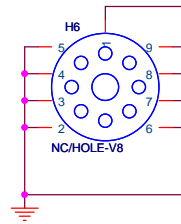
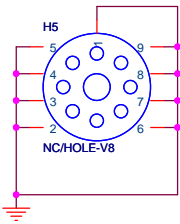
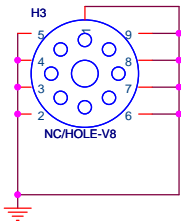
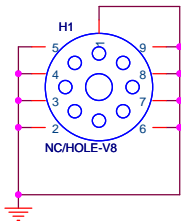
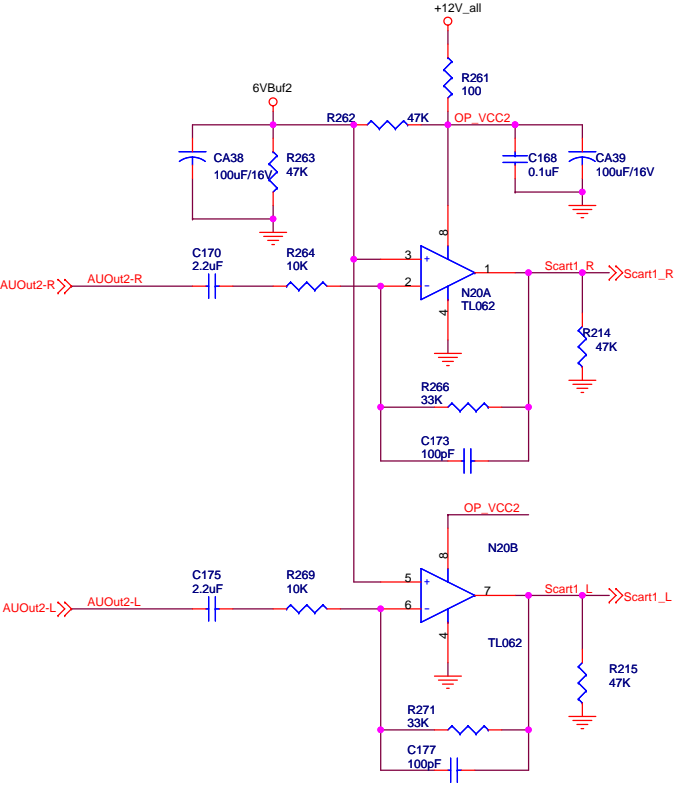


U23 PIN2 connected to PIN1 B/G,DK,I,L
U23 PIN2 connected to GND L'

I2C ADDRESS(R263 NC):
READ is 0X87
WRITE is 0X86



Audio OP(AUOUTL3/AUOUTR3; AUOUTL2/AUOUTR2)



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A3	MST9E19A	0.01	
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FOR TDA1308/PT2308 L26 FB,R298 NC;  
FOR TL062 L26 NC,R298 0;
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