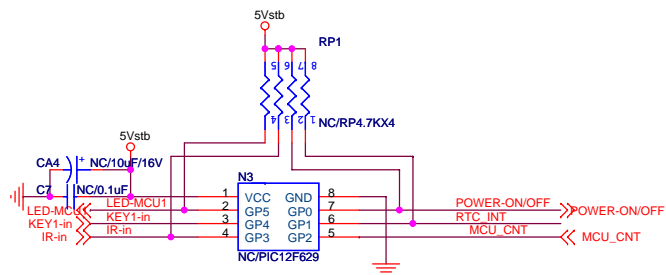
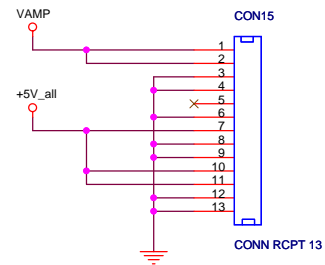
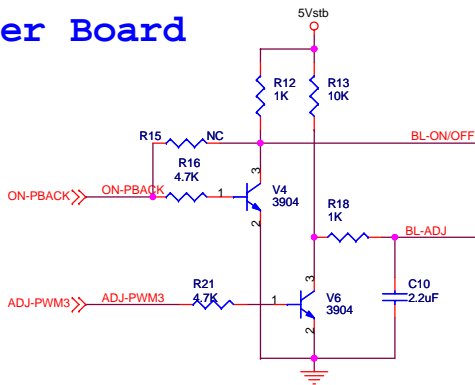


The schematic diagram illustrates the power supply section. It features two voltage regulators: a 5V regulator (U1) and a 12V regulator (U2). The 5V regulator's input is connected to the +5V\_mst pin, and its output provides the +5V\_srb supply. The 12V regulator's input is connected to the +12V\_in pin, and its output provides the +12V\_all supply. A common ground is indicated by a horizontal line at the bottom of the diagram.

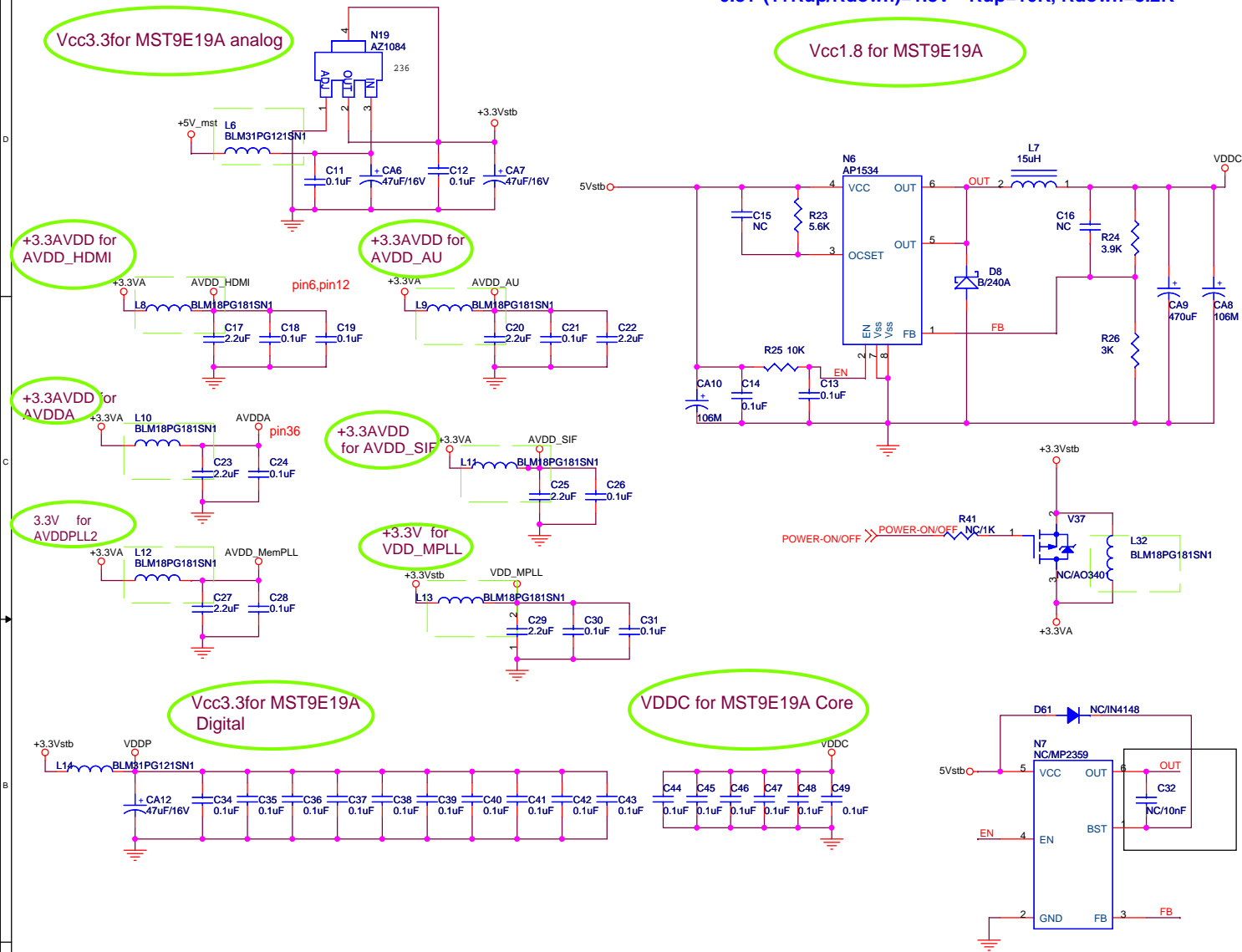


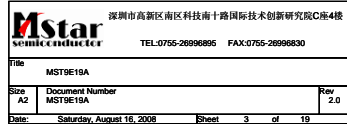
## T0 Inverter Board



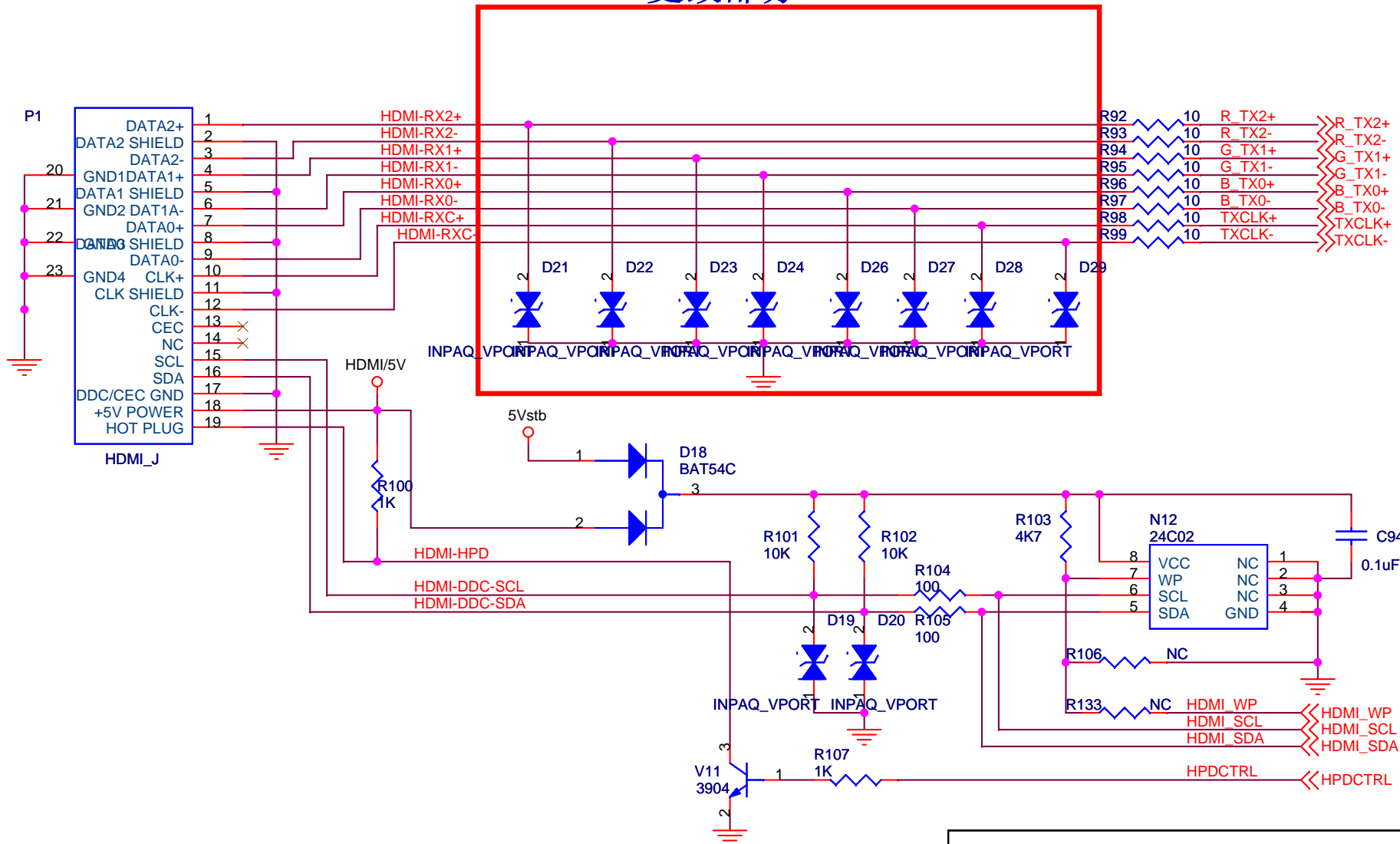
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Size	Document Number	Rev	
A3	MST9E19A	2.0	
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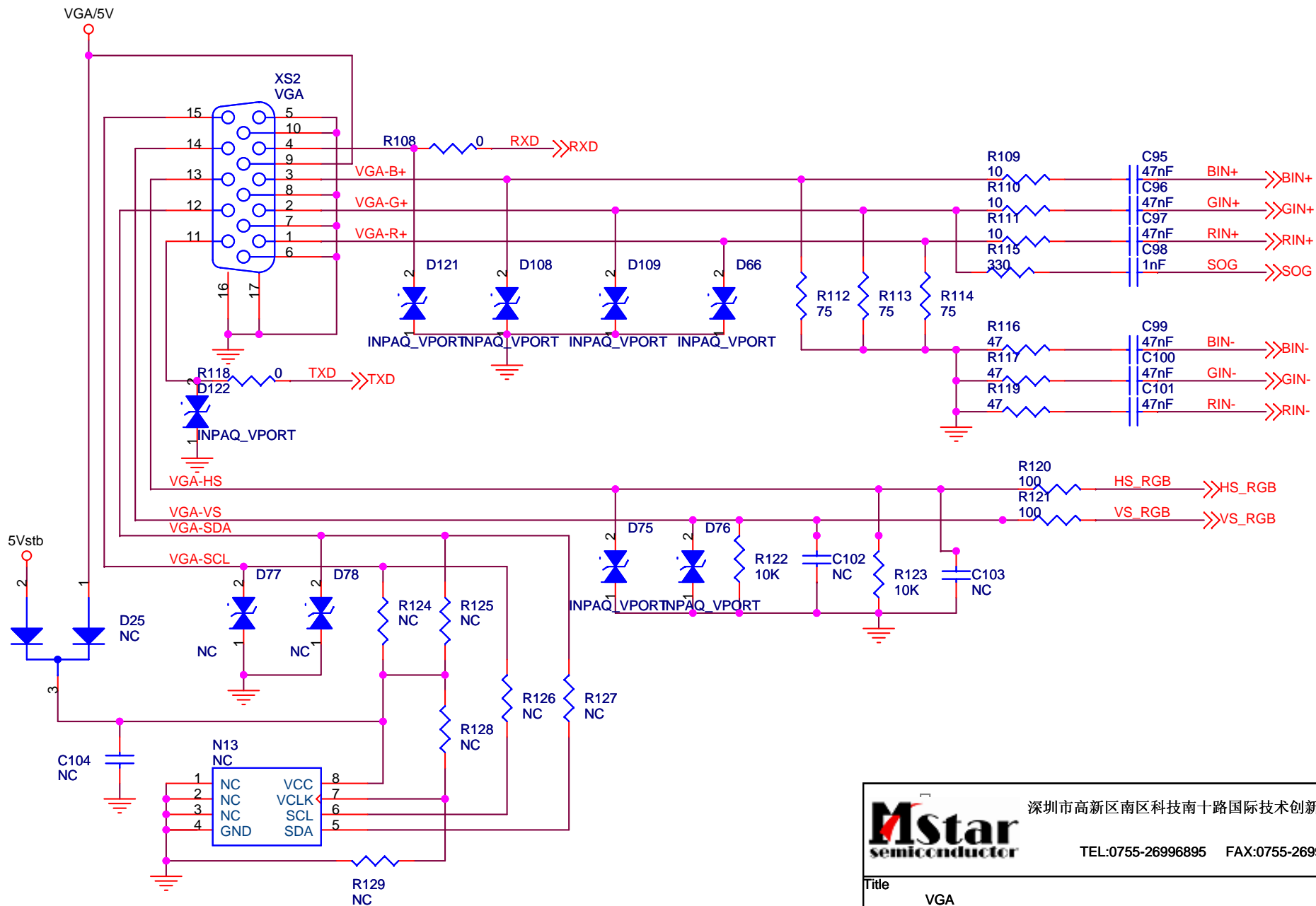
**$0.81 \cdot (1 + R_{up}/R_{down}) = 1.8V$ --- $R_{up}=10K$ ,  $R_{down}=8.2K$**





## 更改部分





**Mstar**  
semiconductor

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Title  
VGA

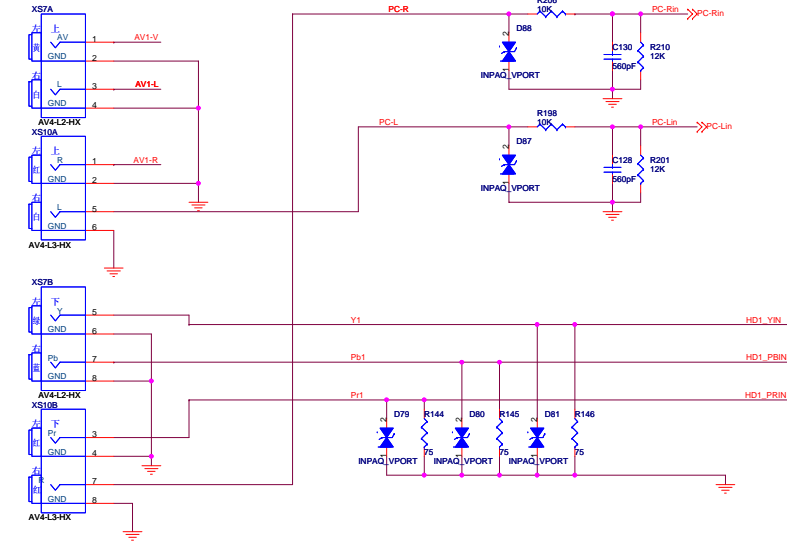
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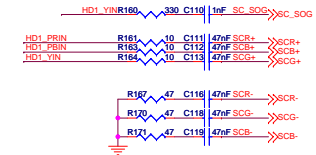
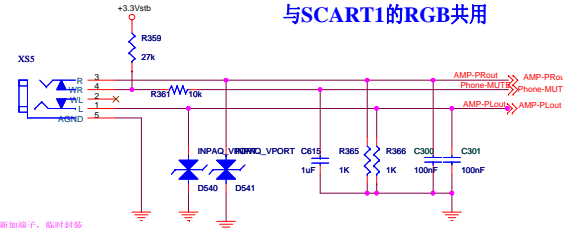


## HDTV Input

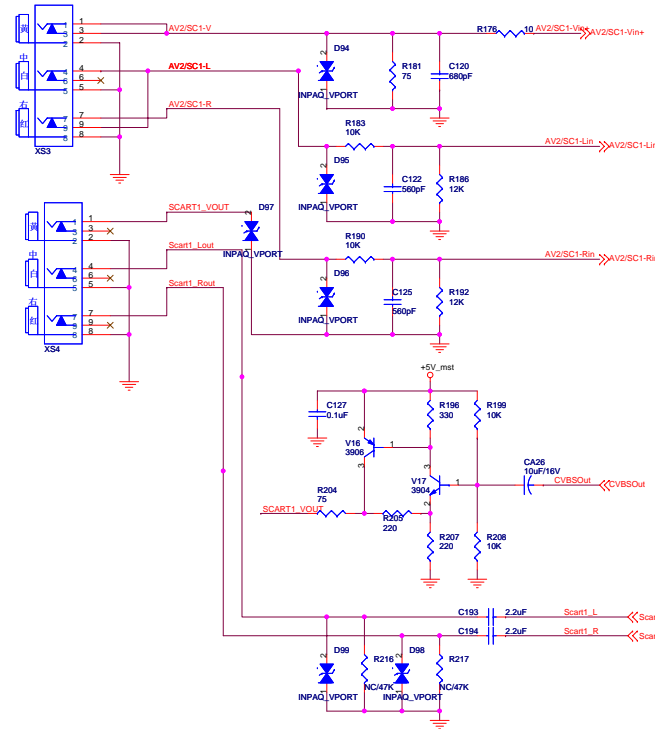
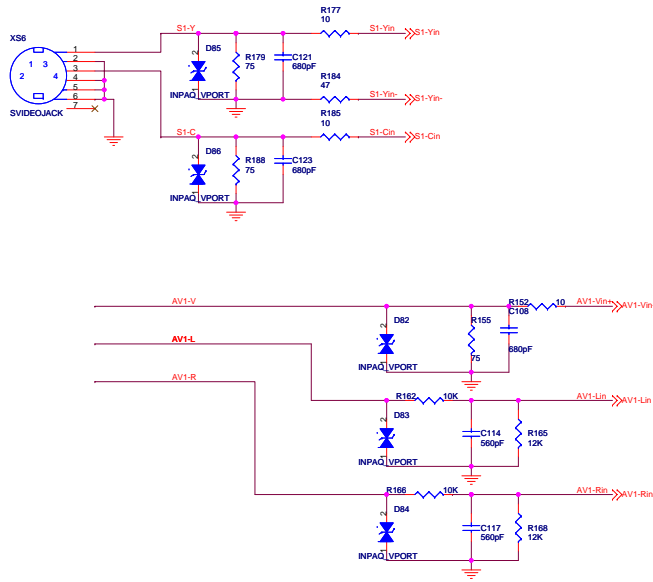
## PC Audio Input



## USB读卡器的图像YPbPr格式 与SCART1的RGB共用

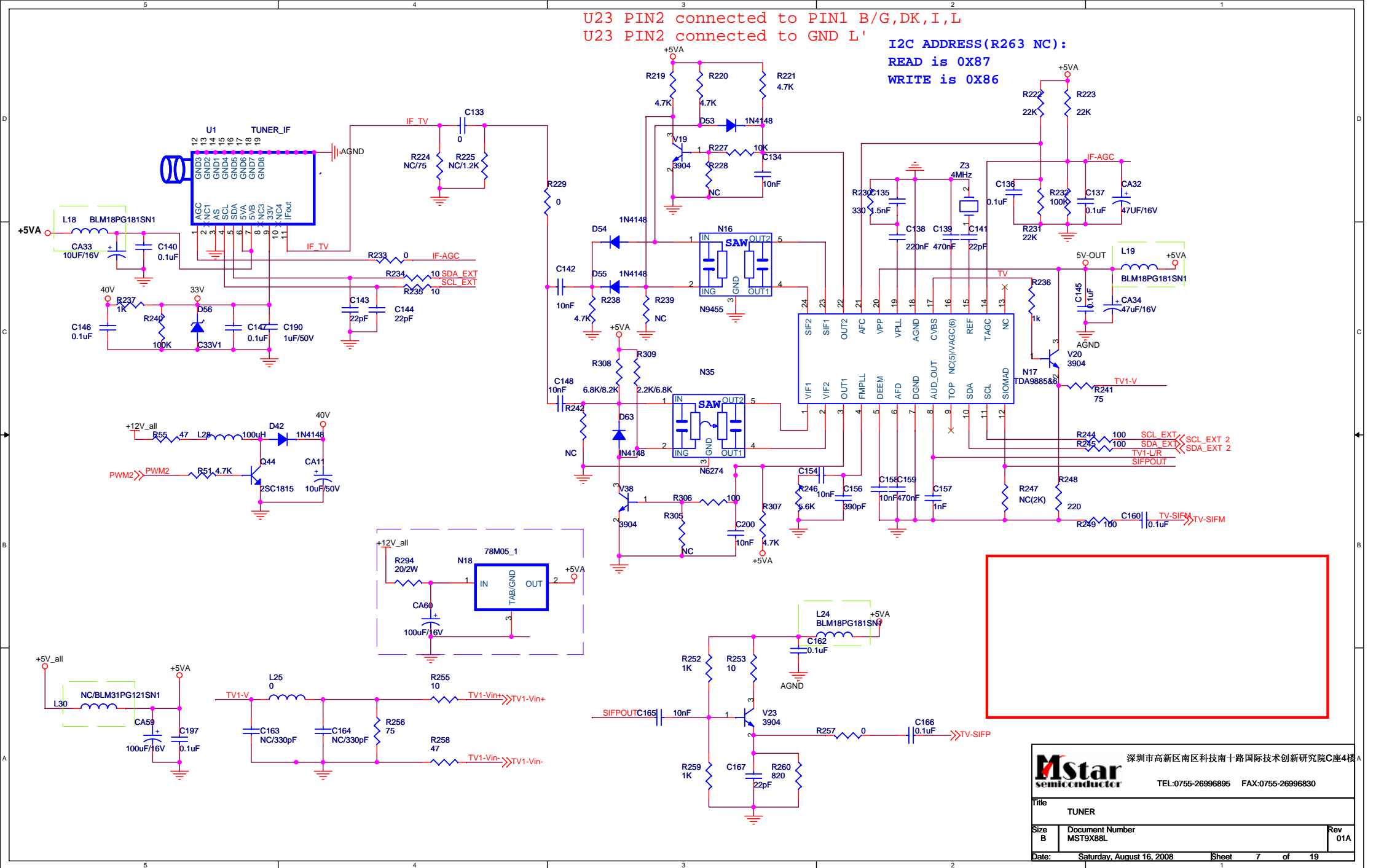


## OR AV2&AVOUT

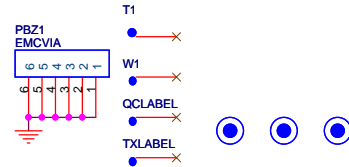
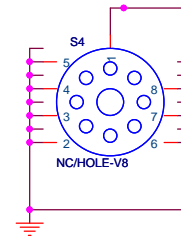
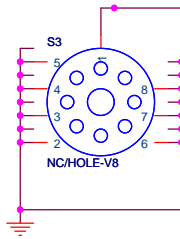
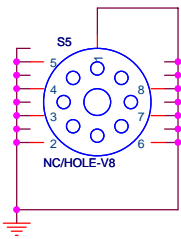
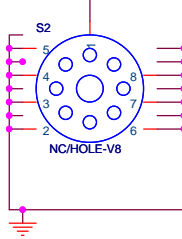
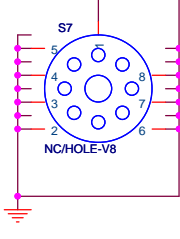
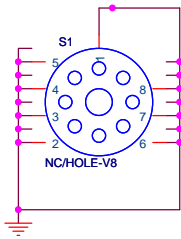
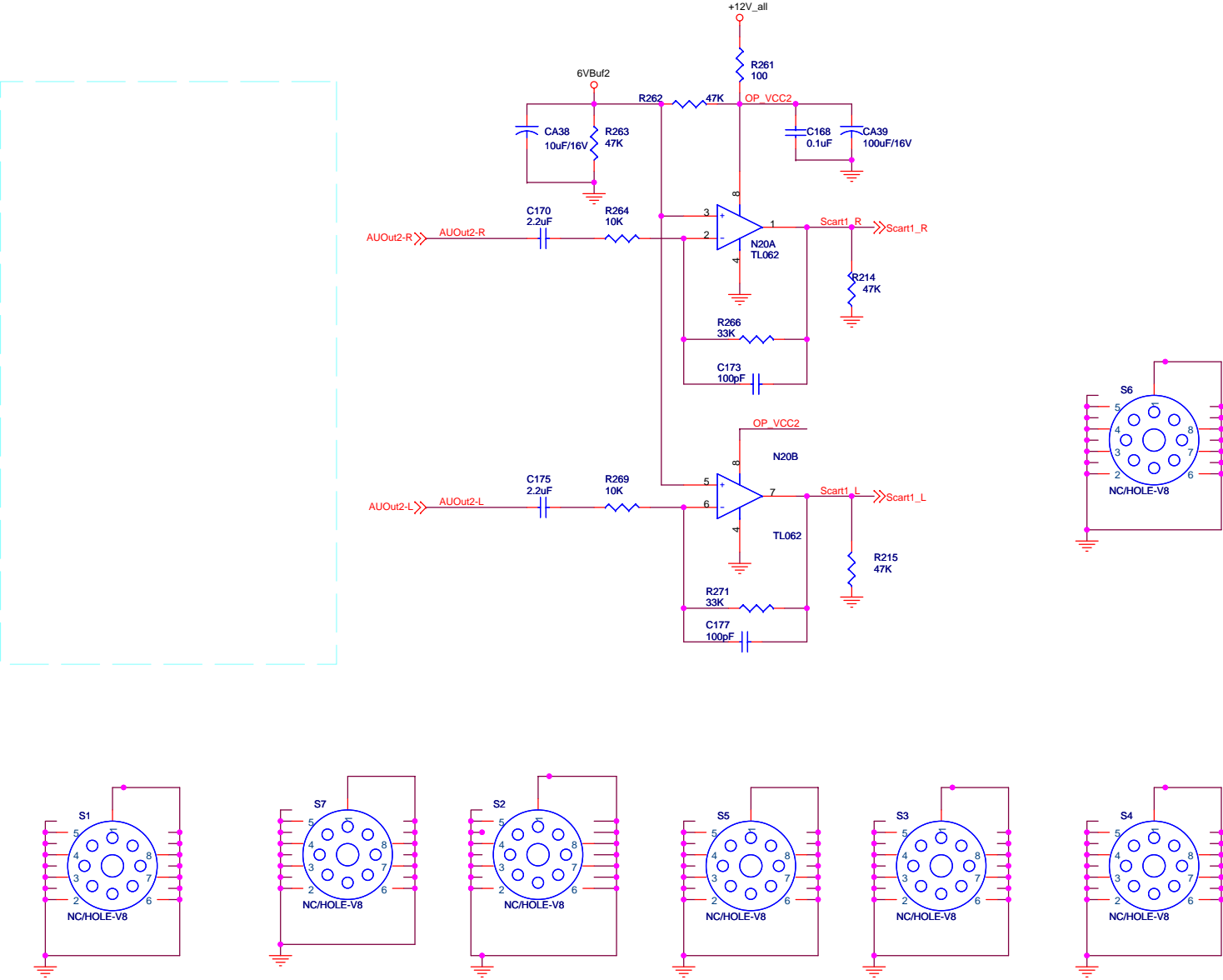


U23 PIN2 connected to PIN1 B/G,DK,I,L  
U23 PIN2 connected to GND L'

I2C ADDRESS(R263 NC):  
READ is 0X87  
WRITE is 0X86



Audio OP(AUOUTL3/AUOUTR3; AUOUTL2/AUOUTR2)



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FOR TDA1308/PT2308 L26 FB,R298 NC;  
FOR TL062 L26 NC,R298 0;

