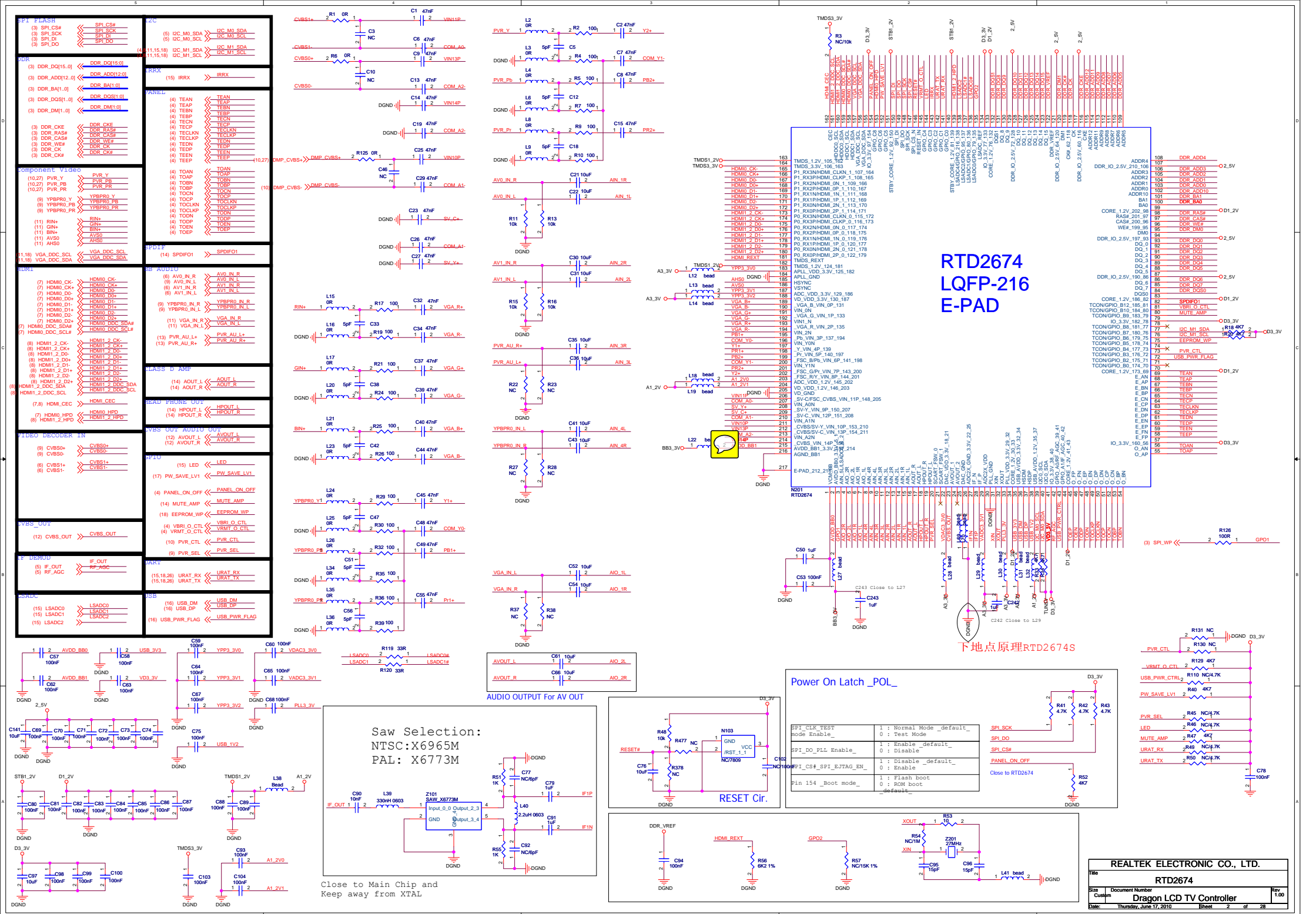
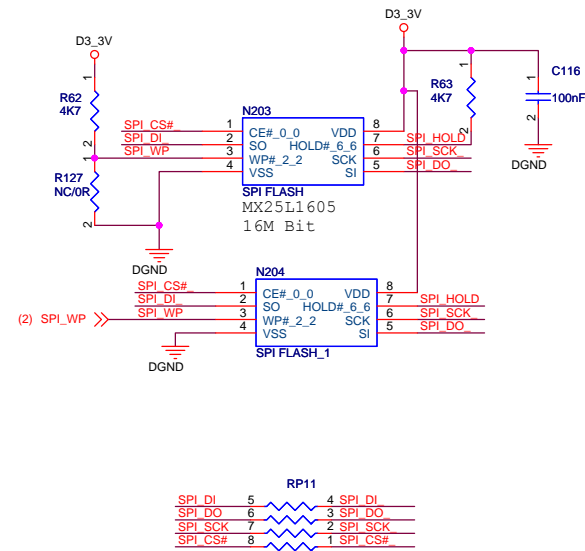
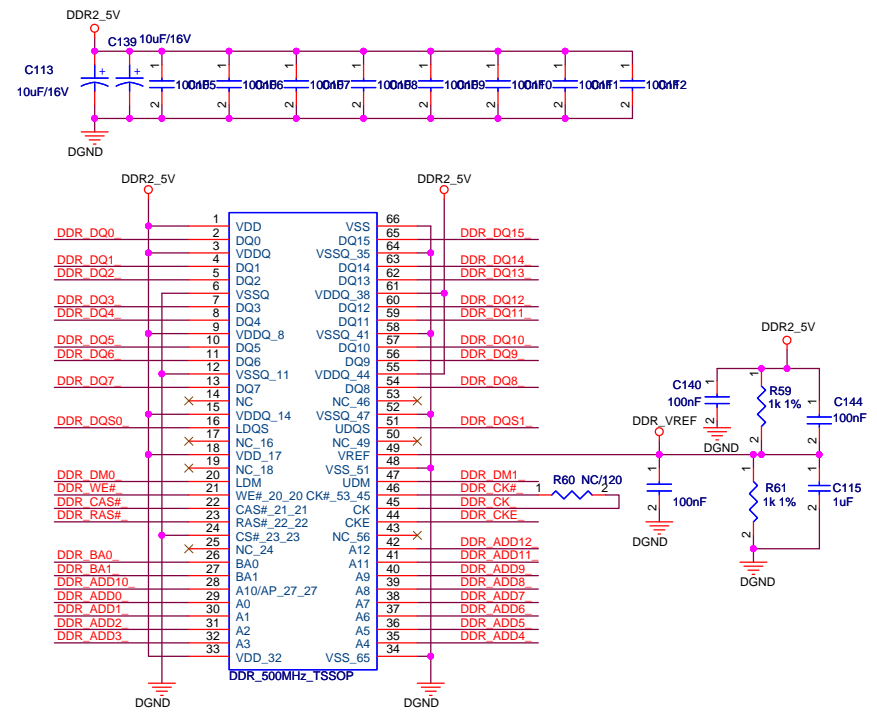
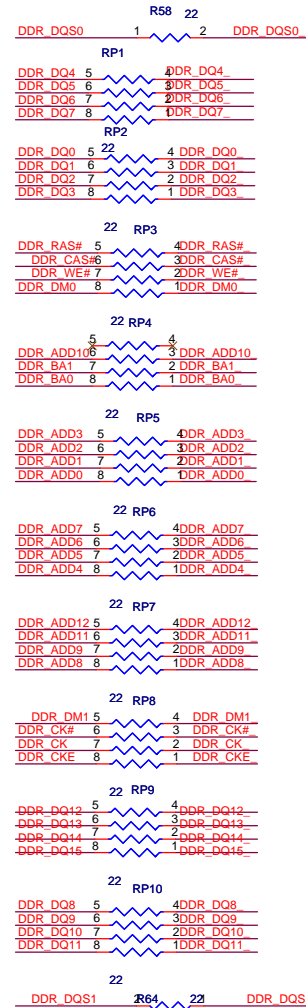
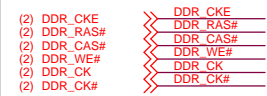


REALTEK ELECTRONIC CO., LTD.		
BLOCK DIGRAM		
Size	Document Number	Rev
Custom	Dragon LCD TV Controller	1.00
Date:	Thursday, June 17, 2010	Sheet 1 of 28



(2) SPI_CS# >>> SPI_CS#
(2) SPI_SCK >>> SPI_SCK
(2) SPI_DI <<< SPI_DI
(2) SPI_DO >>> SPI_DO

(2) DDR_DQ[15..0] >> DDR_DQ[15:0]
(2) DDR_ADD[12..0] >> DDR_ADD[12:0]
(2) DDR_BA[1..0] >> DDR_BA[1:0]
(2) DDR_DQS[1..0] >> DDR_DQS[1:0]
(2) DDR_DM[1..0] >> DDR_DM[1:0]



22

IF DEMOD

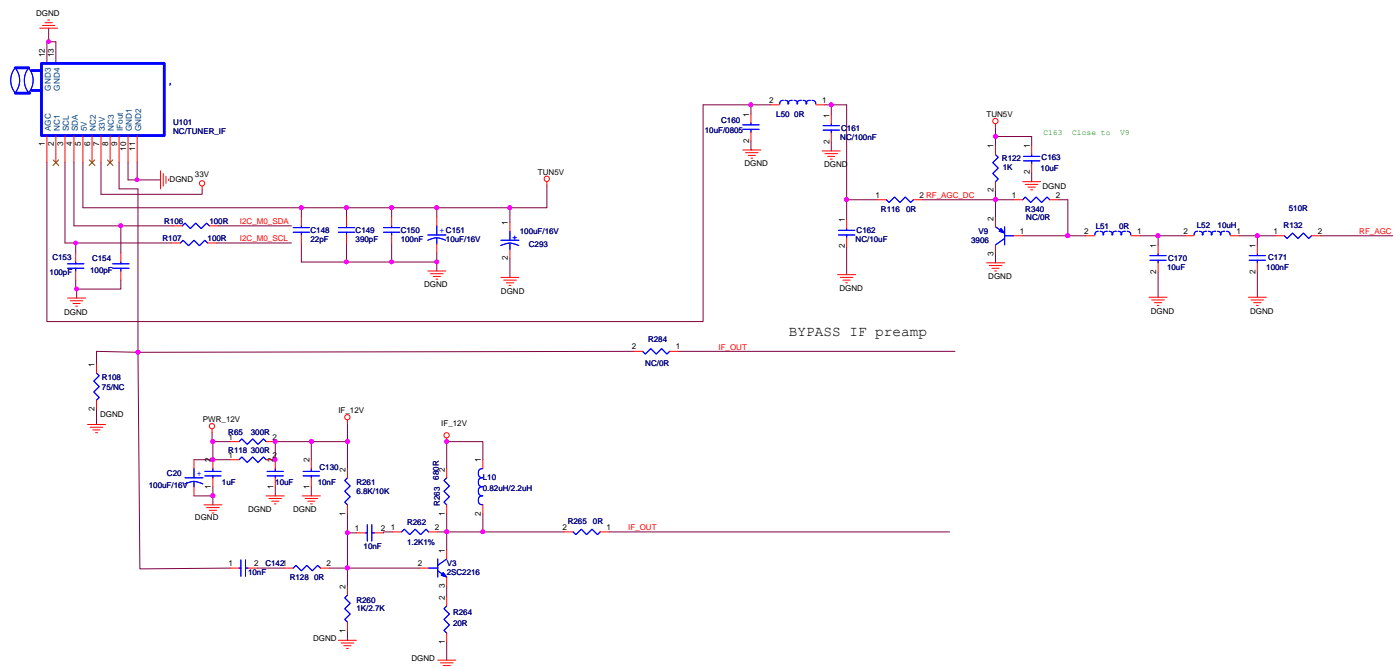
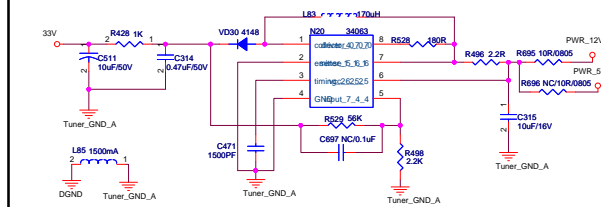
IF DEMOD

(2) I2C_M0_SDA << I2C_M0_SDA
(2) I2C_M0_SCL << I2C_M0_SCL

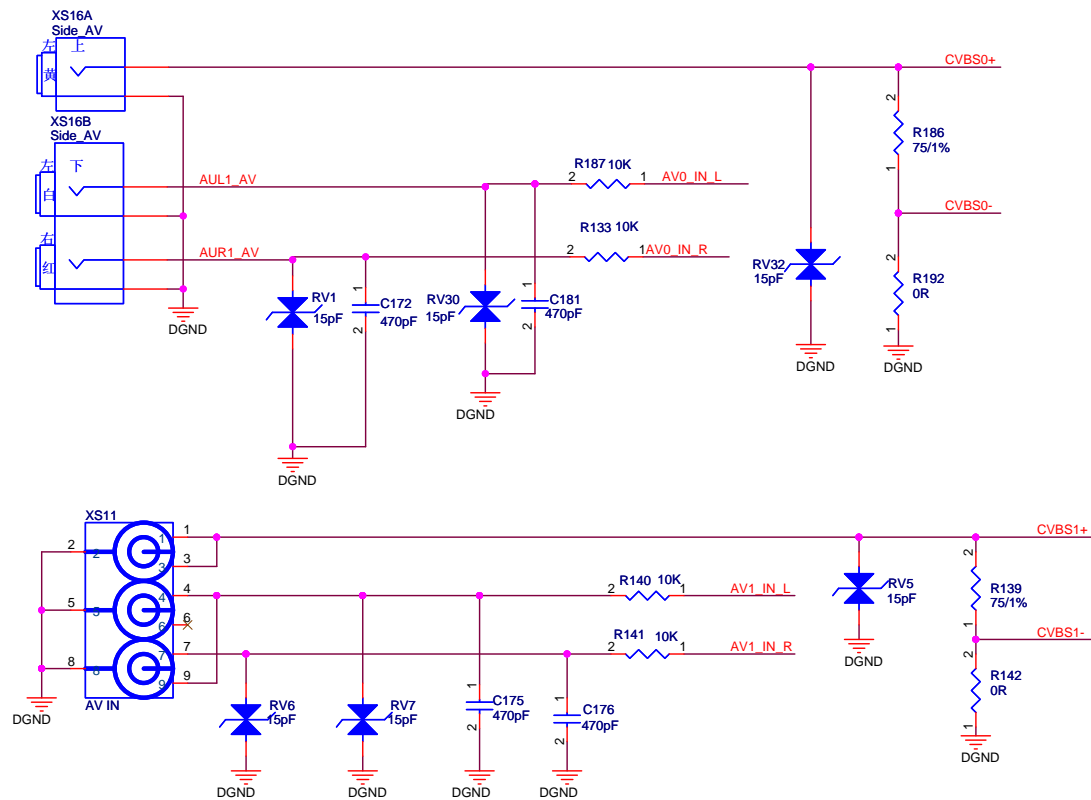
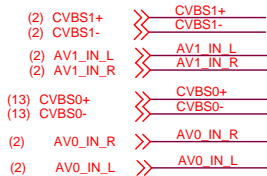
(2) IF_OUT << IF_OUT

(2) RF_AGC << RF_AGC

会产生干扰，排板要注意，同时原理高频头及高清等信号线，远离tuner放置。



VIDEO DECODER IN



REALTEK ELECTRONIC CO., LTD.

Title

AV/SV IN

Size
B

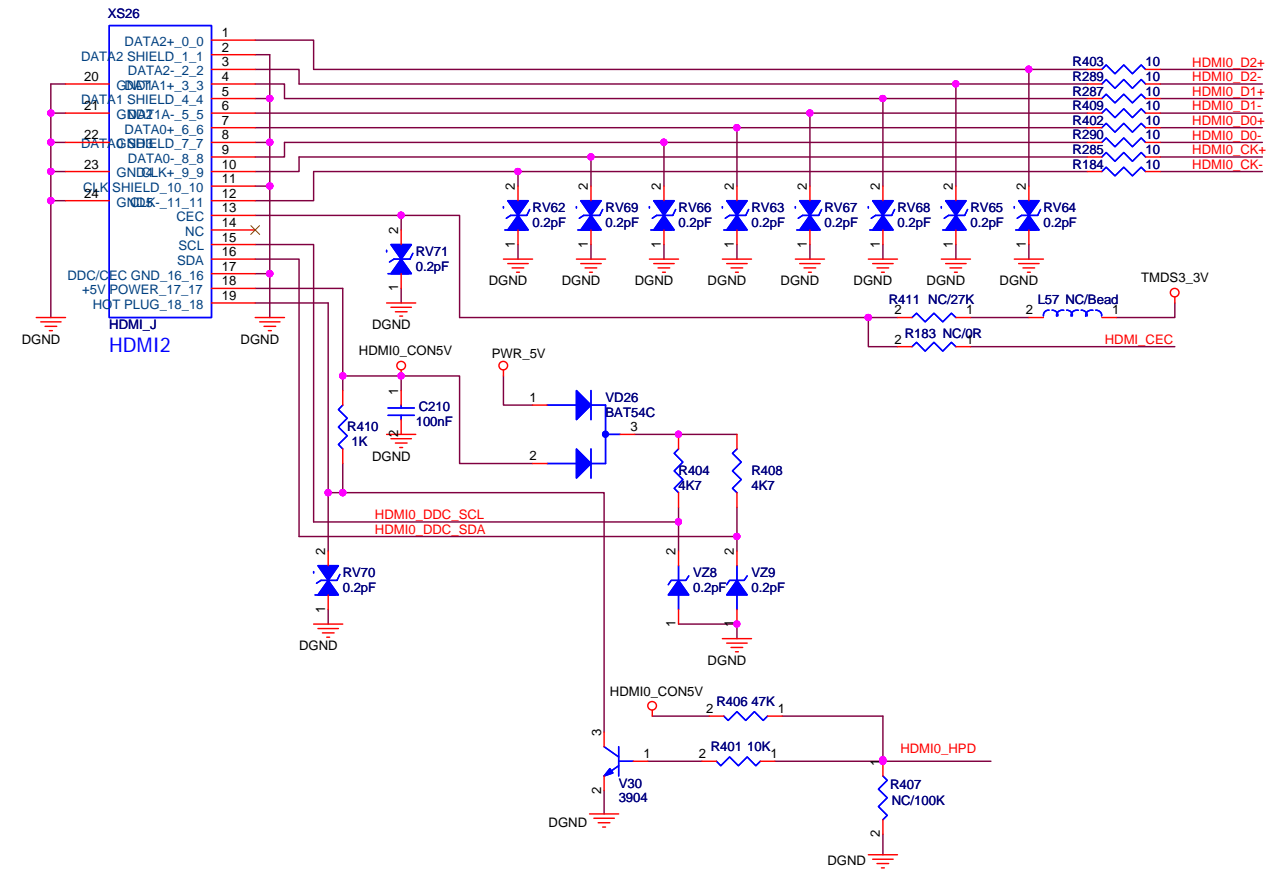
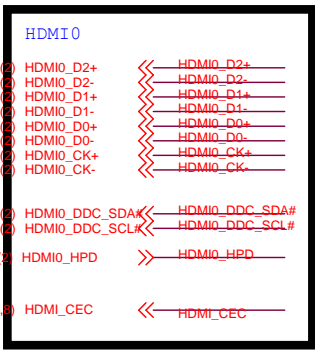
Document Number

Dragon LCD TV Controller

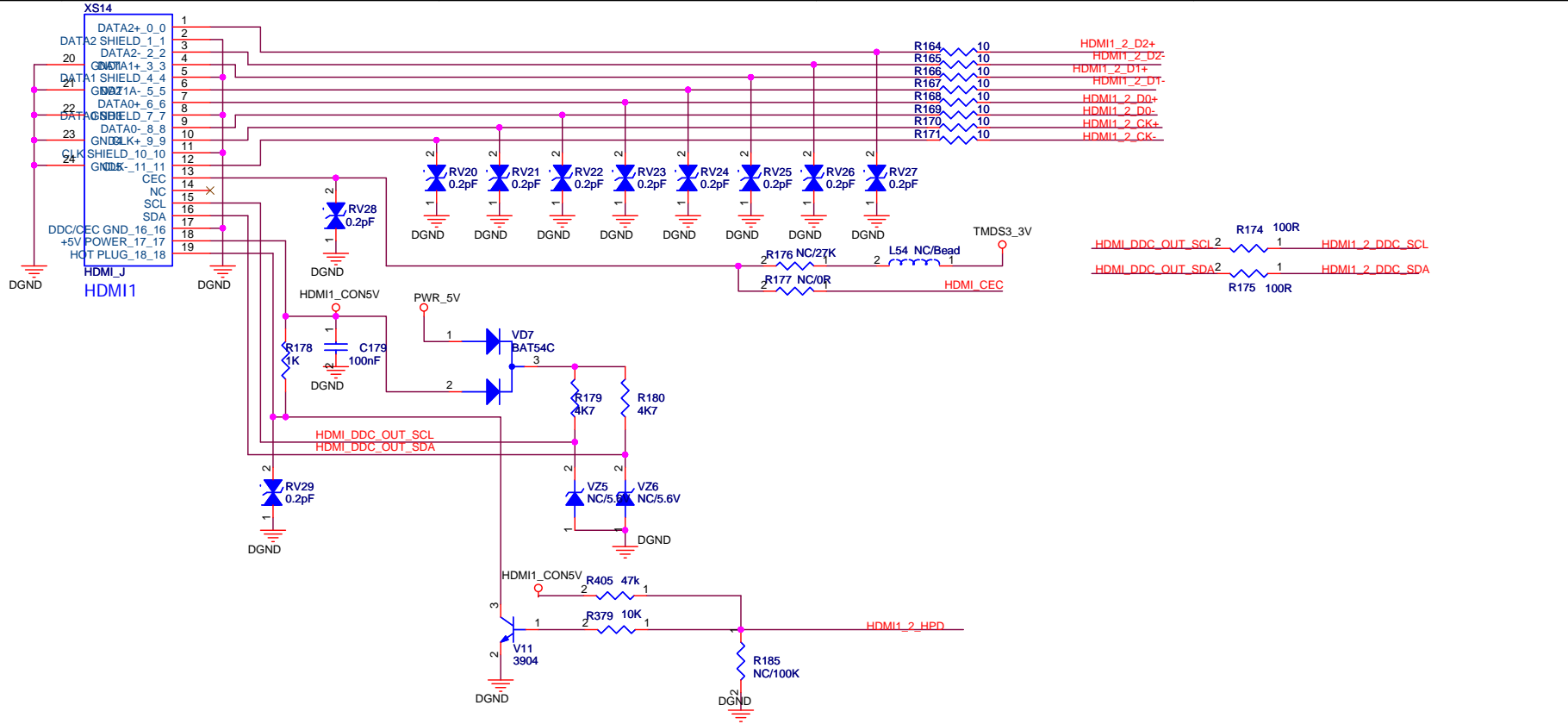
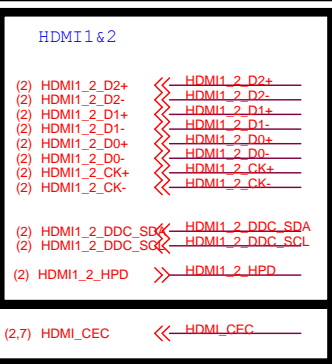
Rev
1.00

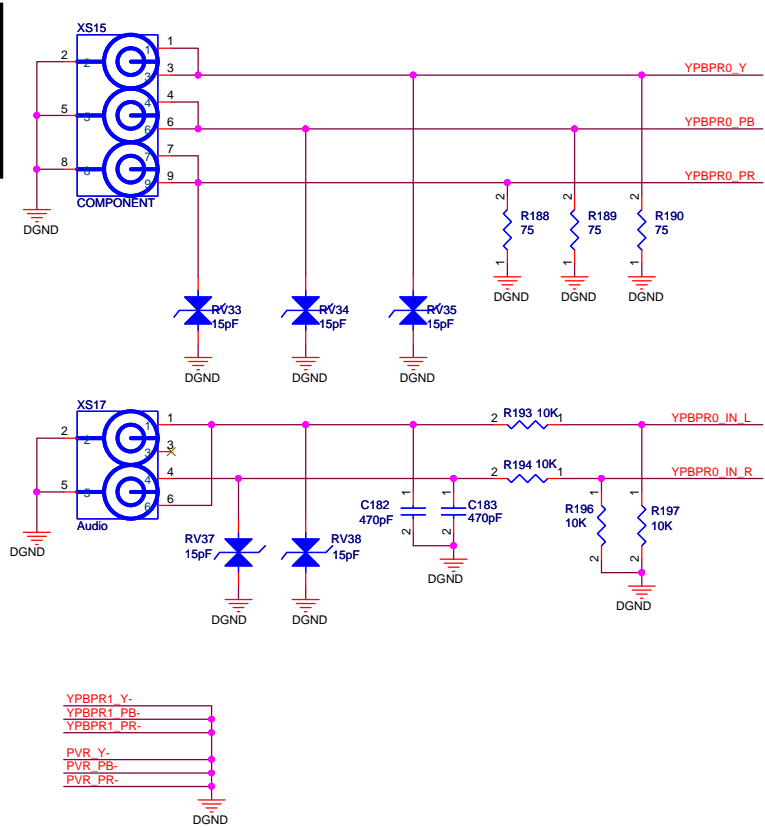
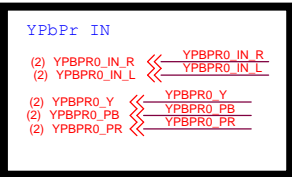
Date: Thursday, June 17, 2010

Sheet 6 of 28



REALTEK ELECTRONIC CO., LTD.			
Title			
HDMI 0 IN			
Size	Document Number		Rev
B	Dragon LCD TV Controller		1.00
Date:	Thursday, June 17, 2010	Sheet	7 of 28

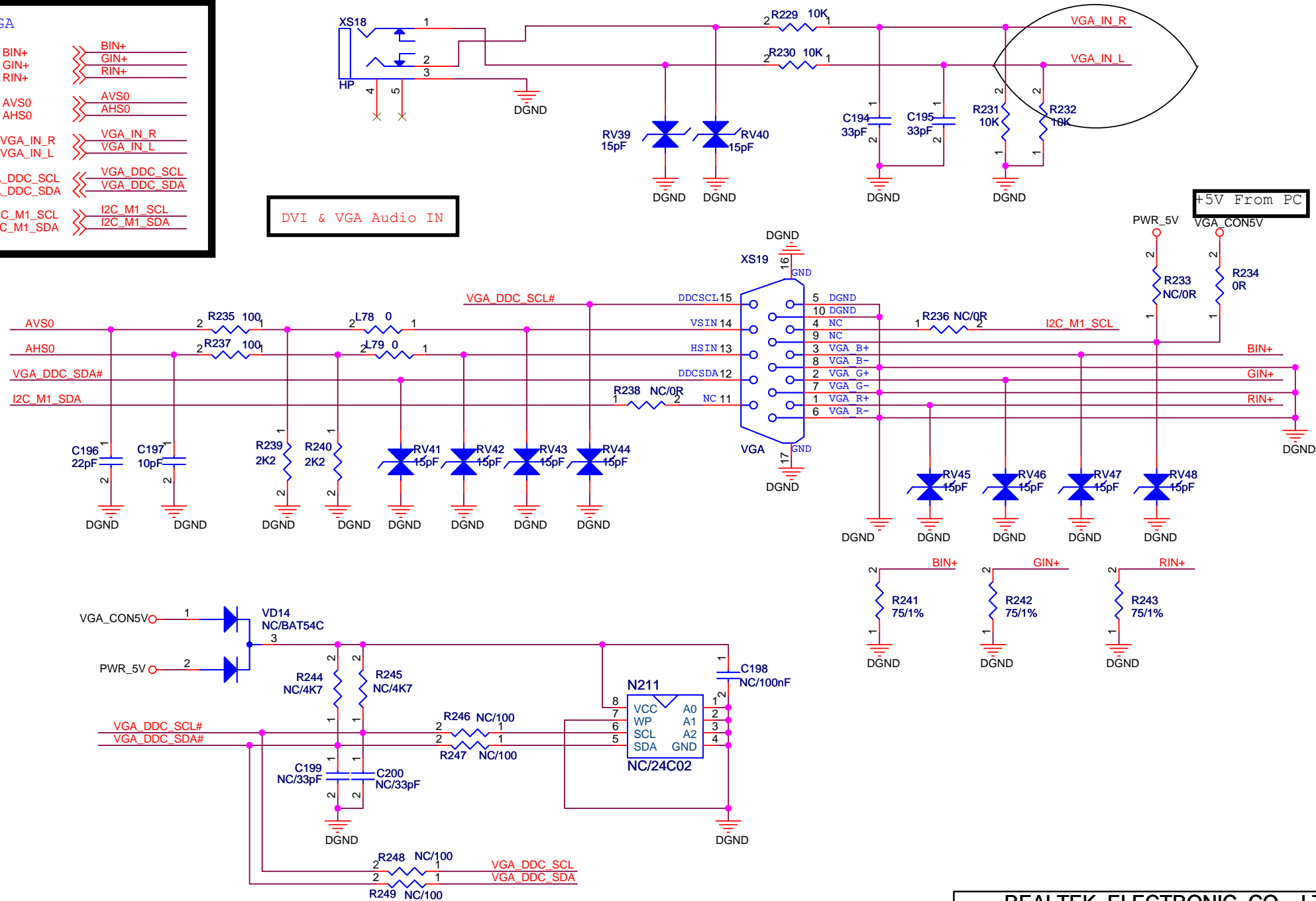




VGA

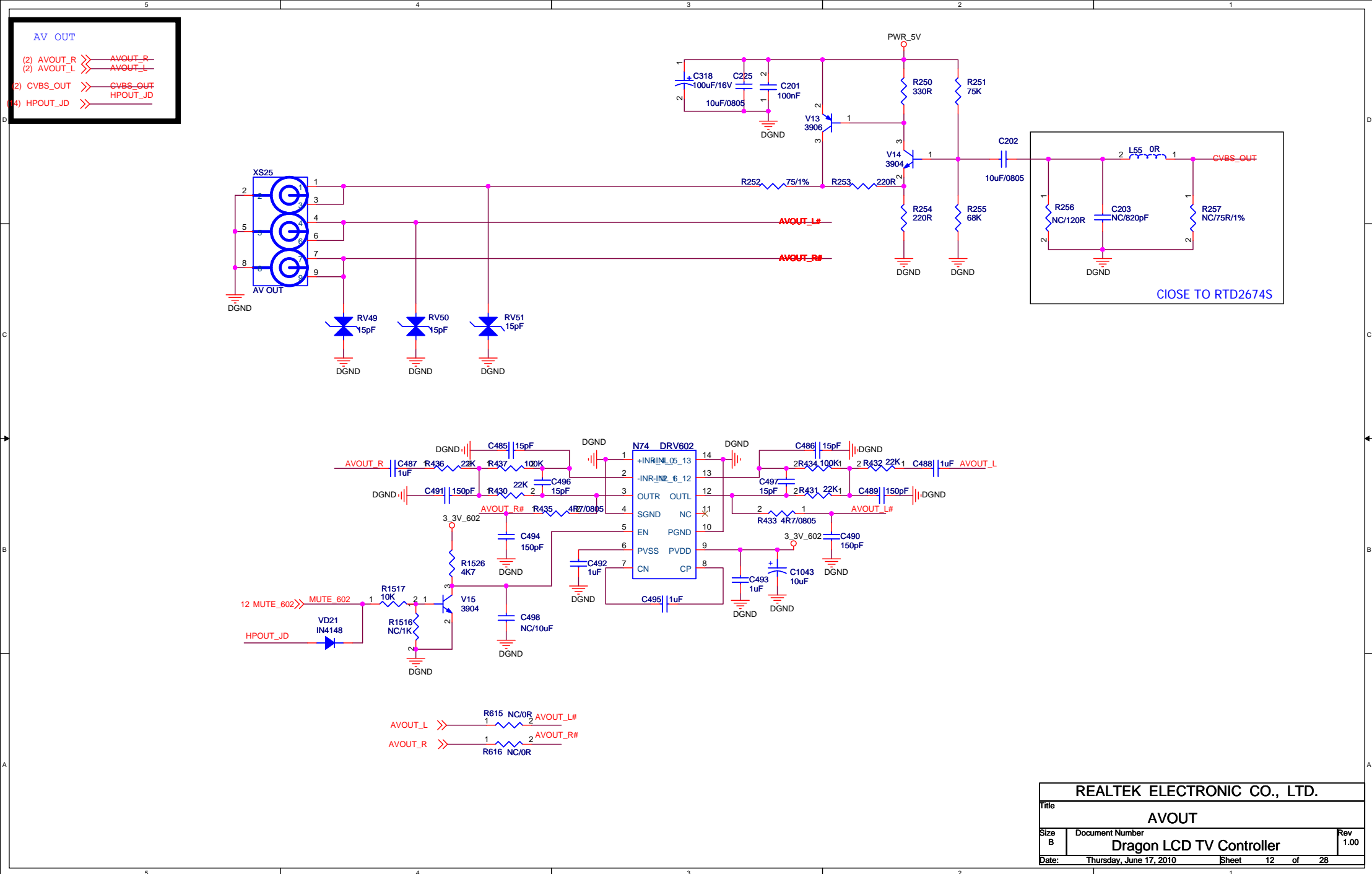
(2) BIN+	BIN+
(2) GIN+	GIN+
(2) RIN+	RIN+
(2) AVS0	AVS0
(2) AHS0	AHS0
(2) VGA_IN_R	VGA_IN_R
(2) VGA_IN_L	VGA_IN_L
(2,18) VGA_DDC_SCL	VGA_DDC_SCL
(2,18) VGA_DDC_SDA	VGA_DDC_SDA
(15,18) I2C_M1_SCL	I2C_M1_SCL
(15,18) I2C_M1_SDA	I2C_M1_SDA

DVI & VGA Audio IN

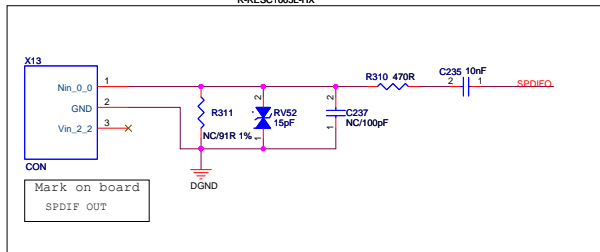
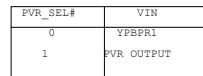
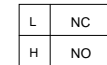


REALTEK ELECTRONIC CO., LTD.

Title		
VGA IN		
Size	Document Number	Rev
Custom	Dragon LCD TV Controller	1.00
Date:	Thursday, June 17, 2010	Sheet 11 of 28



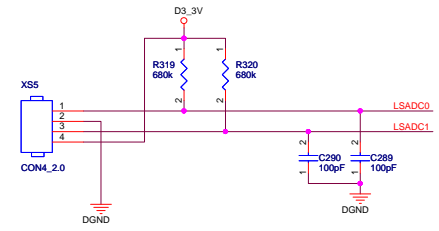
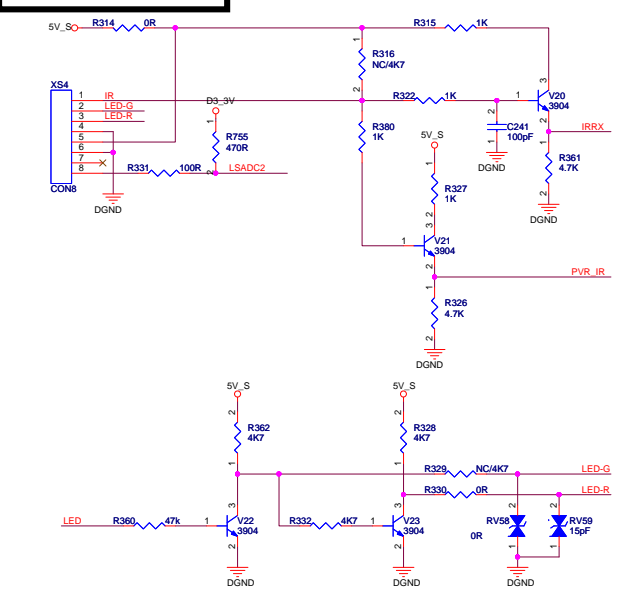
(2) AOUT_L >>> AOUT_L
(2) AOUT_R >>> AOUT_R
(2) HPOUT_L <<< HPOUT_L
(2) HPOUT_R <<< HPOUT_R
(2) MUTE_AMP >>> MUTE_AMP
(2) SPDIF01 >>> SPDIF01
(6) SPDIF02 >>> SPDIF02
(3) PVR_SEL >>> PVR_SEL
(2) HPOUT_ID <<< HPOUT_ID
SPDIF02 <<< SPDIF02

[illegible]

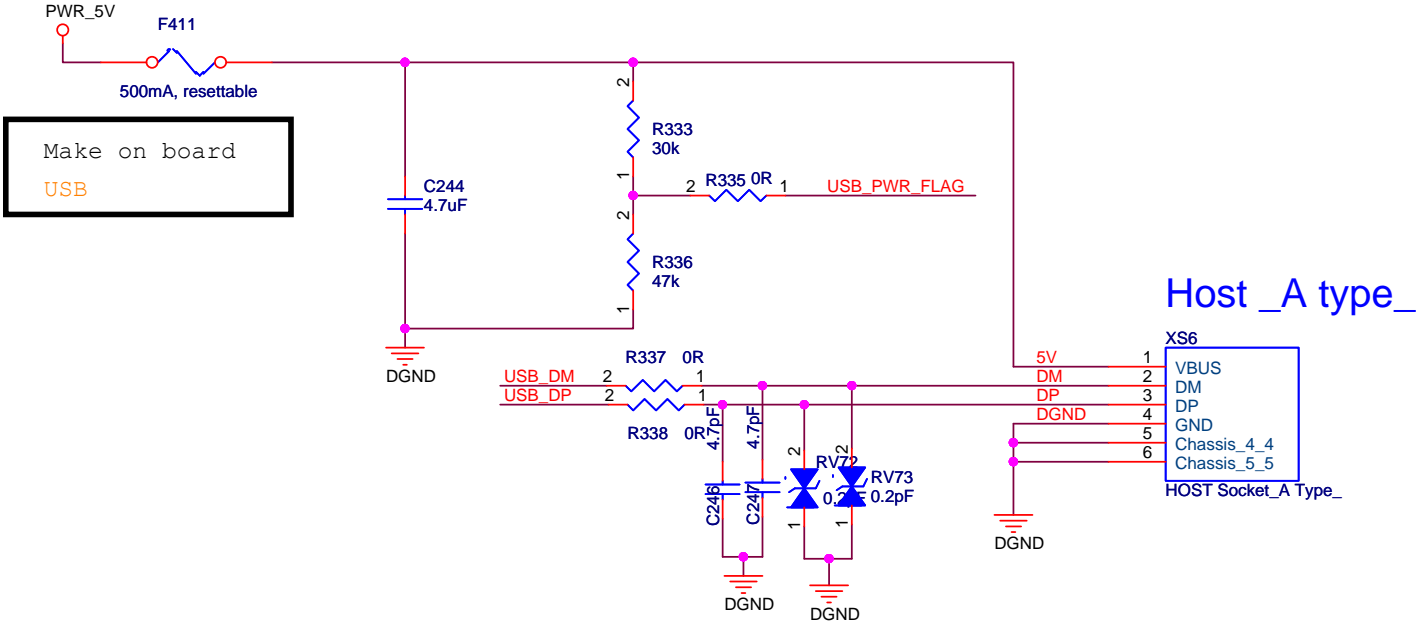
IR/KEY

- (2) LSADC0
- (2) LSADC1
- (2) LSADC2
- (26) PVR_IR
- (2) IRRX
- (2) LED
- (4,8,12,18) I2C_M1_SDA
- (4,8,12,18) I2C_M1_SCL
- (2,8,26) URAT_RX
- (2,8,26) URAT_TX

PVR_IR (26)



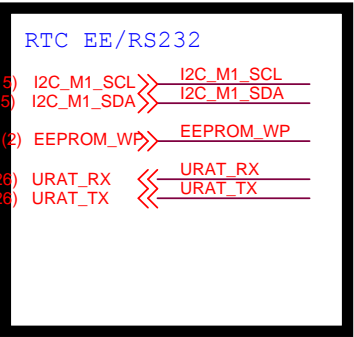
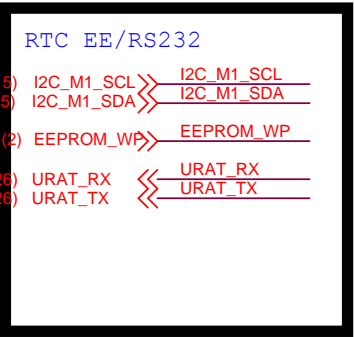
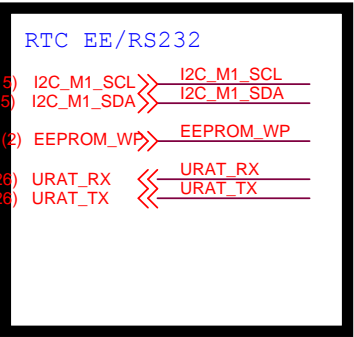
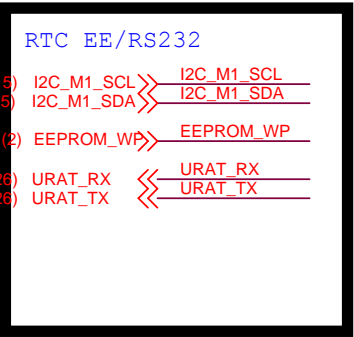
USB

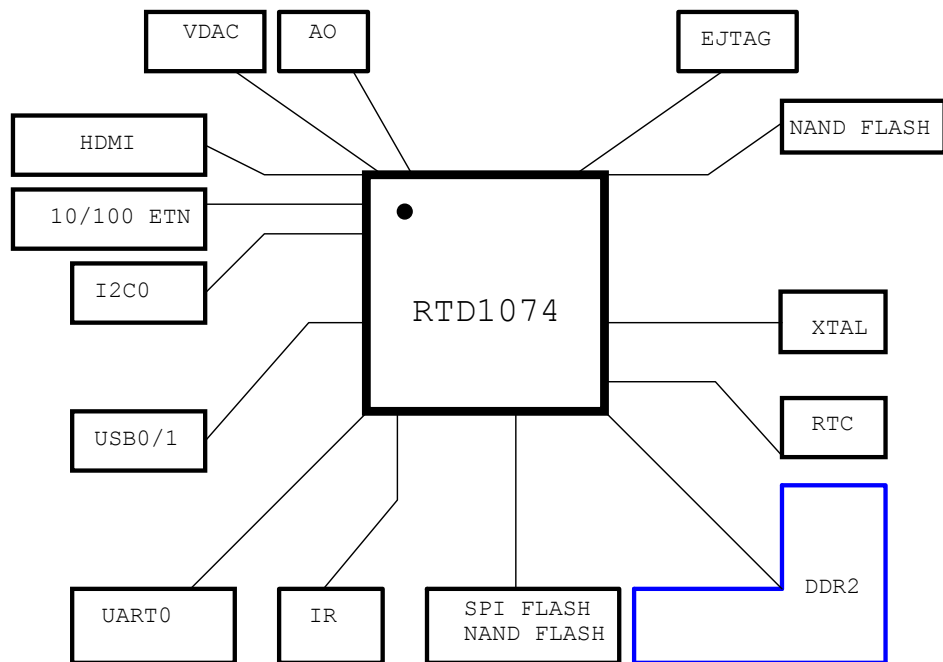


REALTEK ELECTRONIC CO., LTD.			
Title			
USB			
Size	Document Number		Rev
Custom	Dragon LCD TV Controller		1.00
Date:	Thursday, June 17, 2010	Sheet	16 of 28

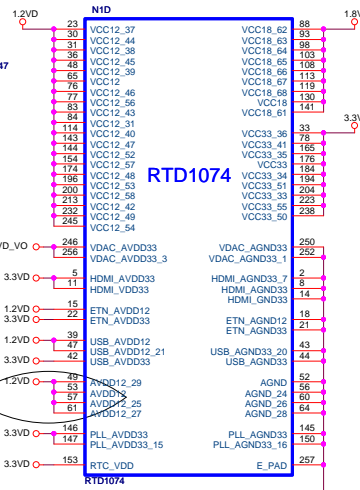
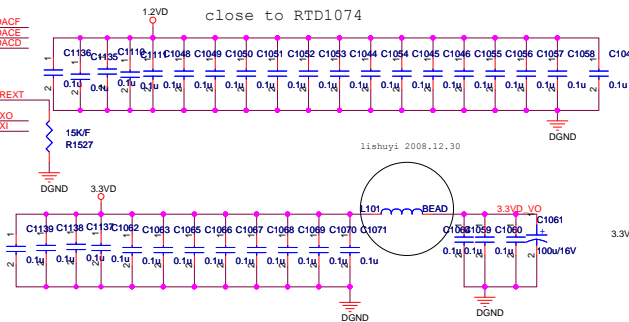
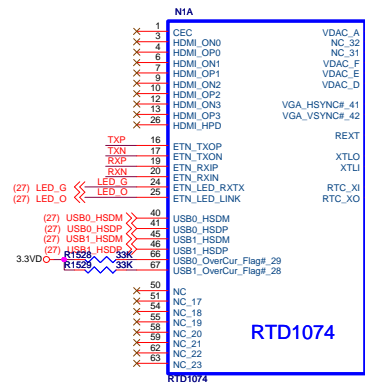
RTC EE/RS232

(5)	I2C_M1_SCL	»» I2C_M1_SCL
(5)	I2C_M1_SDA	»» I2C_M1_SDA
(2)	EEPROM_WP	»» EEPROM_WP
(6)	URAT_RX	»» URAT_RX
(6)	URAT_TX	»» URAT_TX

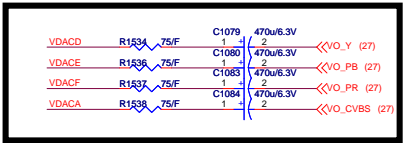
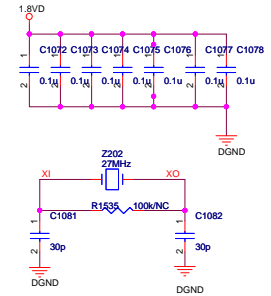


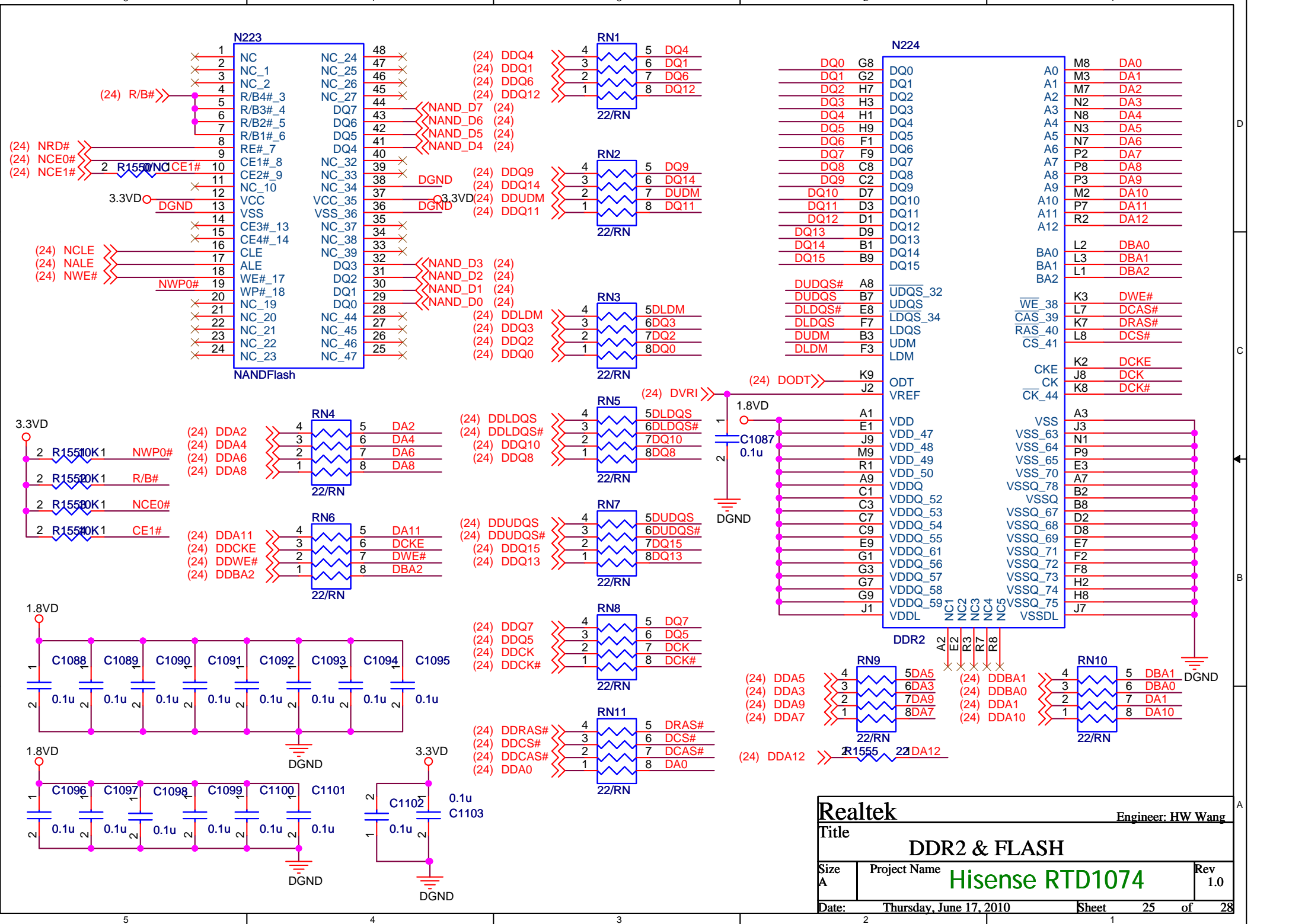


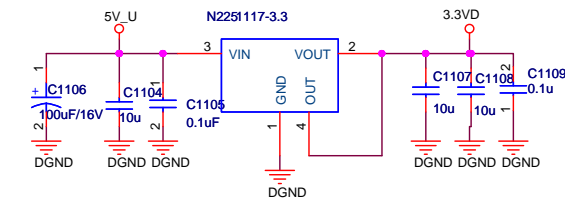
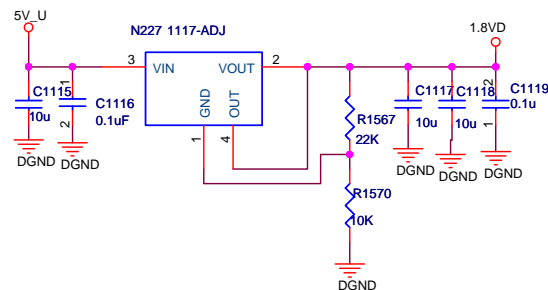
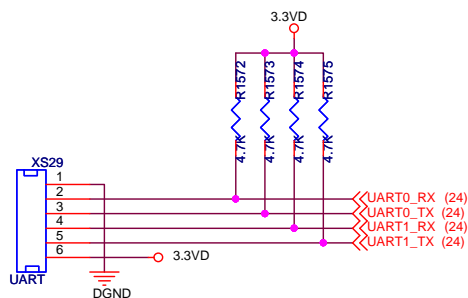
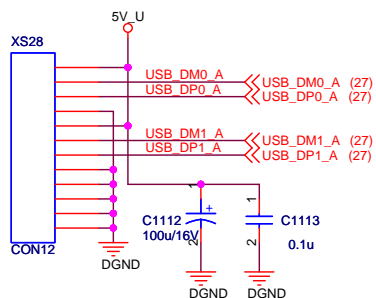
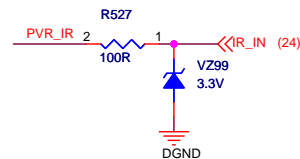
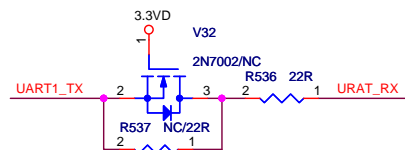
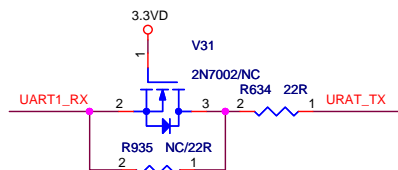
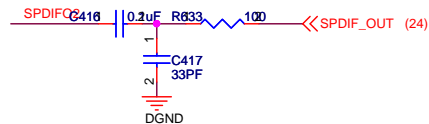
Realtek		Engineer: Shou-Te Yen	
Title			
FUNCTION BLOCK			
Size	Project Name	Rev	
Custom	Hisense RTD1074	1.0	
Date:		Thursday, June 17, 2010	
Sheet		21	of 28



与RTD1073不同







URAT_TX (2,15,18)

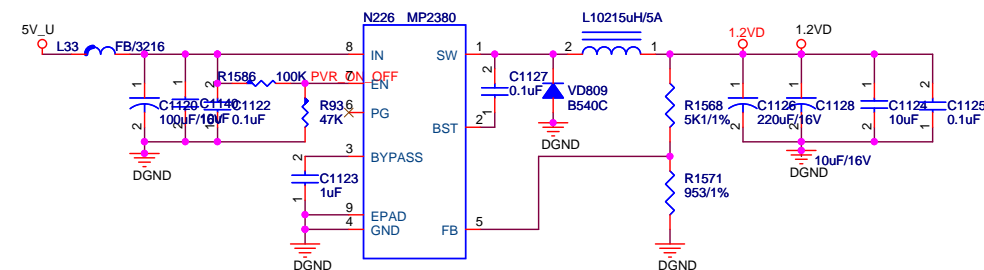
URAT_RX (2,15,18)

SPDIFO2 (14)

PVR_IR (15)

PVR_ON_OFF (10)

+5V FOR SYS

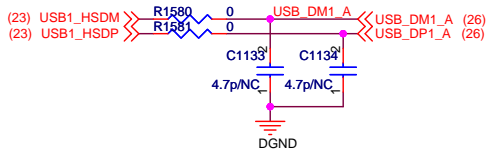
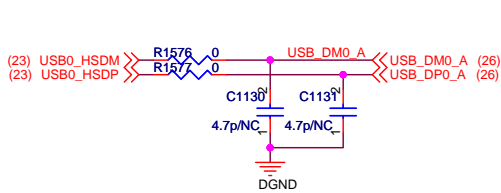


$$R38=R1= 10.83 \times _VOUT - 0.923_ \text{ k}\Omega <R39=R2=10K>$$

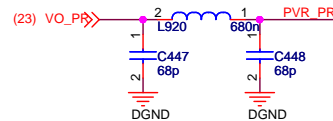
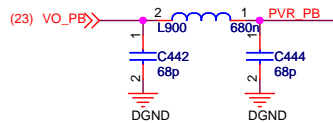
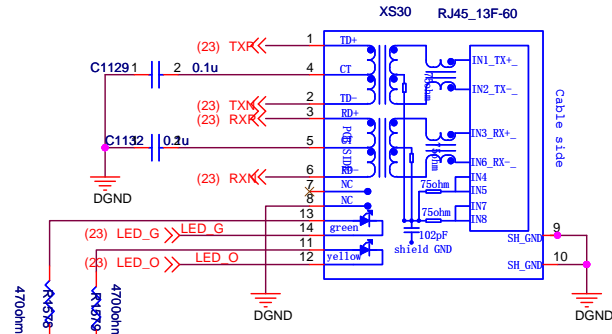
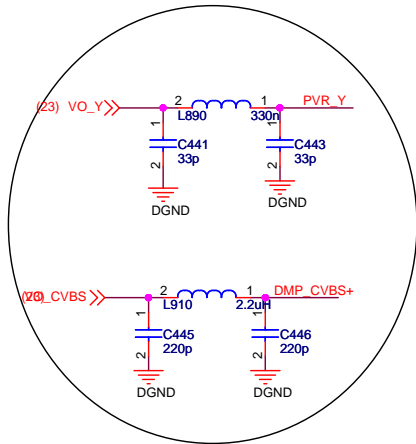
$$VOUT= 0.923 \times _R1+R2_ /R2$$

lishuyi 2008.12.30

Realtek		Engineer: HW Wang	
Title		Power & Connectors	
Size B	Project Name	Hisense RTD1074	Rev 1.0
Date:	Friday, September 17, 2010	Sheet 26	of 28



AV OUT



PVR_Y (9,10)

PVR_PB (9,10)

PVR_PR (9,10)

DMP_CVBS+ (2,10)

Realtek		Engineer: HW Wang	
Title		Video I/O & USB NET	
Size B	Project Name	Hisense RTD1074	Rev 1.0
Date:	Thursday, June 17, 2010	Sheet 27	of 28

