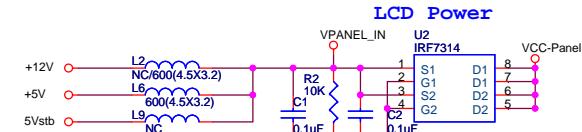
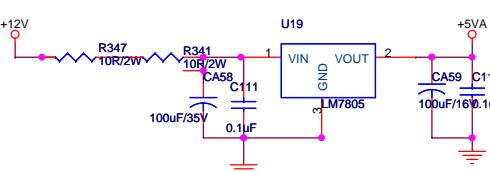
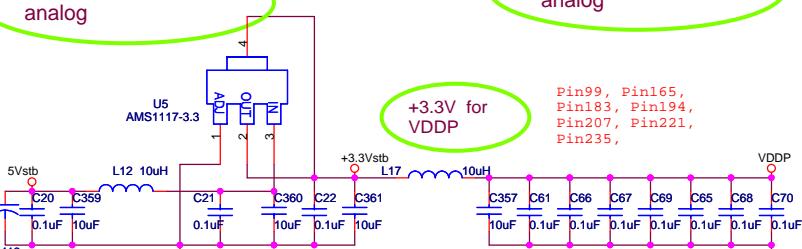


Normal: L
Standby:H

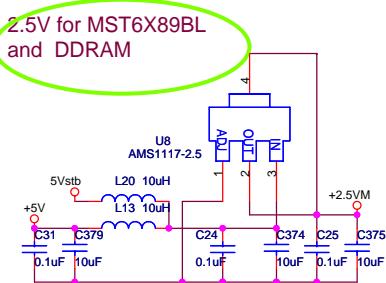


5Vstb SAMSUNG panel 0~3.3V R34 Conn
LG/CMO/AU panel 0~5V,R34 NC

Vcc3.3for MST6X89BL analog

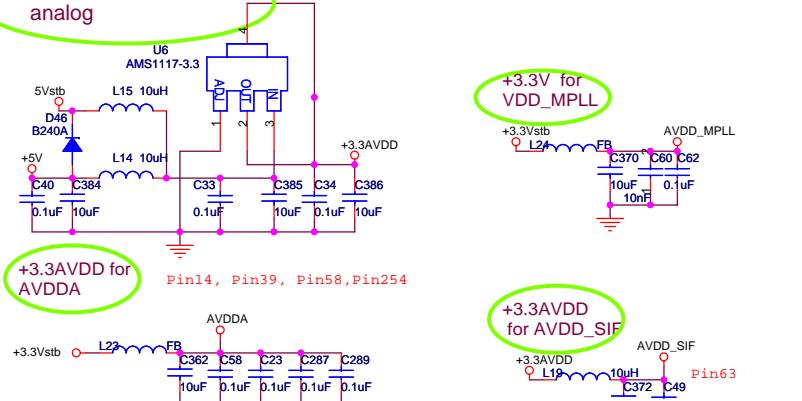


Vcc3.3for MST6X89BL analog

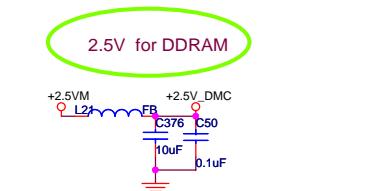


2.5V for MST6X89BL
and DDRAM

Vcc3.3for MST6X89BL analog



3V for
D_MPLL

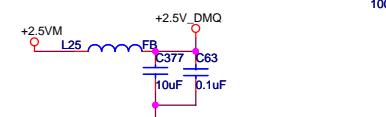


2.5V for DDRAM

+3.3AVDD for AVDDA

Circuit diagram showing a feedback loop from AVDDA through resistors C362, C58, C23, C287, and C289 to ground, with a 10uF capacitor connected between C362 and C58.

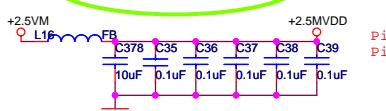
3AVDD
AVDD_SIF



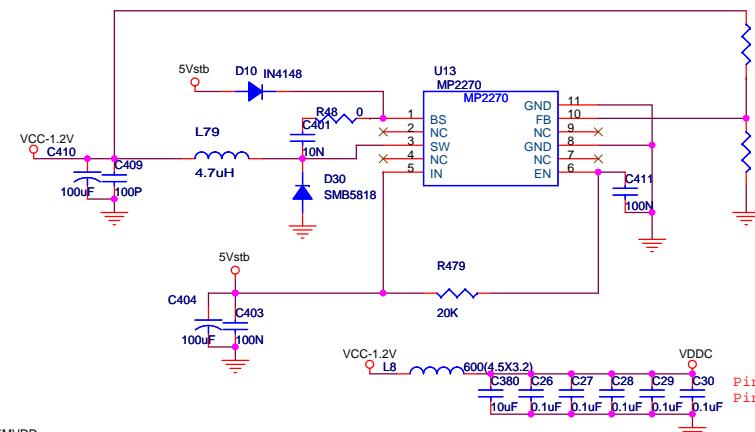
+2.5V for MST6X89BL

+3.3AVDD AVDD_MemP

AVDD_AU
Pin7



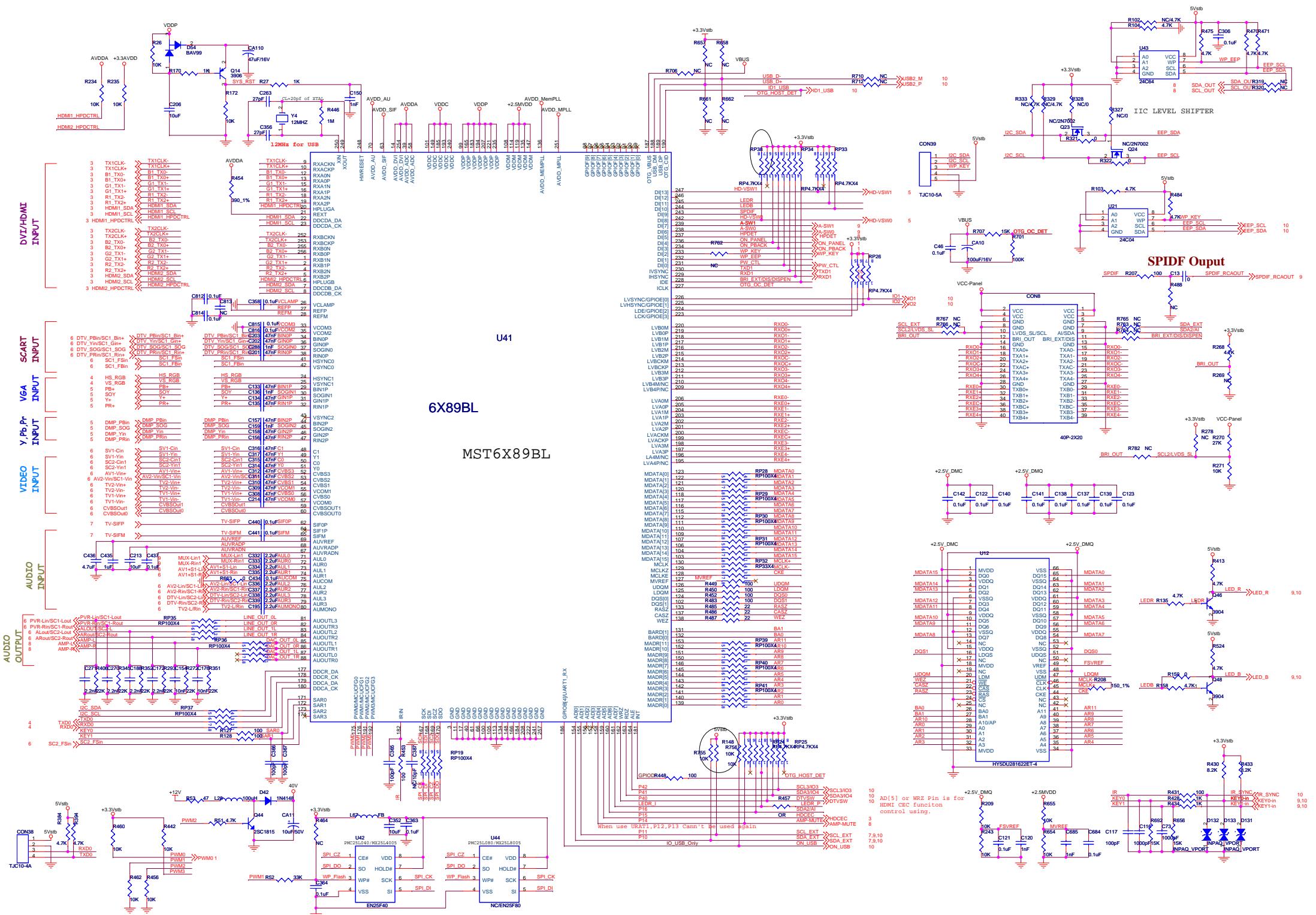
Pin108, Pin114,
Pin119, Pin135, P14



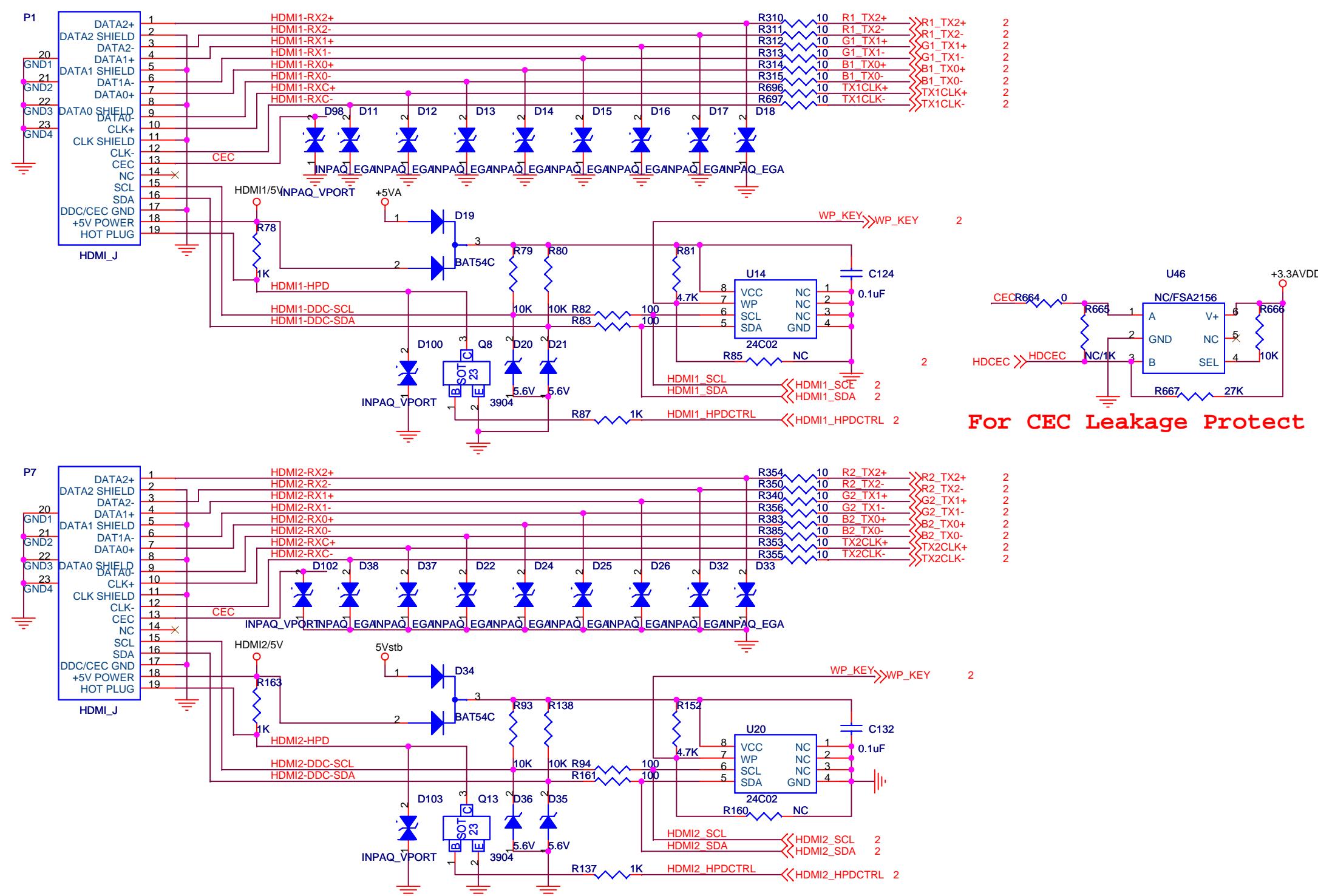
VCC-1.2V
L8
600(4.5X3.2)
C380 C26 C27 C28 C29 C30
10uF 0.1uF 0.1uF 0.1uF 0.1uF 0.1uF
VDCC
Pin99, Pin146, Pin182,
Pin195, Pin242

Pin99, Pin146, Pin181
Pin195, Pin242

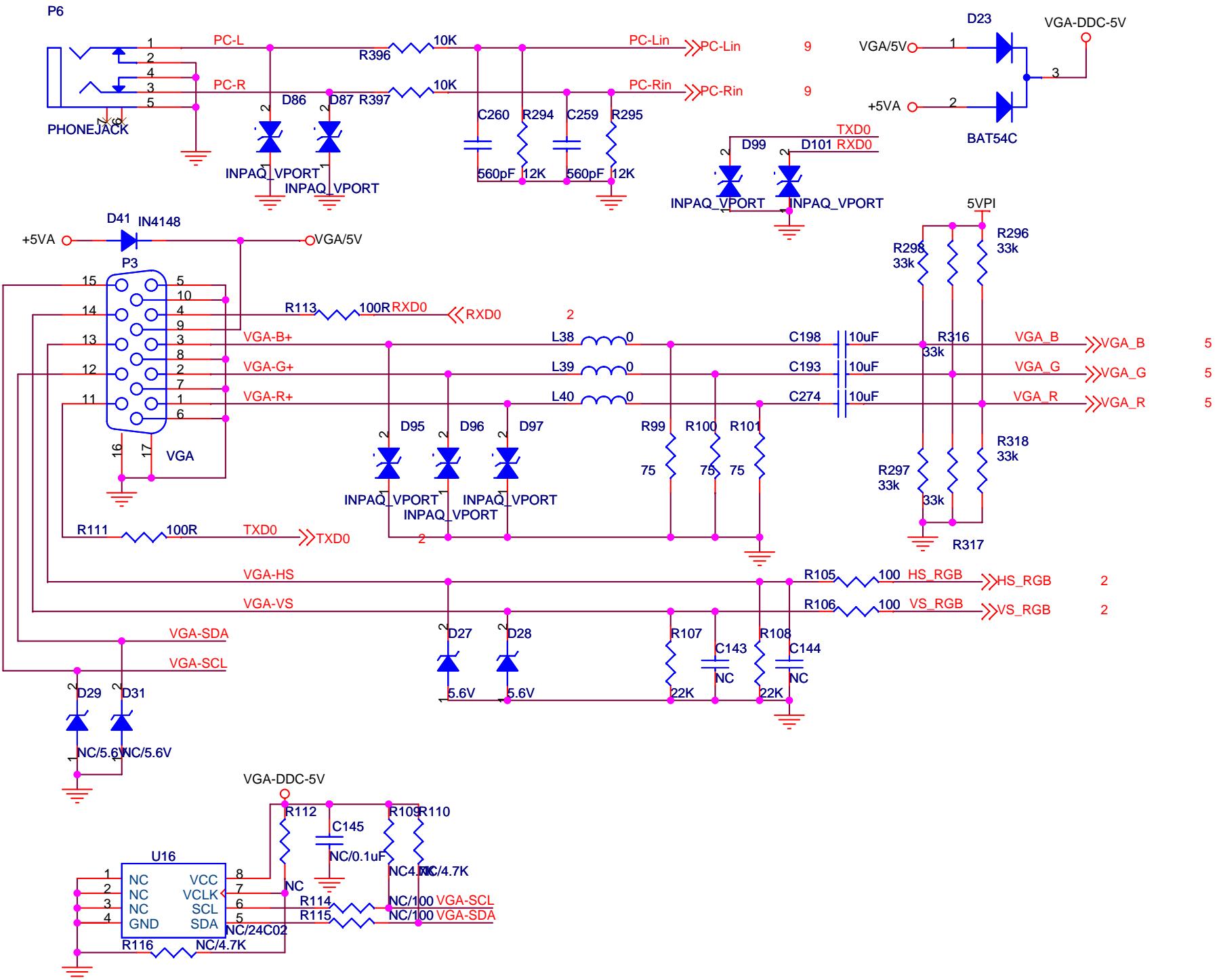
1、电源管理电路及屏供电电路部分



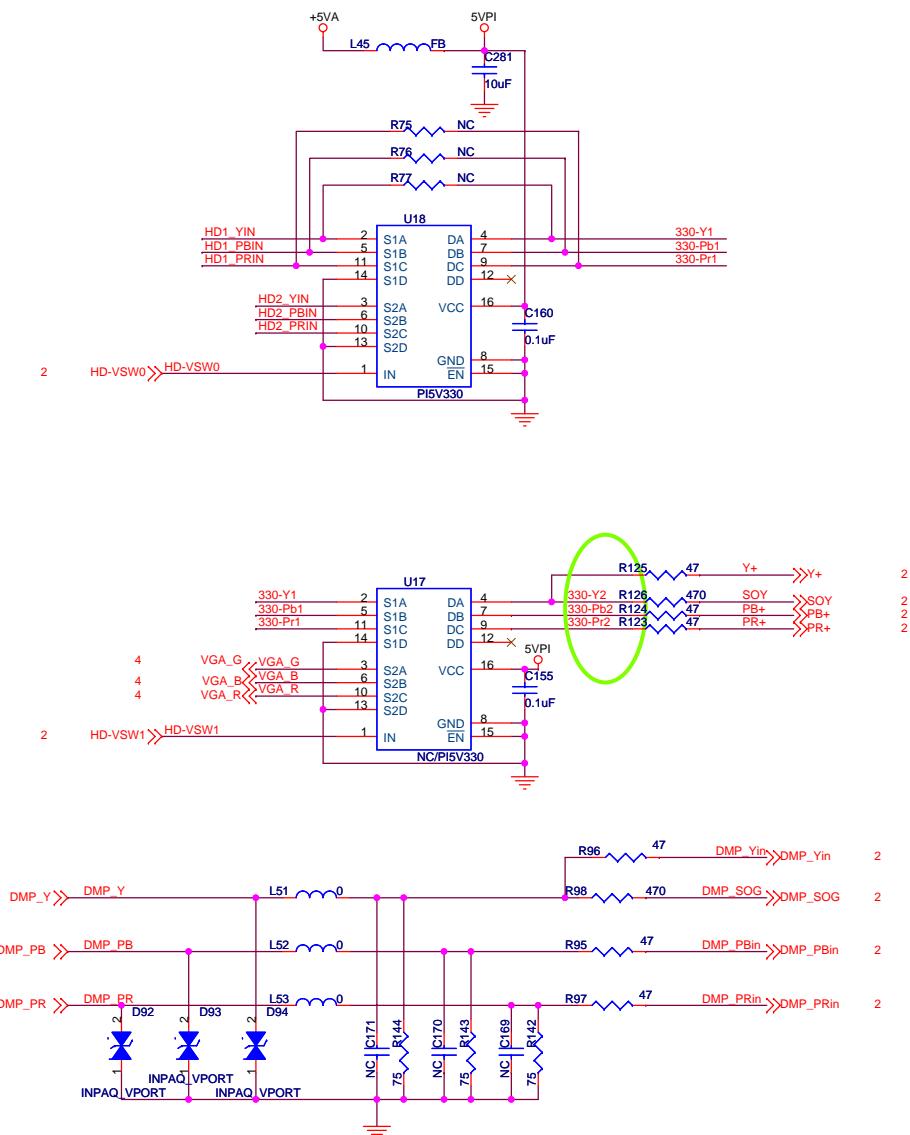
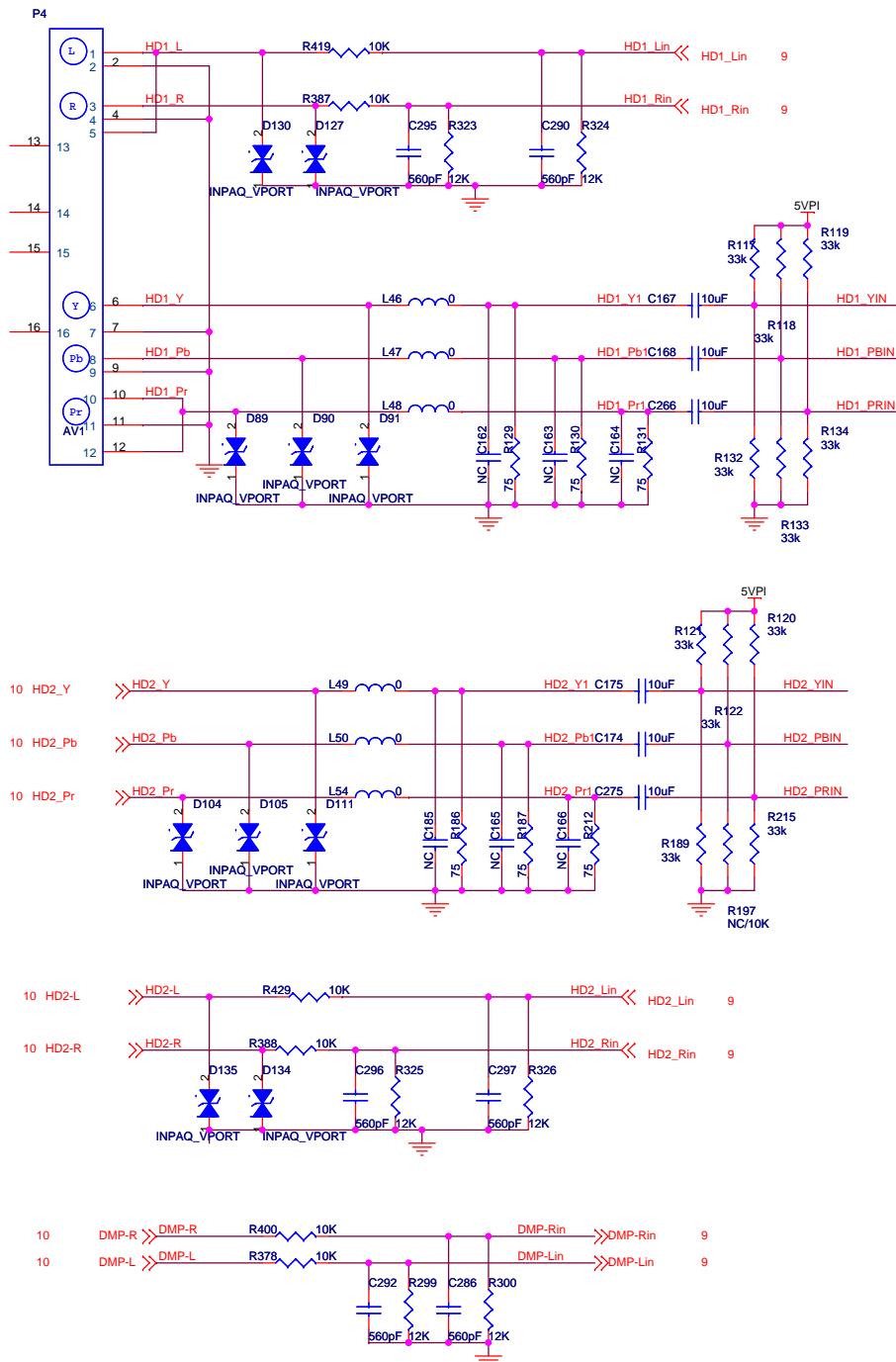
2、主芯片MST6U29DL、FLASH、SDRAM、LVDS接口电路部分



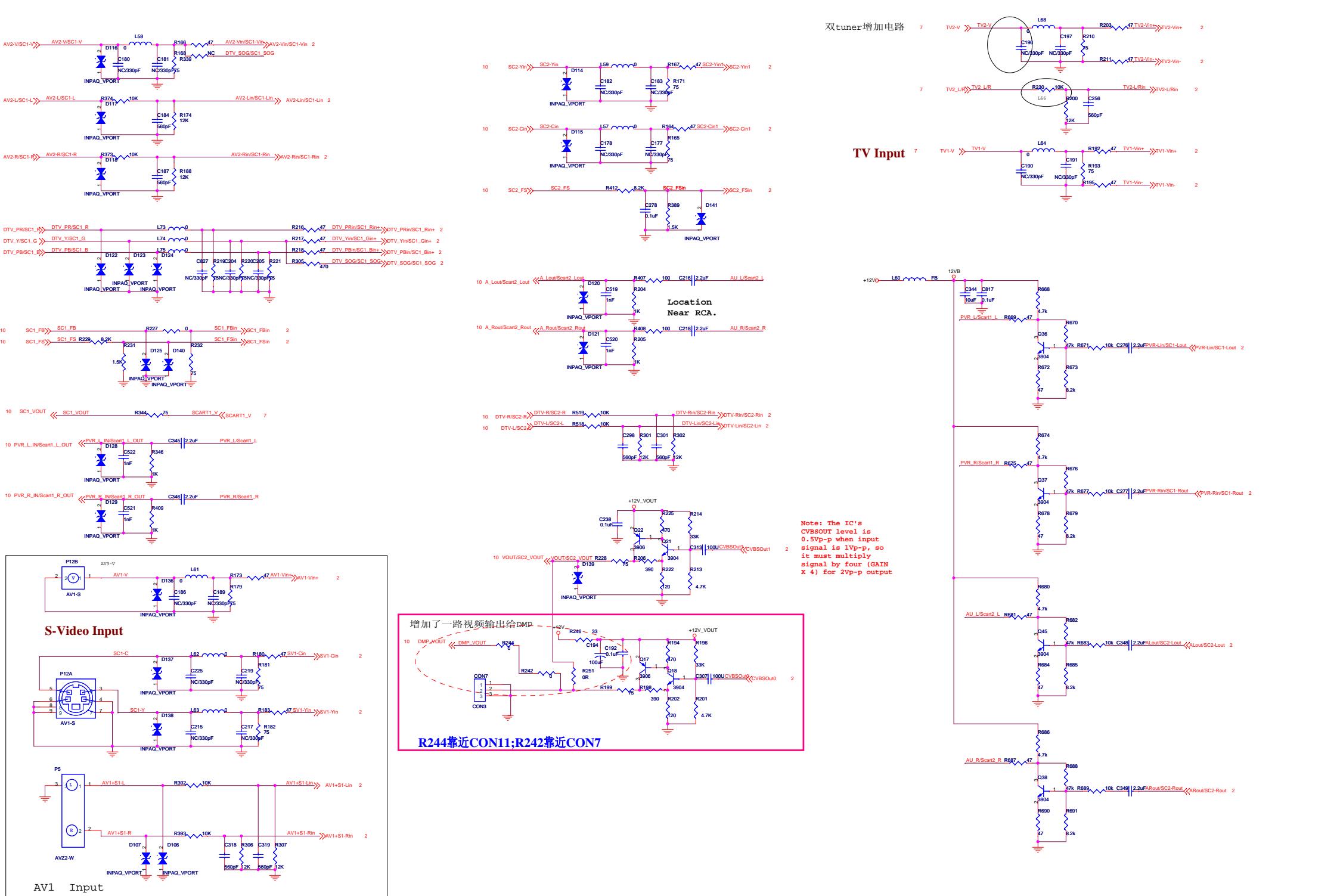
3、HDMI接口电路部分



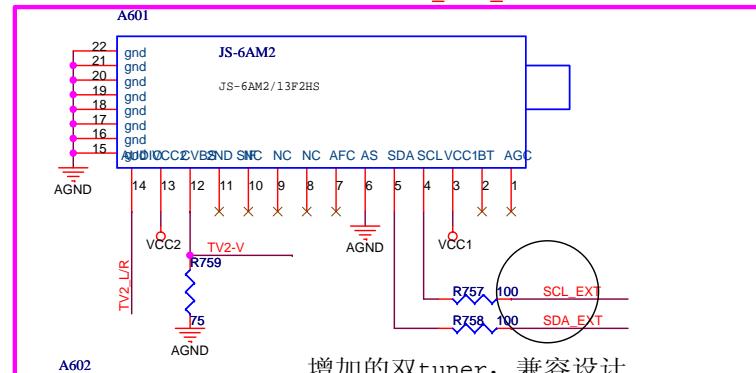
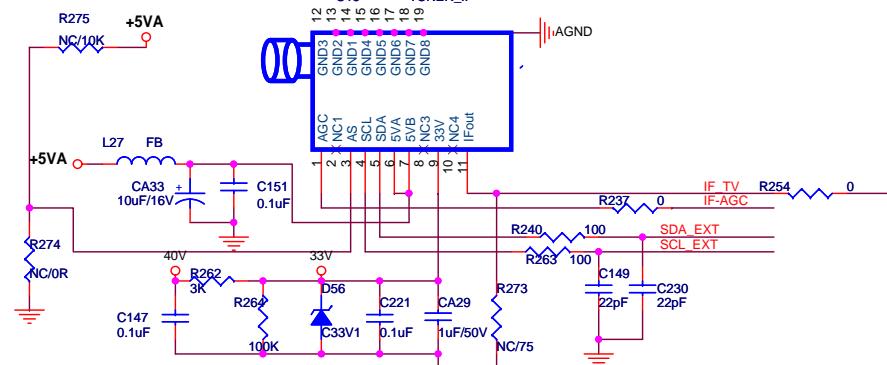
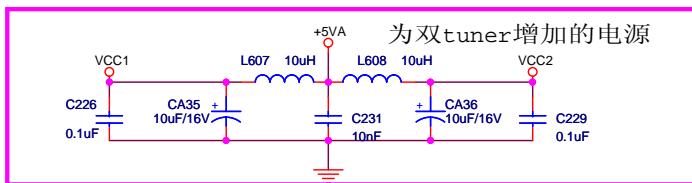
4、VGA接口电路部分



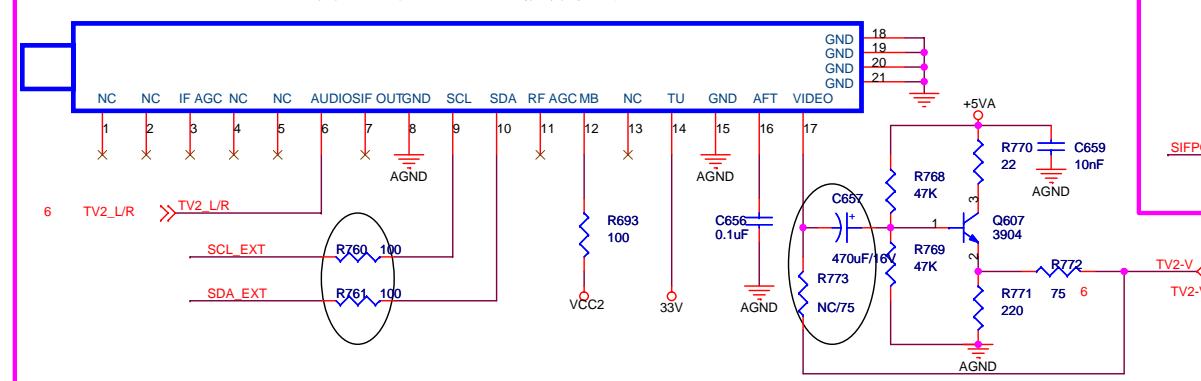
5、高清、DMP接口及切换电路部分



6、AV、S端子视频、音频输入接口匹配电路部分



增加的双tuner，兼容设计



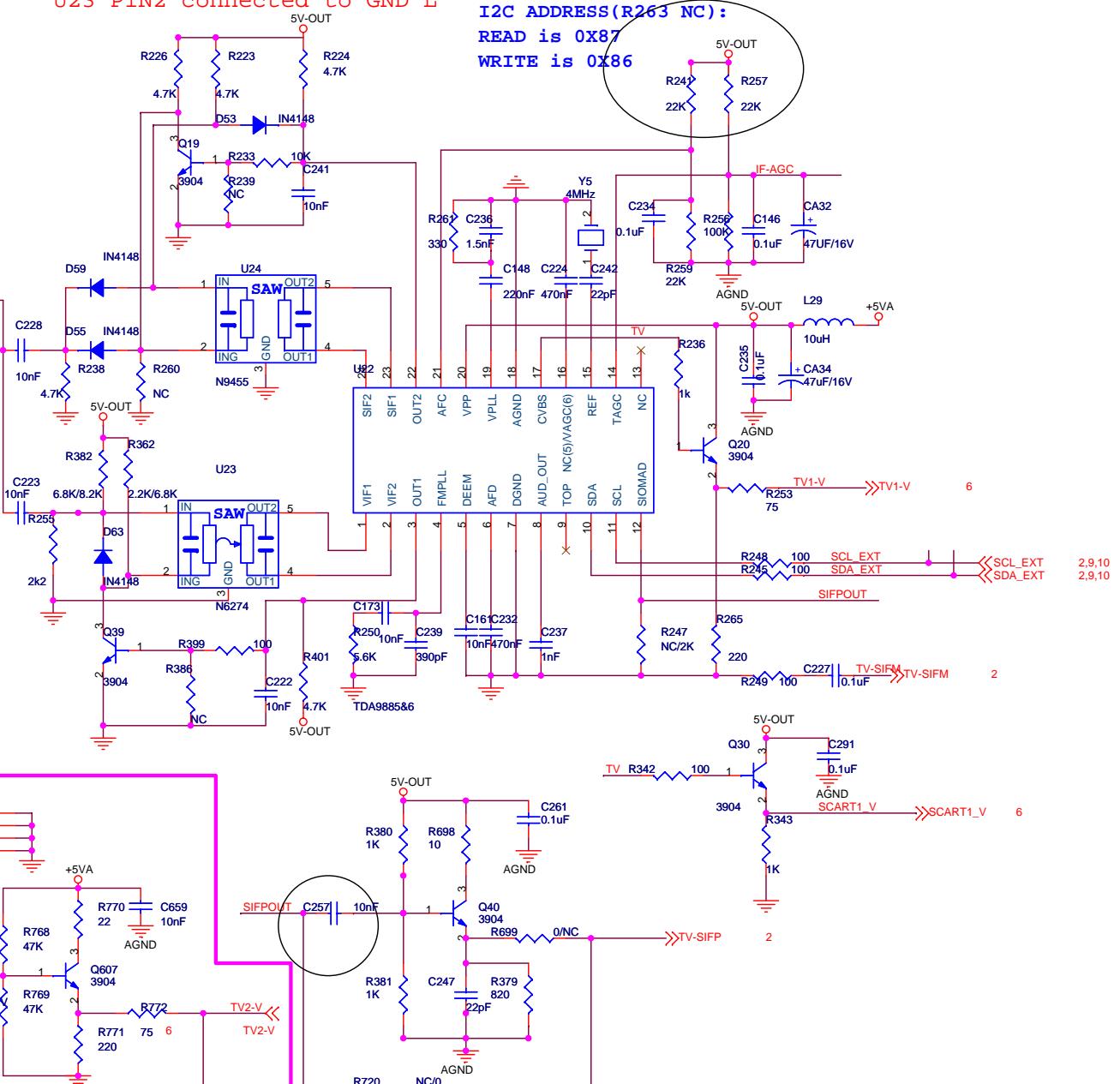
U23 PIN2 connected to GND L' I_{2C} ADDR

U23 PIN2 connected to GND

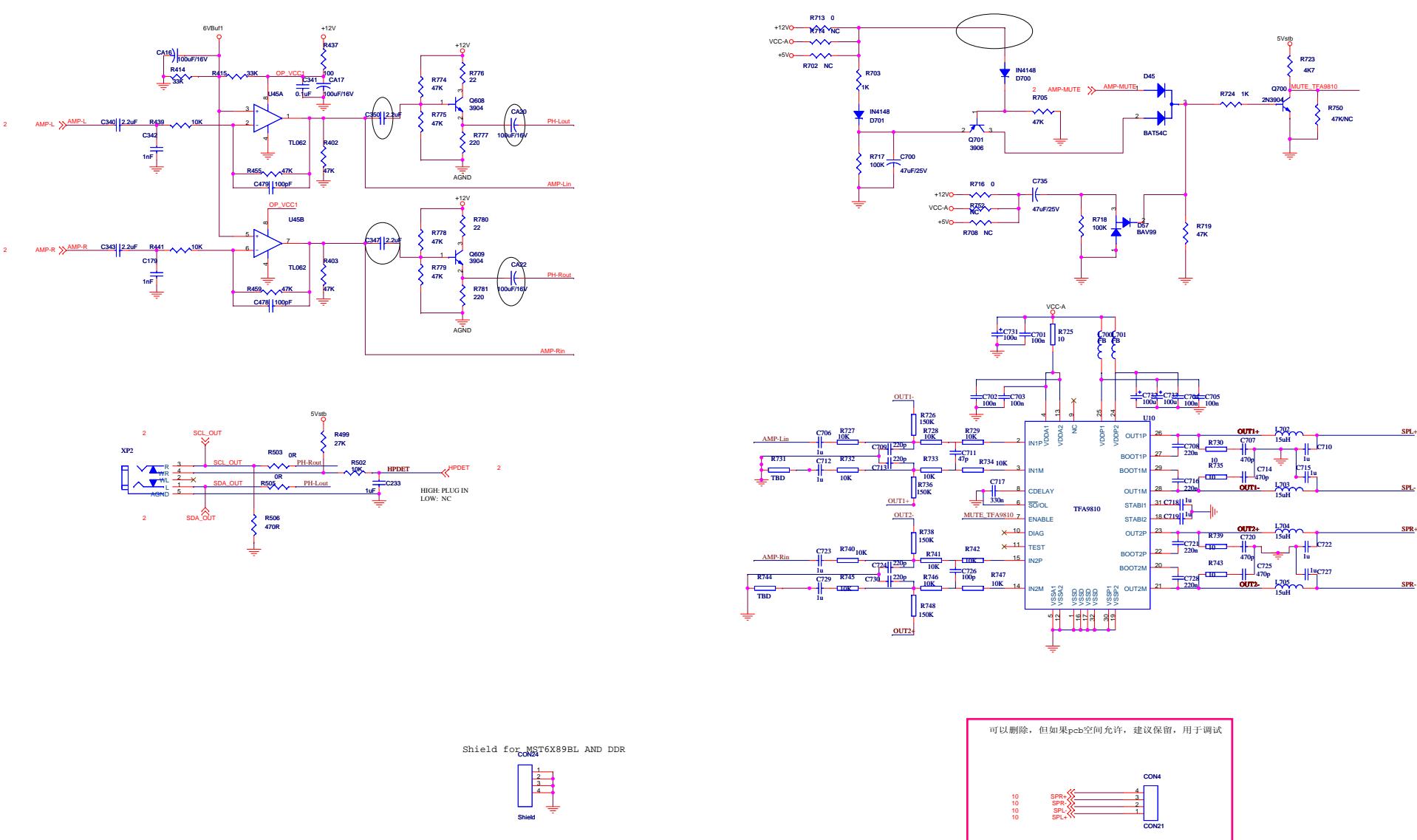
I2C ADDRESS (R263 NC)

READ is 0x87

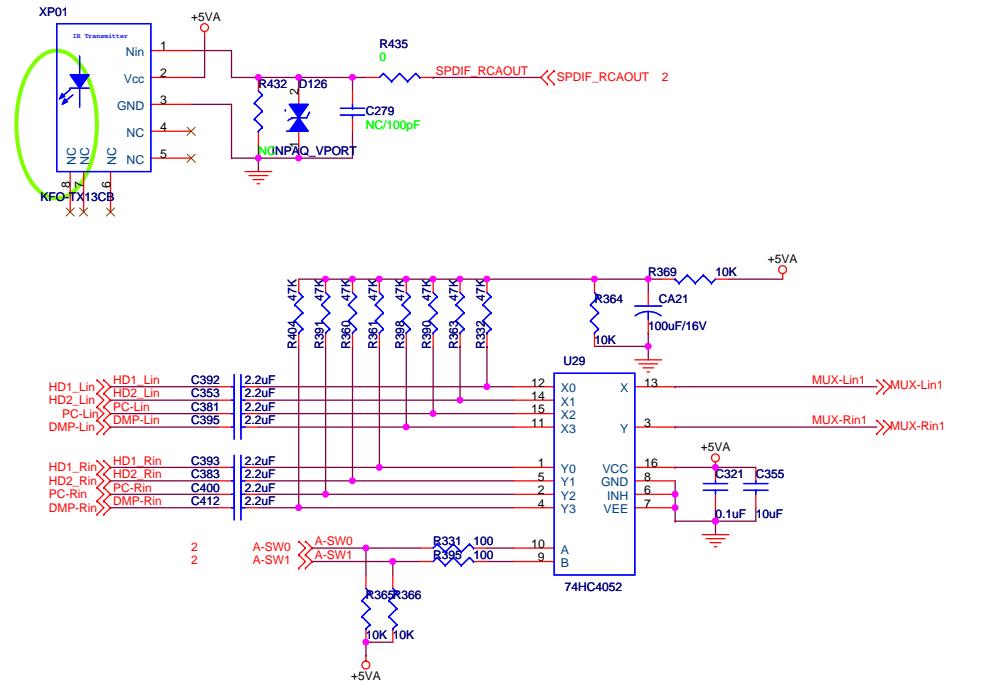
WRITE is 0x8



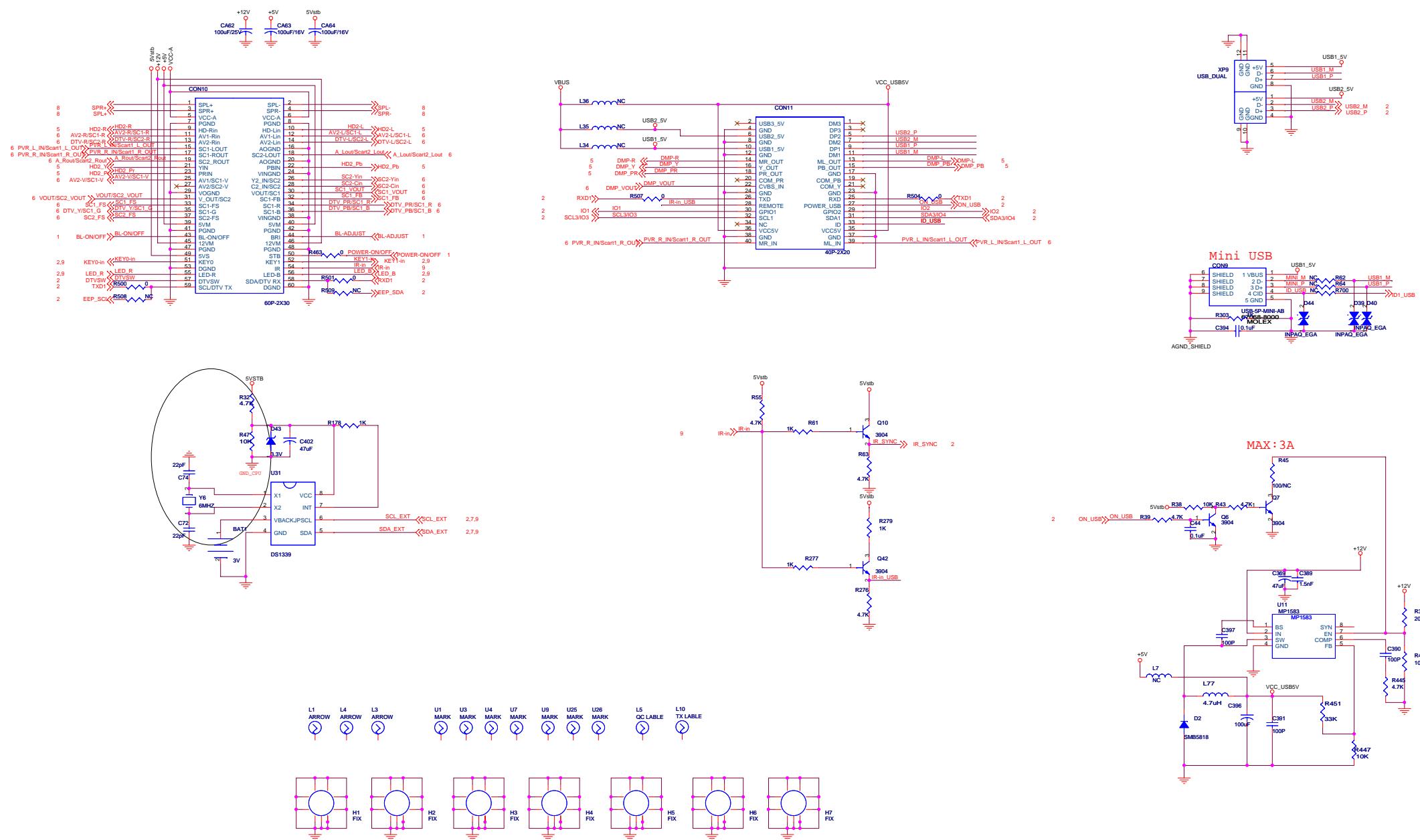
7、TV接收、中频放大、图像解调电路部分



8、伴音运放、耳机输出、伴音功放及开关机静音电路部分



9、高清、VGA、DMP伴音切换及光纤伴音输出电路部分



10、USB、Mini USB输入、IR输入接口电路部分