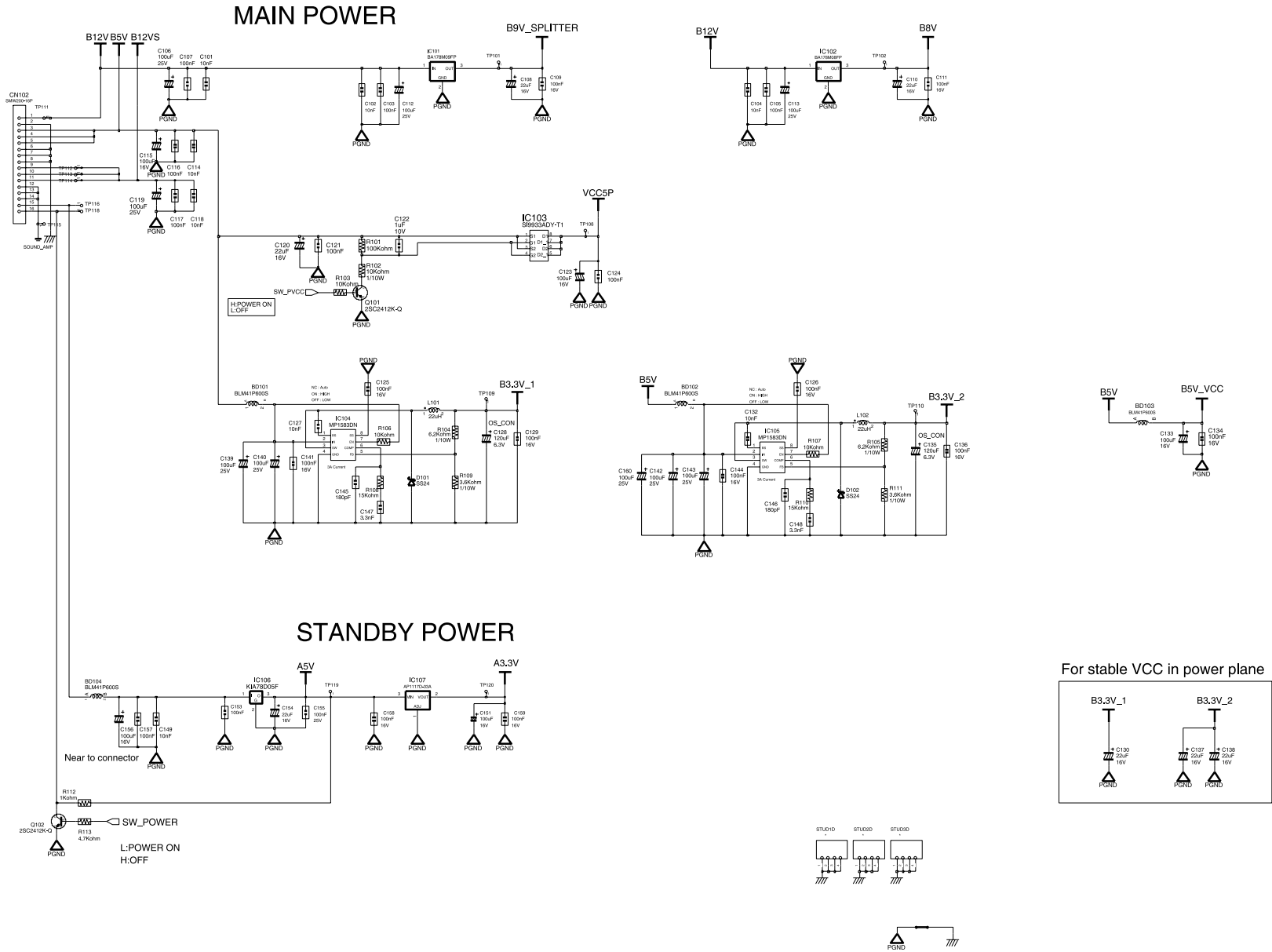


9 Schematic Diagrams

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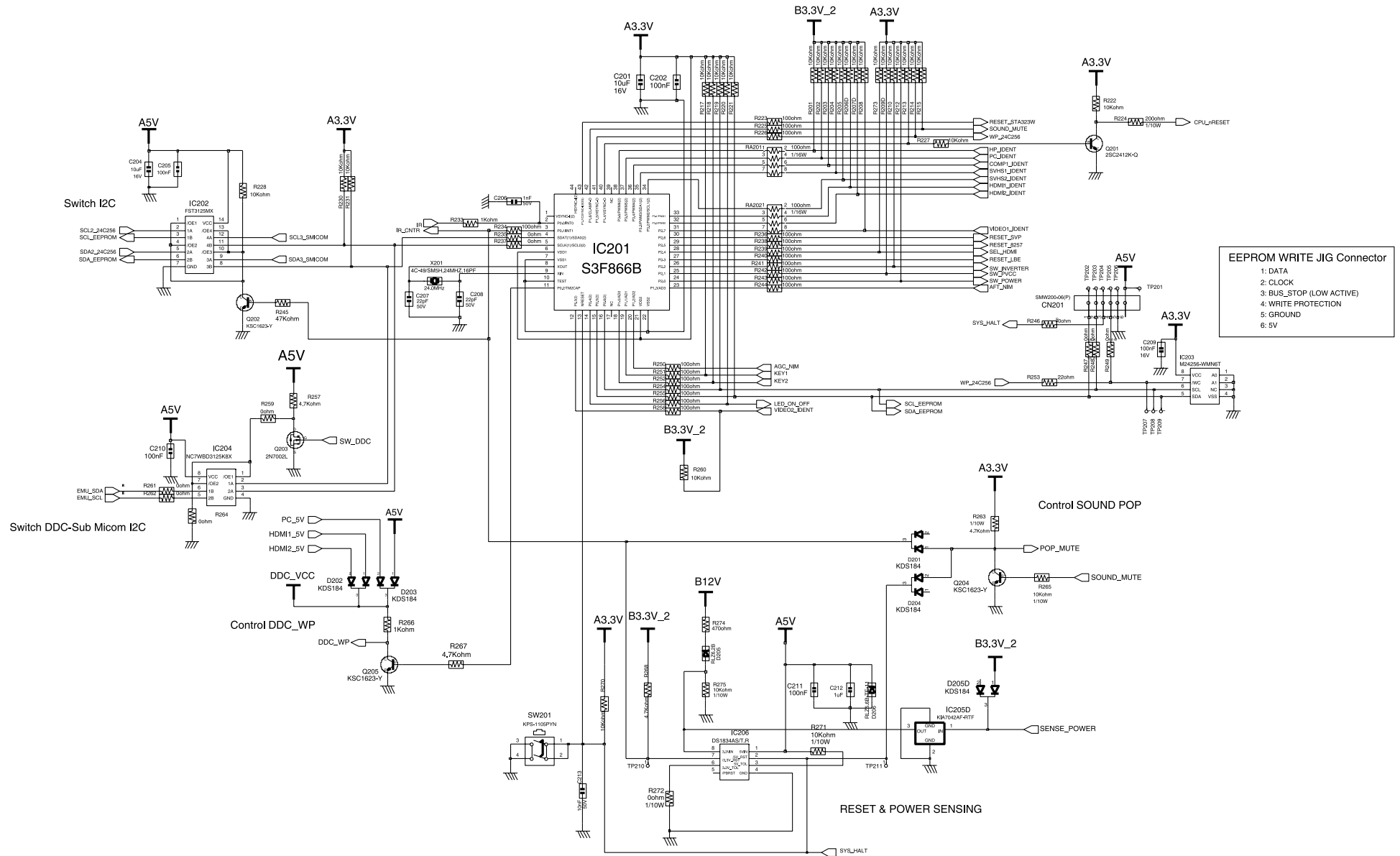
9-1 Power Schematic Diagram



9 Schematic Diagrams

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9-2 Stand by Microm Schematic Diagram

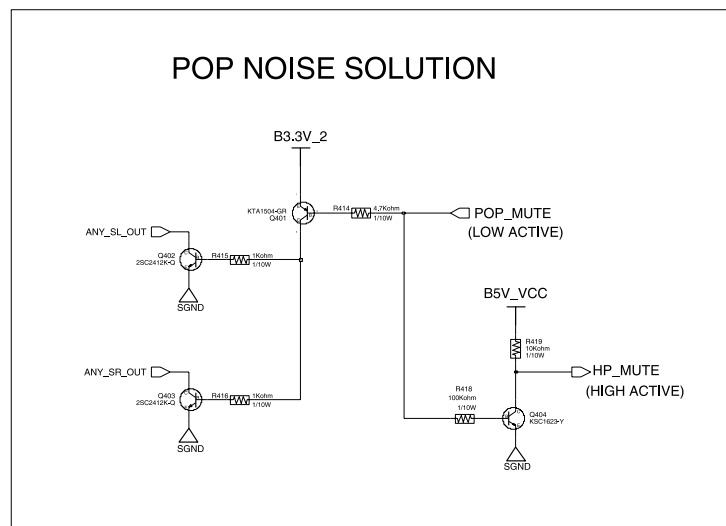
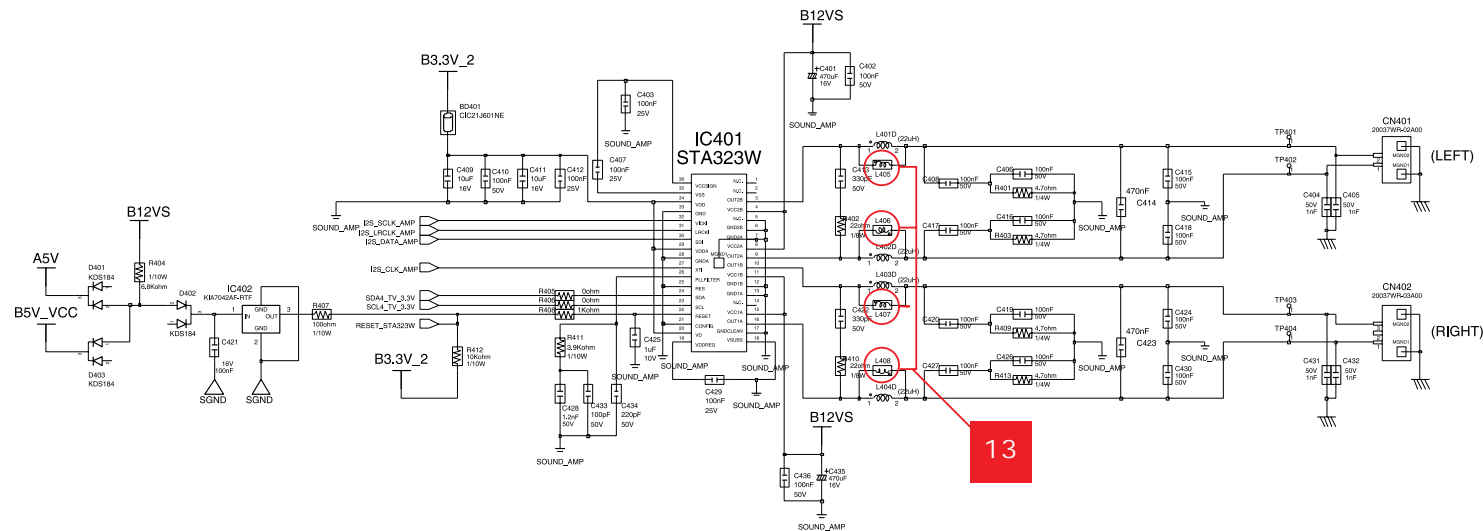


9-3 STV8257 Diagram



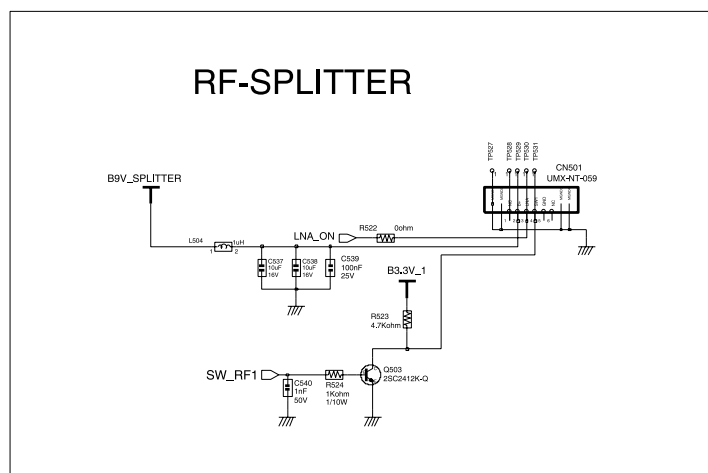
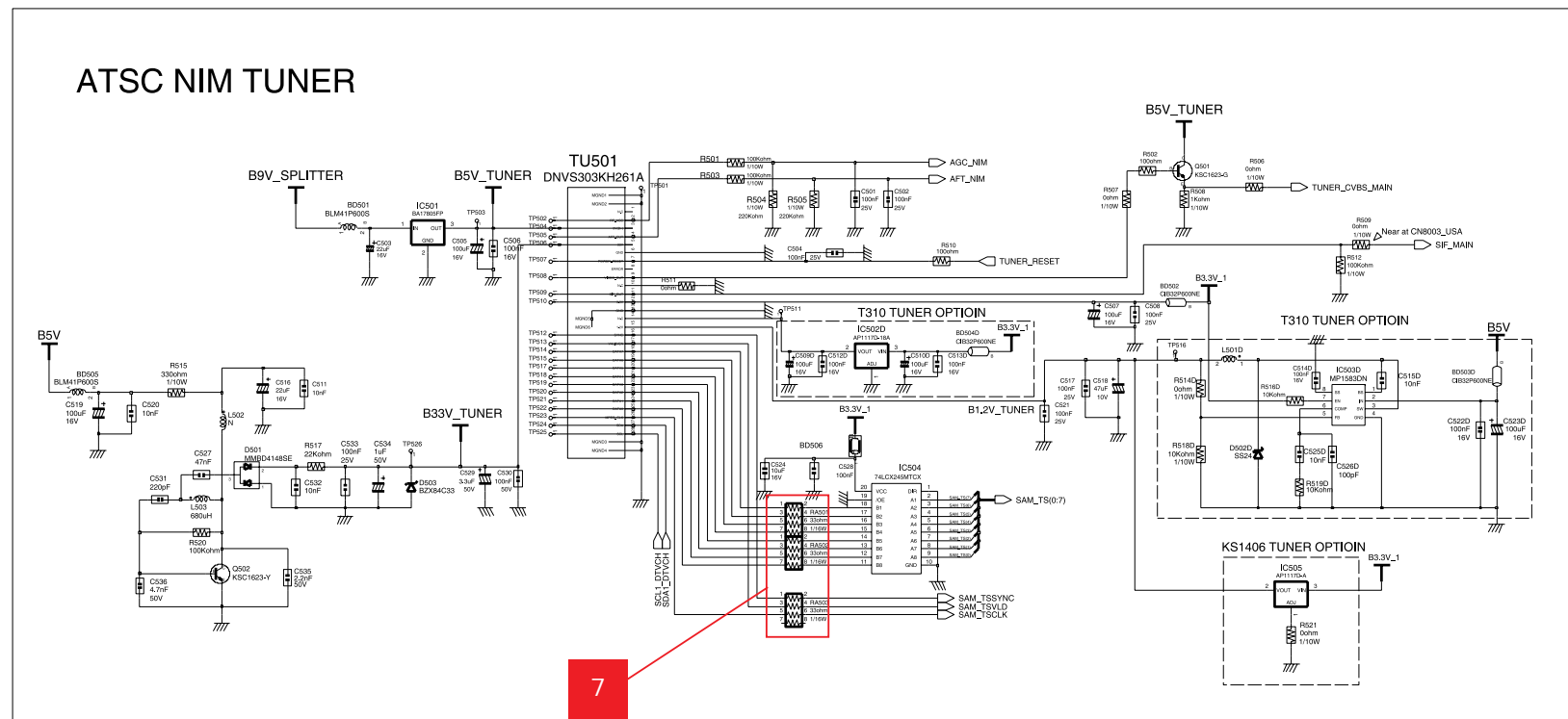
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9-4 STA323W Schematic Diagram



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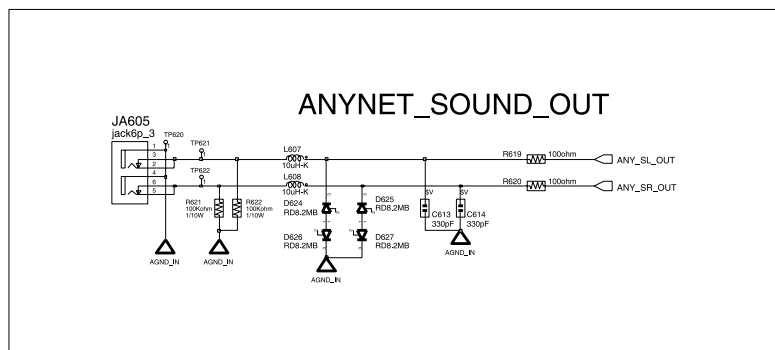
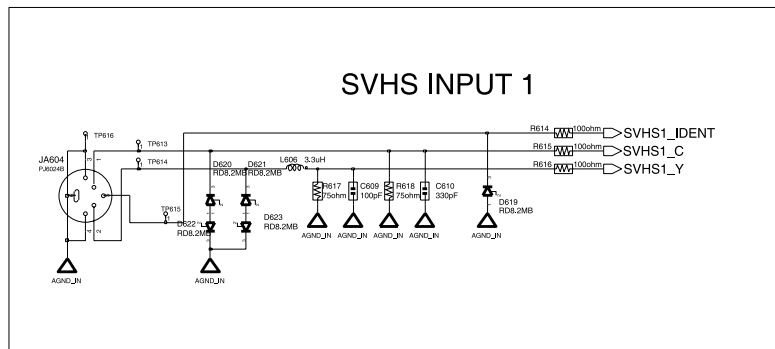
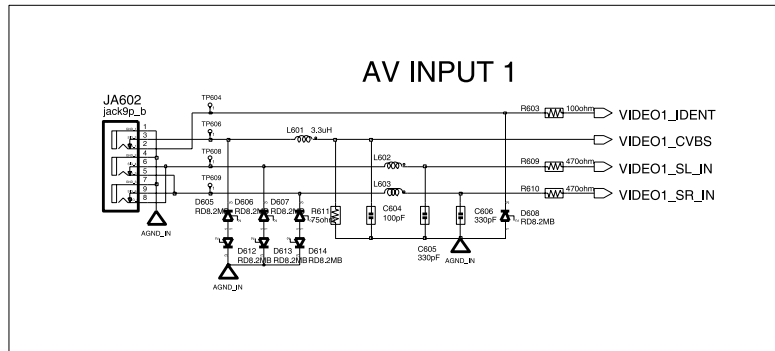
9-5 TUNER_SPLITTER Schmatic Diagram



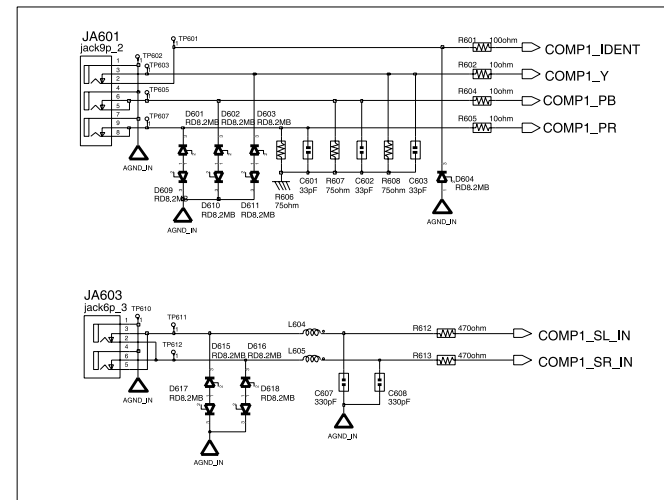
9 Schematic Diagrams

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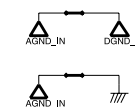
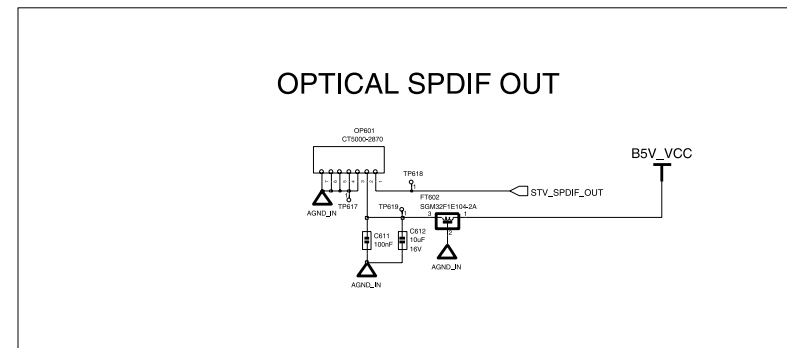
9-6 AV_IN_OUT_1 Schmatic Diagram



COMPONENT 1



OPTICAL SPDIF OUT



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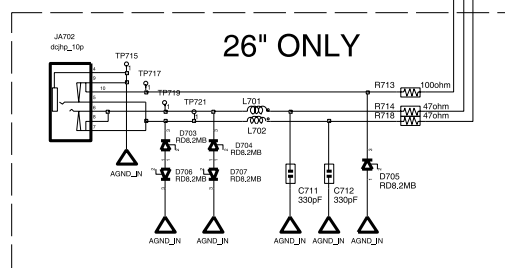
9-7 AV_IN_OUT_2 Schmatic Diagram

SIDE_AV

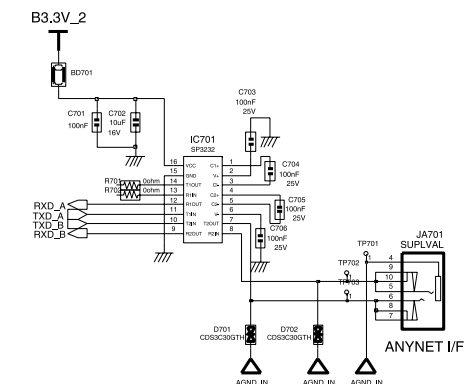
SIDE_SVIDEO INPUT

SIDE_AV INPUT

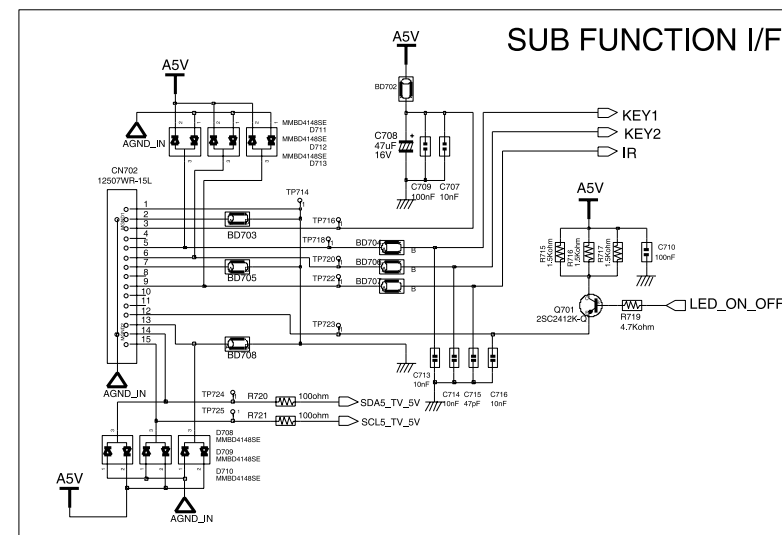
SIDE_HP INPUT



ANYNET & SERVICE I/F



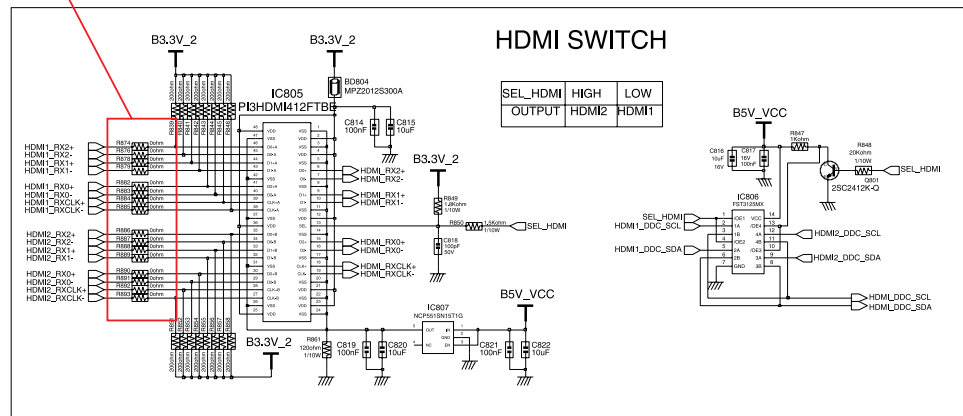
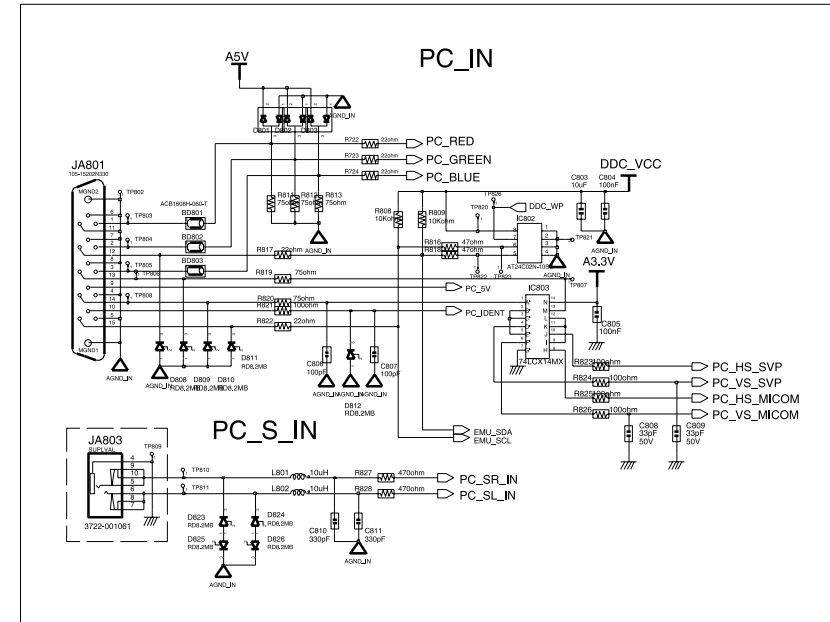
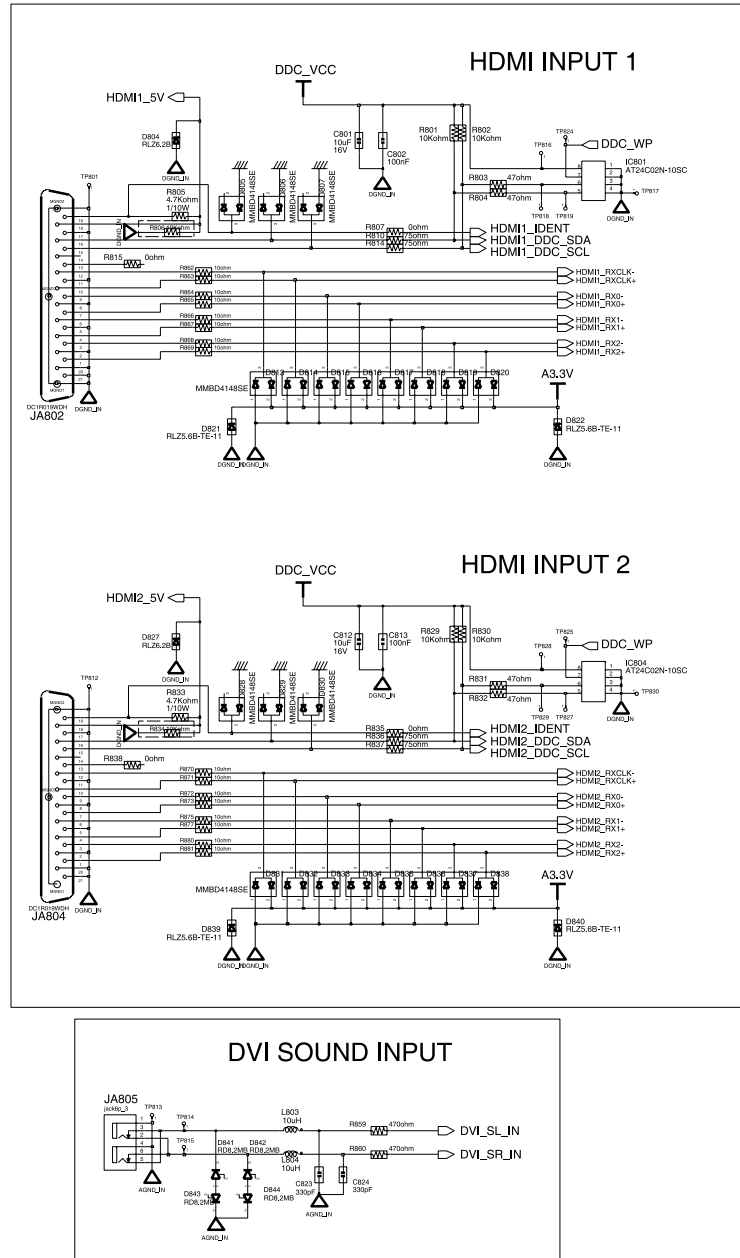
SUB FUNCTION I/F



9 Schematic Diagrams

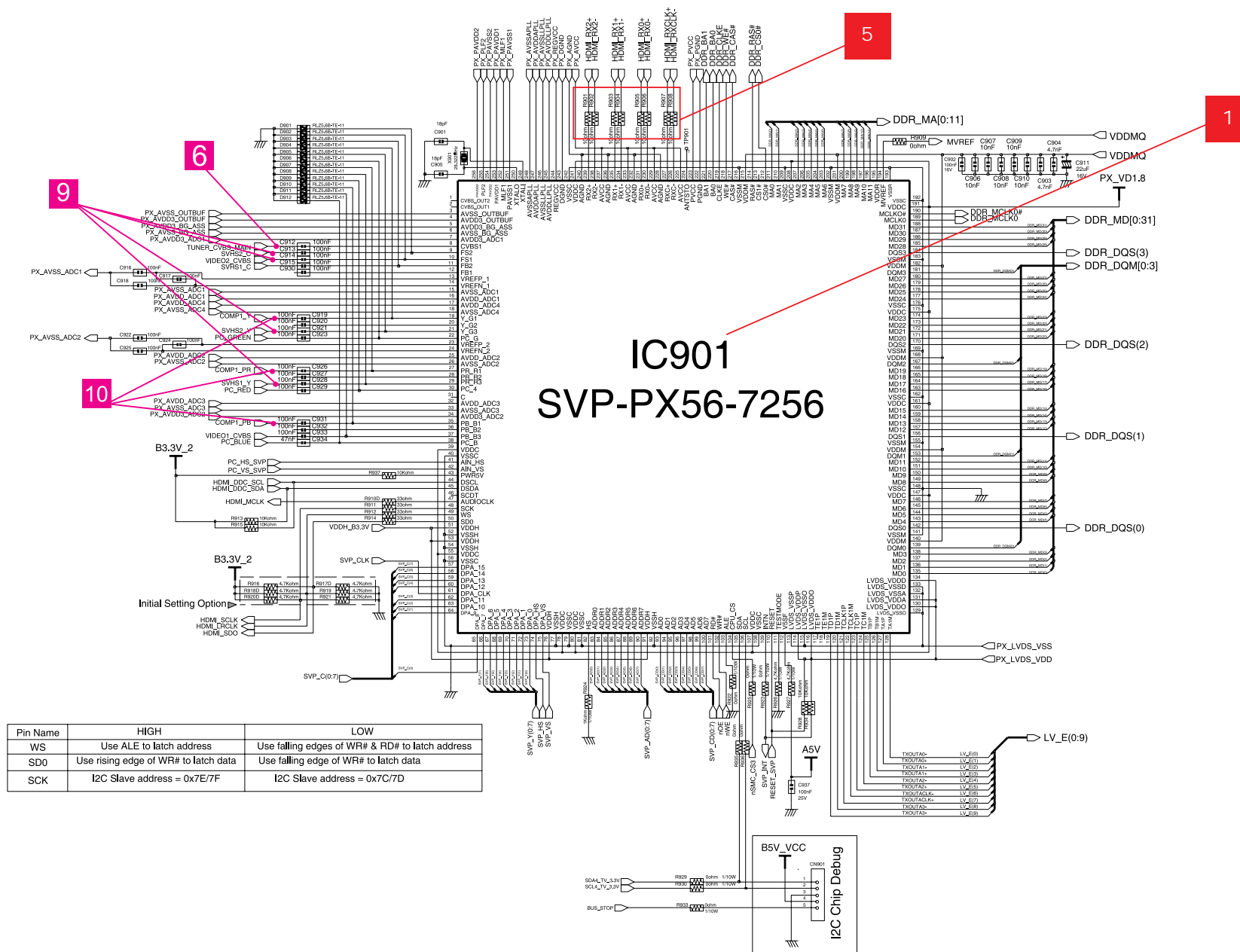
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9-8 PC_HDMI_INPUT Schmatic Diagram



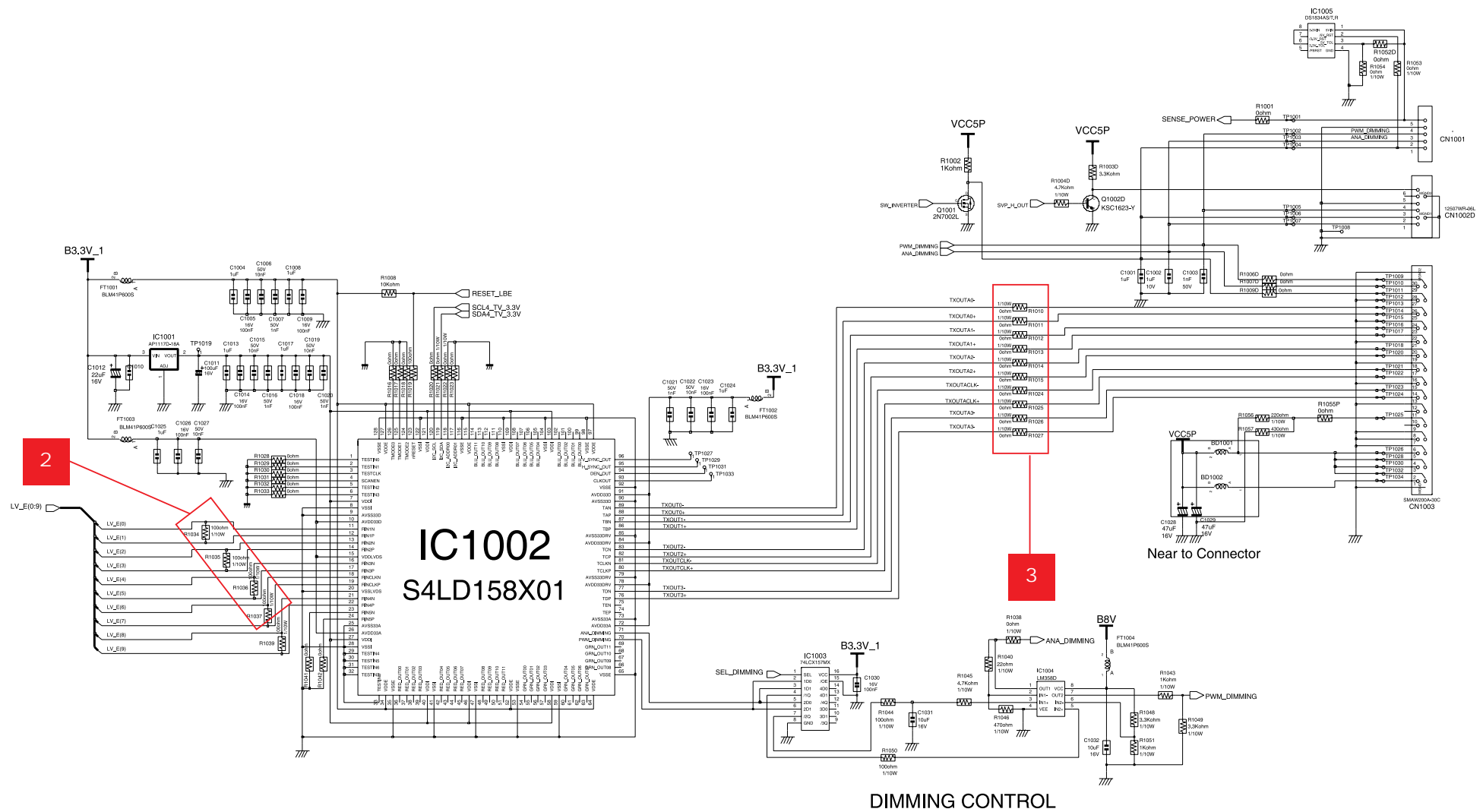
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9-9 SVP_PX Schmatic Diagram



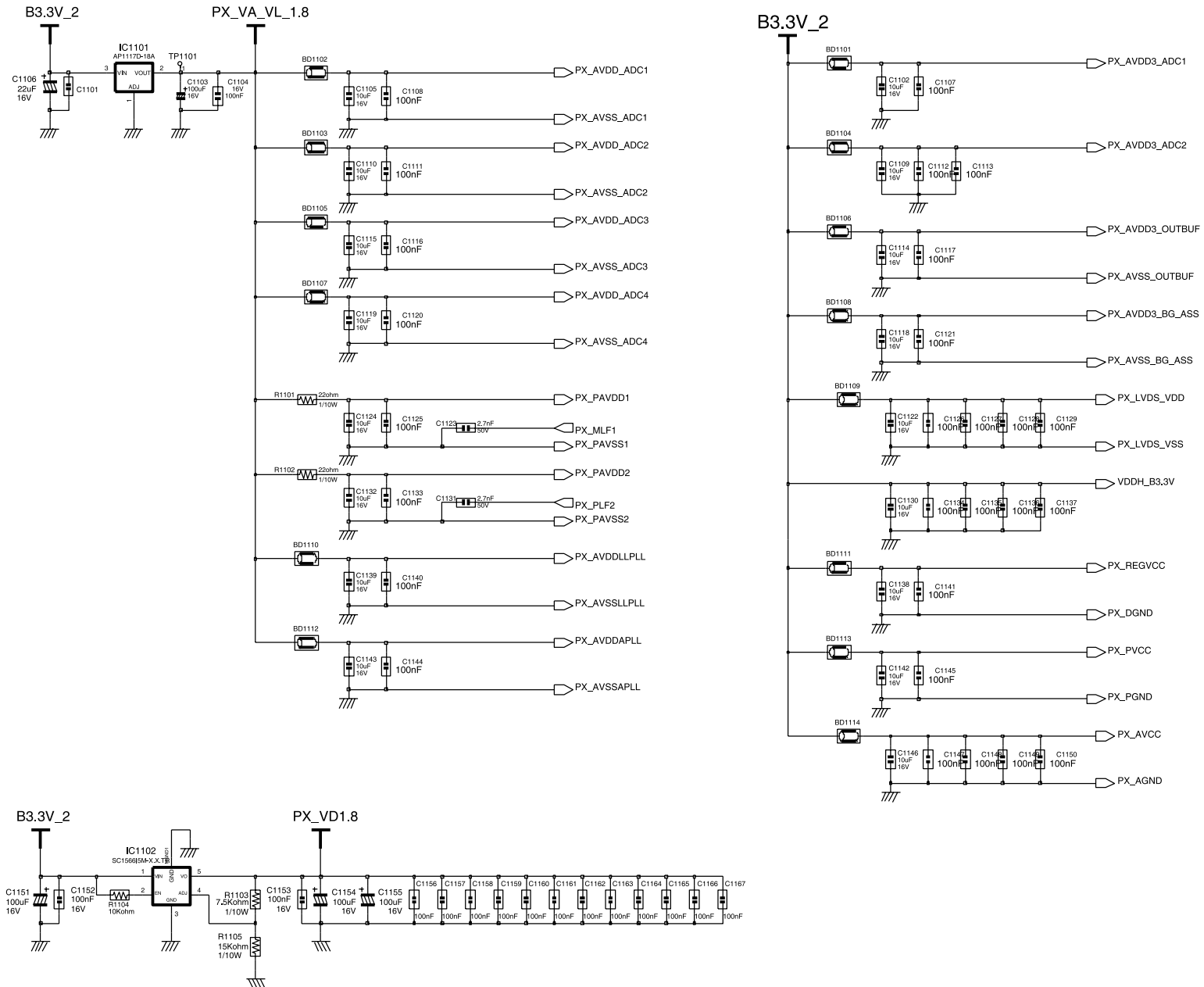
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9-10 LBE Schmatic Diagram



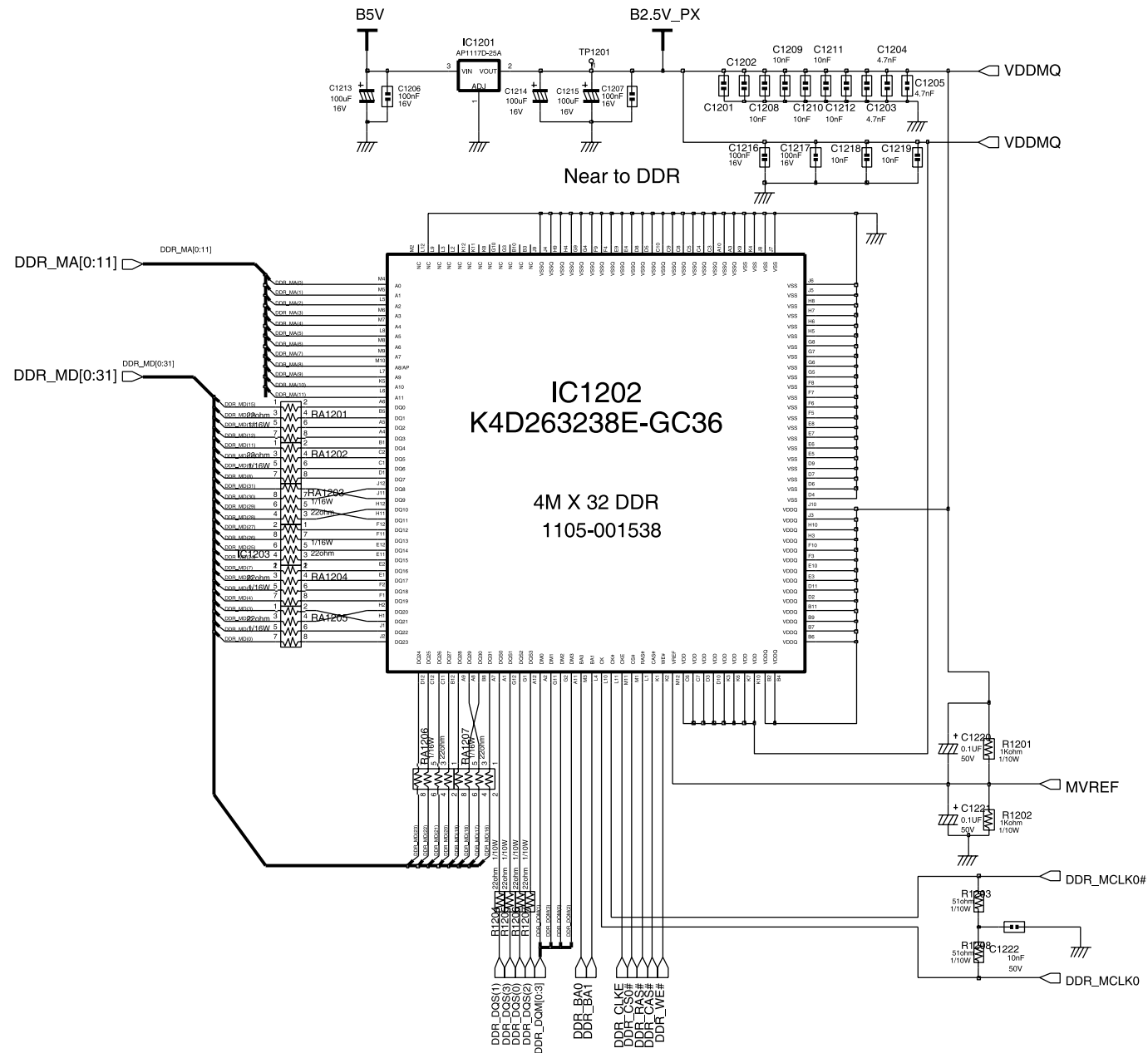
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9-11 SVP_PX_POWER Schmatic Diagram



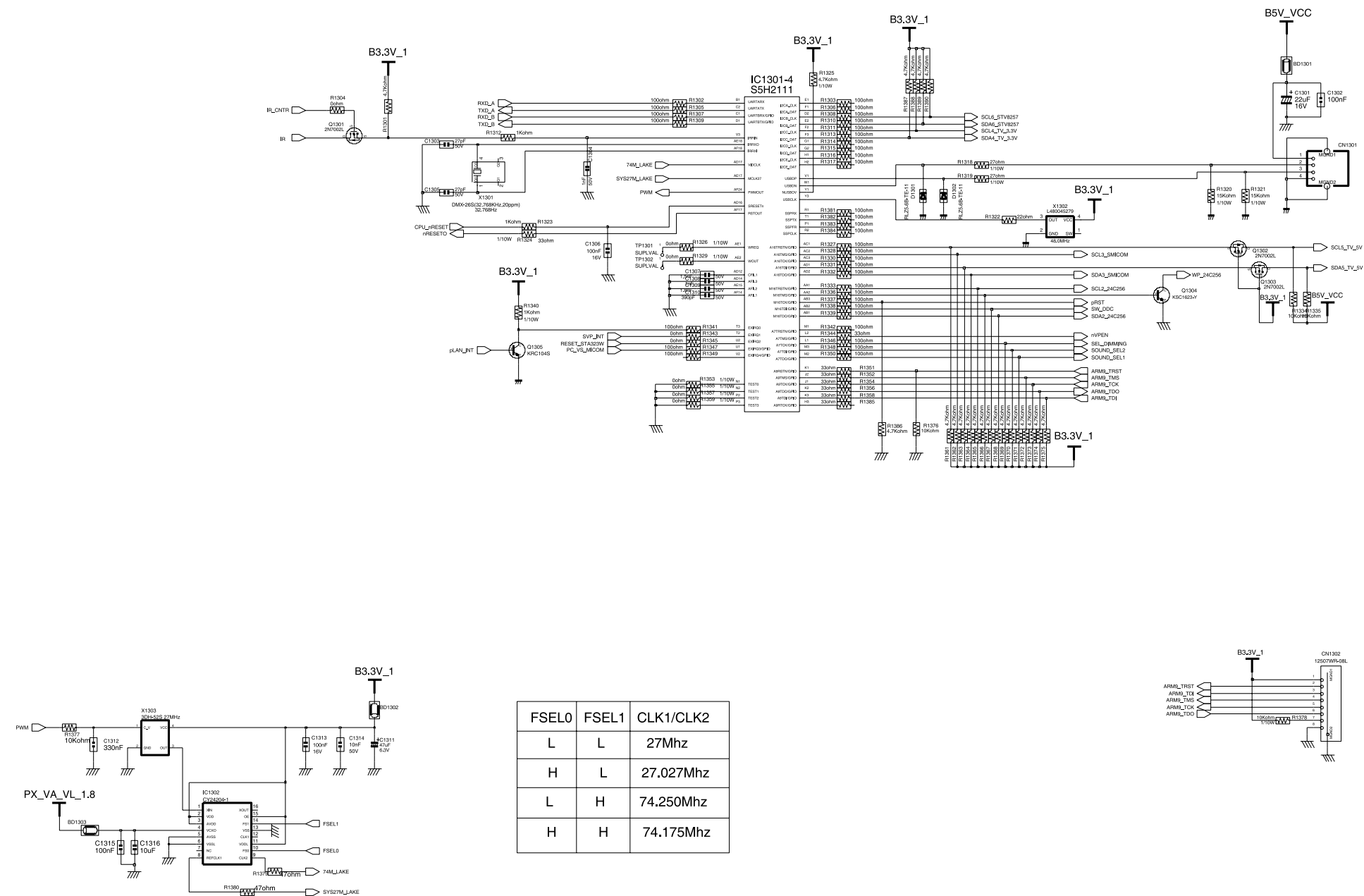
- This Document can not be used without Samsung's authorization.

9-12 SVP_PX_DDR Schmetic Diagram

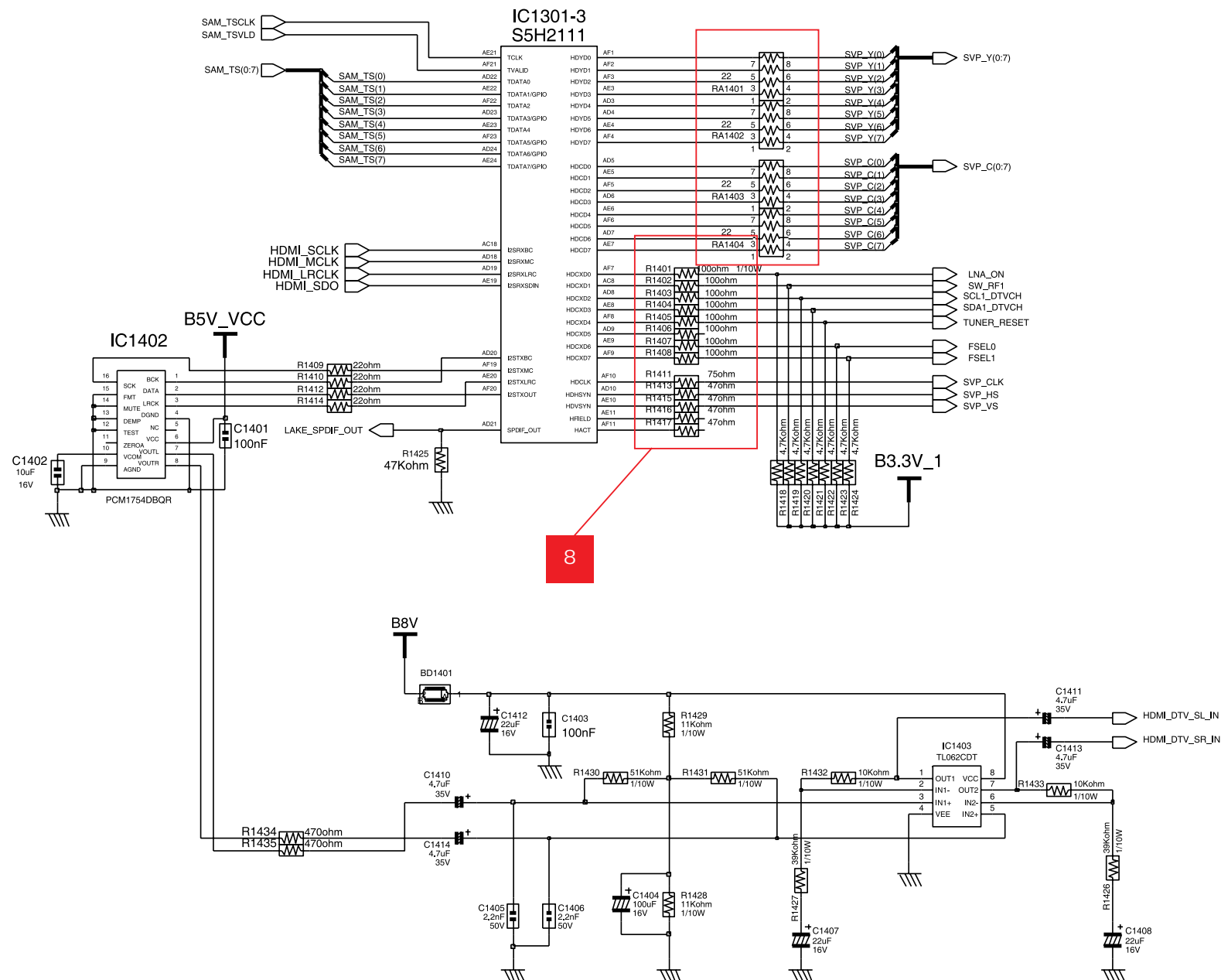


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9-13 LAKE_I2C_GPIO_USB_UART_ETC Schmetic Diagram

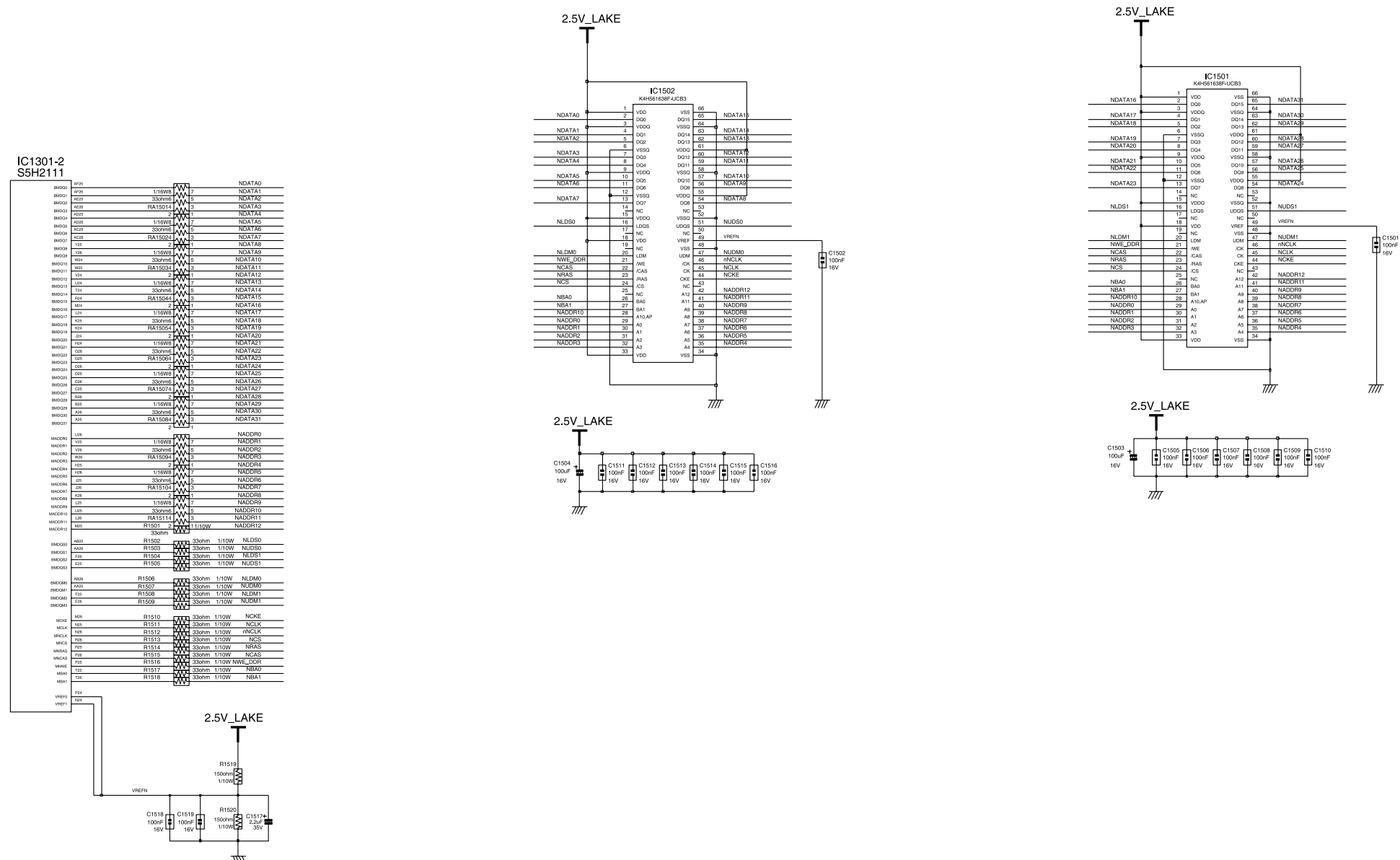


9-14



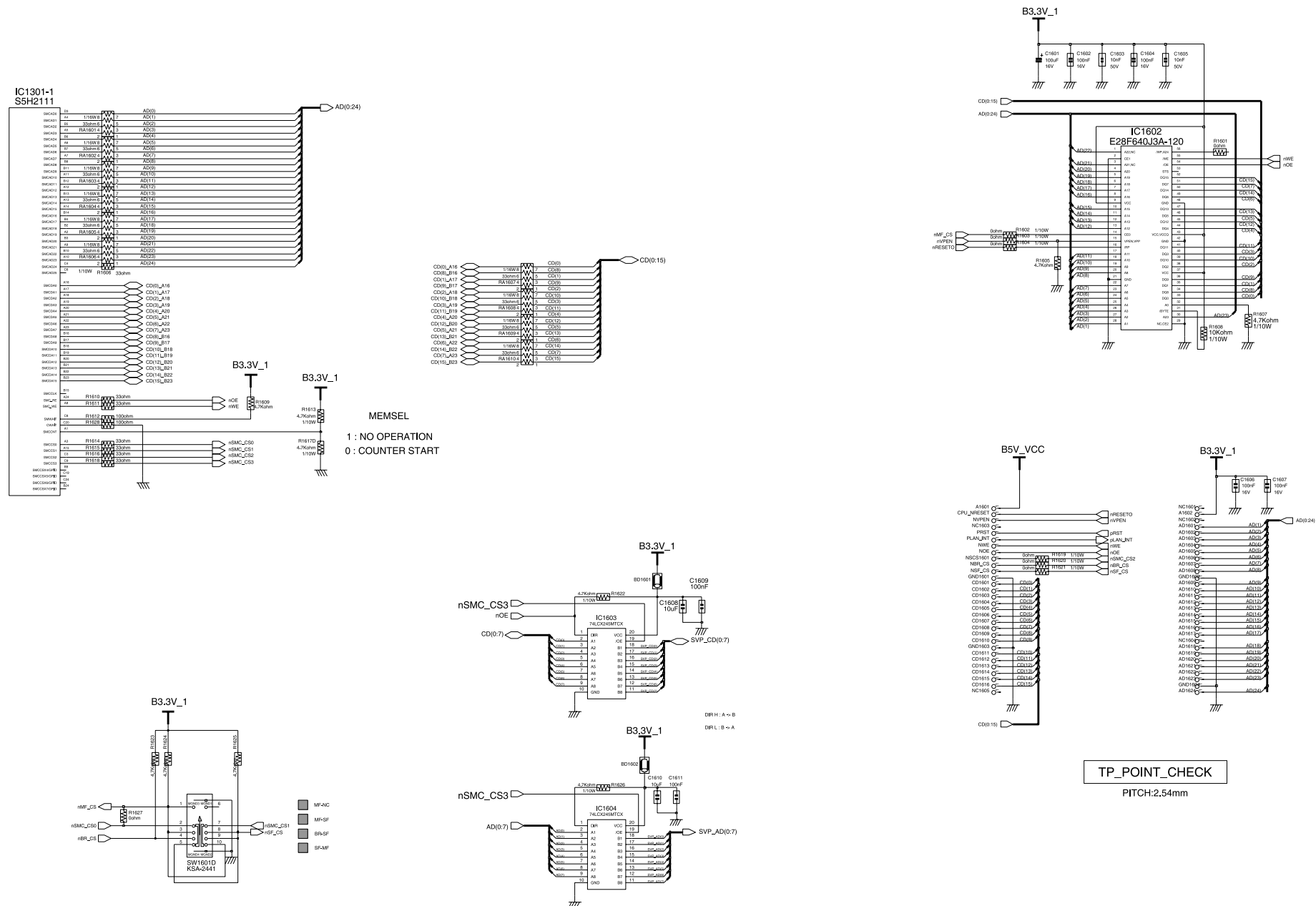
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9-15 LAKE_DDR Schmetic Diagram



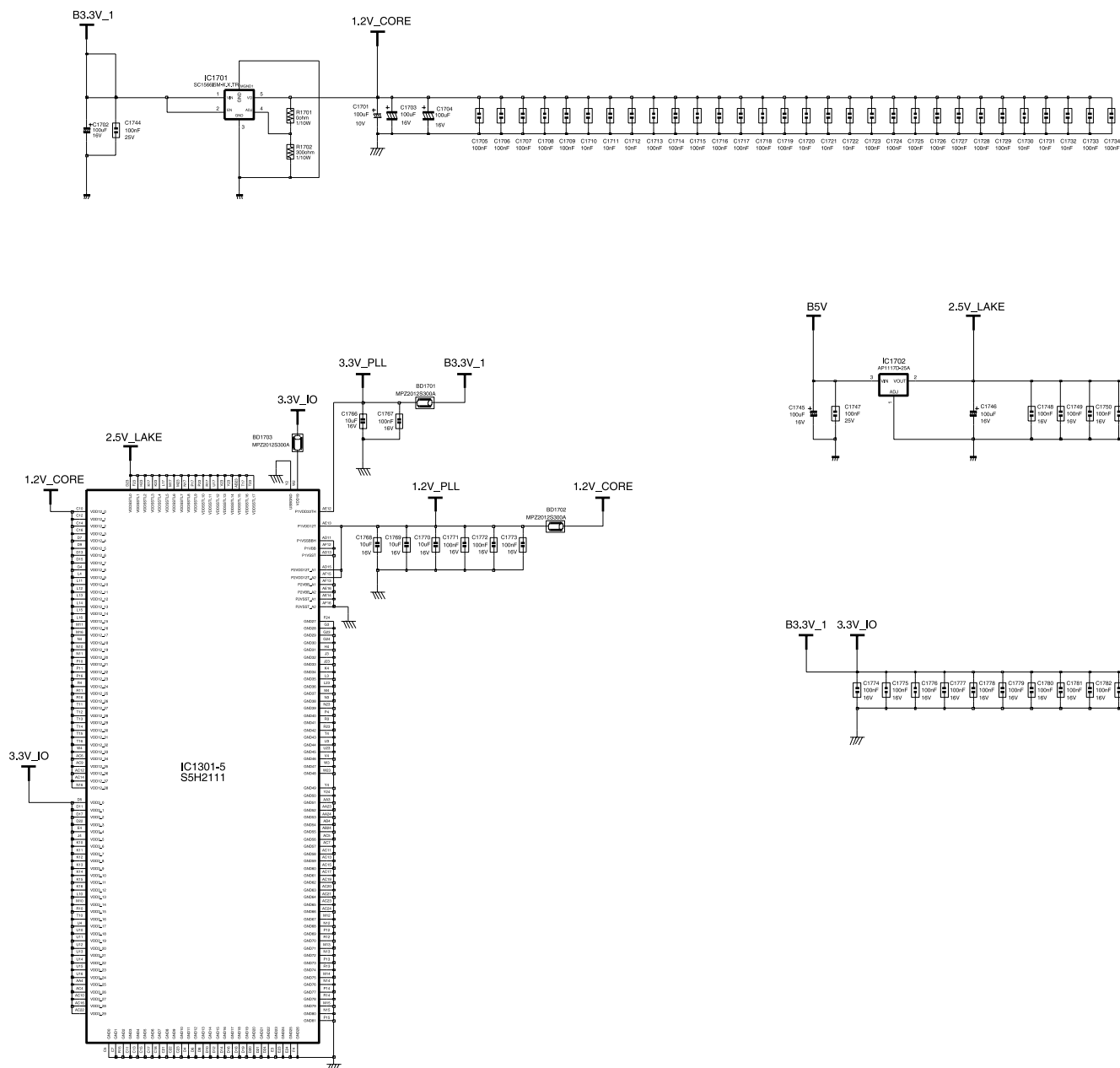
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9-16 LAKE_FLASH_DEBUG Schmetic Diagram



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9-17 LAKE_POWER Schmetic Diagram



Memo